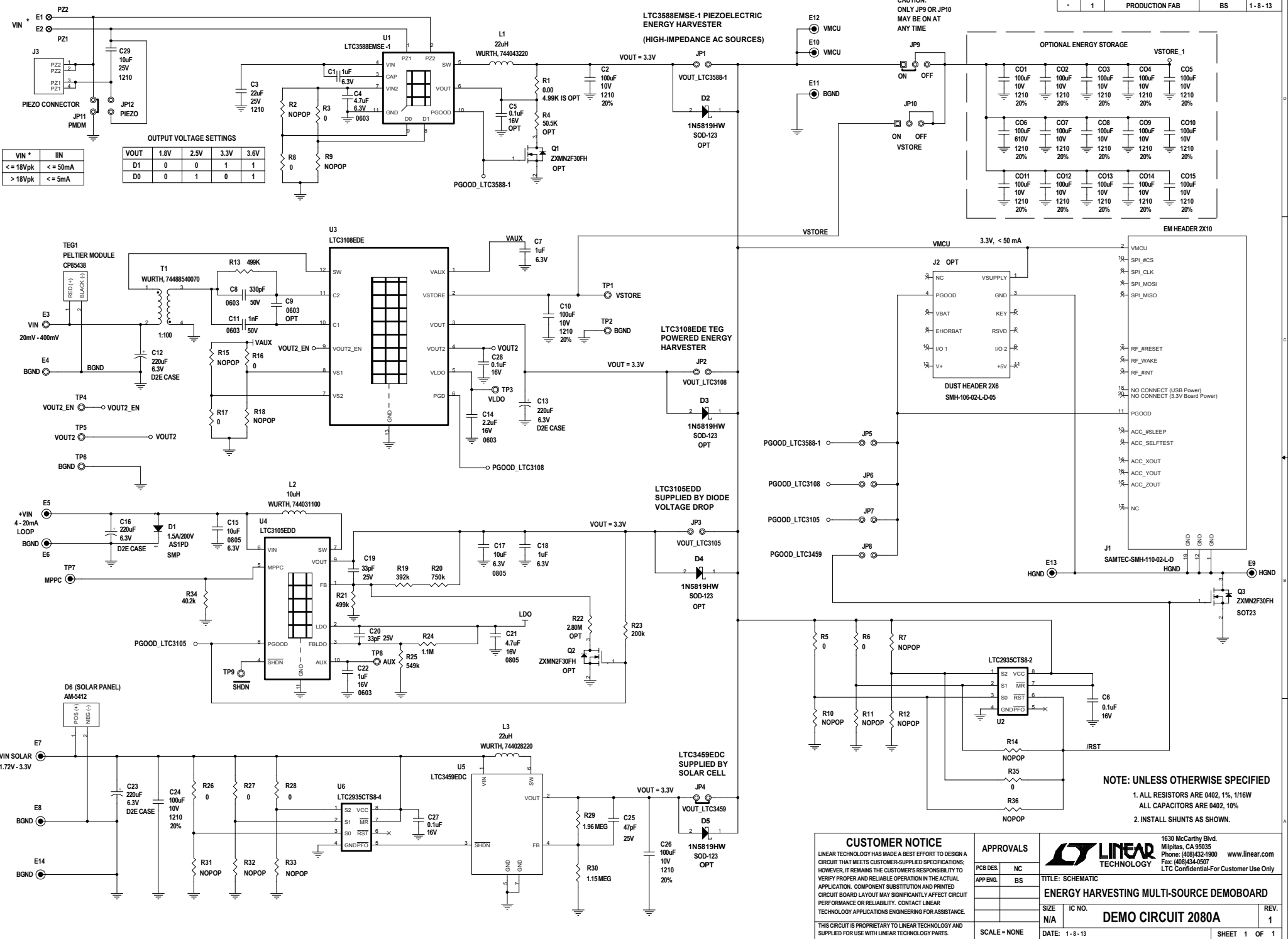


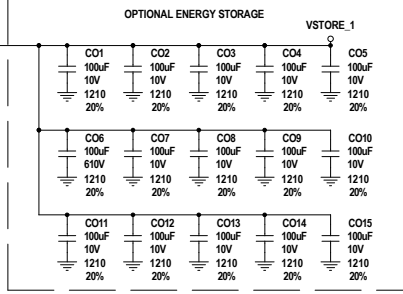
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION FAB	BS	1-8-13



VIN *	IIN
<= 18Vpk	<= 50mA
> 18Vpk	<= 5mA

OUTPUT VOLTAGE SETTINGS				
VOUT	1.8V	2.5V	3.3V	3.6V
D1	0	0	1	1
D0	0	1	0	1

CAUTION:  
ONLY JP9 OR JP10  
MAY BE ON AT  
ANY TIME



NOTE: UNLESS OTHERWISE SPECIFIED  
1. ALL RESISTORS ARE 0402, 1%, 1/16W  
ALL CAPACITORS ARE 0402, 10%

**CUSTOMER NOTICE**  
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.  
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	NC
APP ENG.	BS

**LINEAR TECHNOLOGY**  
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TITLE: SCHEMATIC  
**ENERGY HARVESTING MULTI-SOURCE DEMOBOARD**

SIZE: N/A  
IC NO.: **DEMO CIRCUIT 2080A**

DATE: 1-8-13

REVISION: 1  
SHEET 1 OF 1