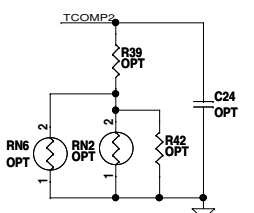
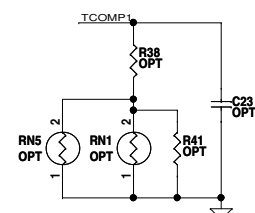
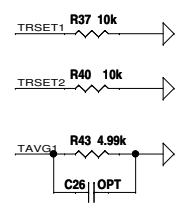
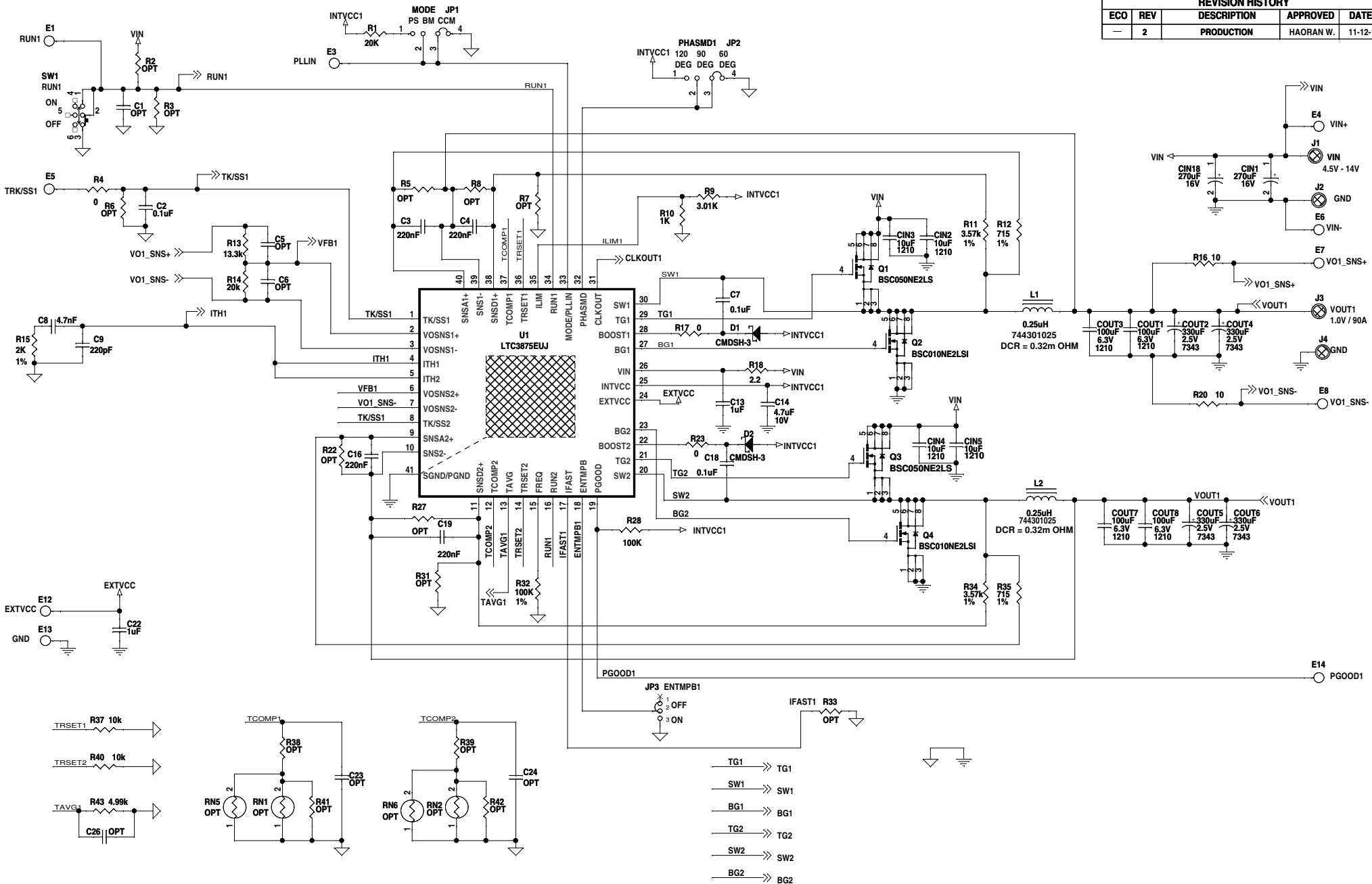


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	HAORAN W.	11-12-13



CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

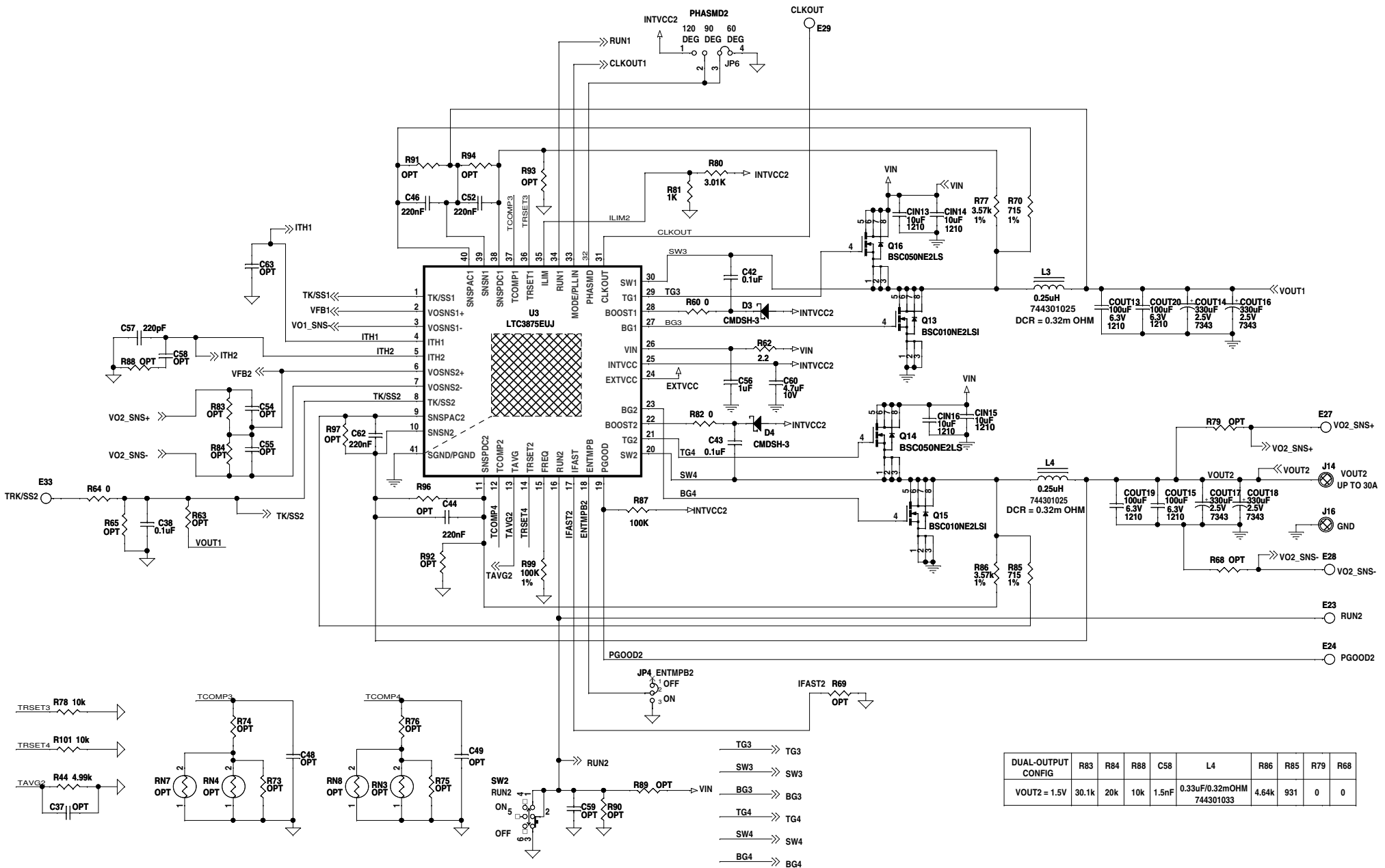
APPROVALS	
PCB DES.	HZ
APP ENG.	HAORAN W.
SCALE	NONE

LINEAR TECHNOLOGY

1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900 www.linear.com
 Fax: (408)434-0507
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TITLE: SCHEMATIC
HIGH EFFICIENCY HIGH CURRENT POLYPHASE SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DCR INDUCTOR

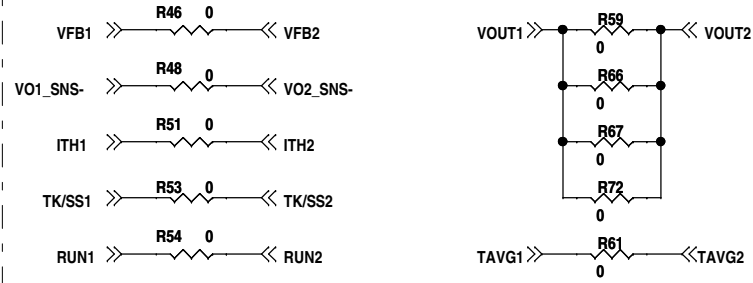
SIZE B	IC NO. LTC3875EUJ	REV. 2
DATE: Tuesday, April 15, 2014		SHEET 1 OF 3



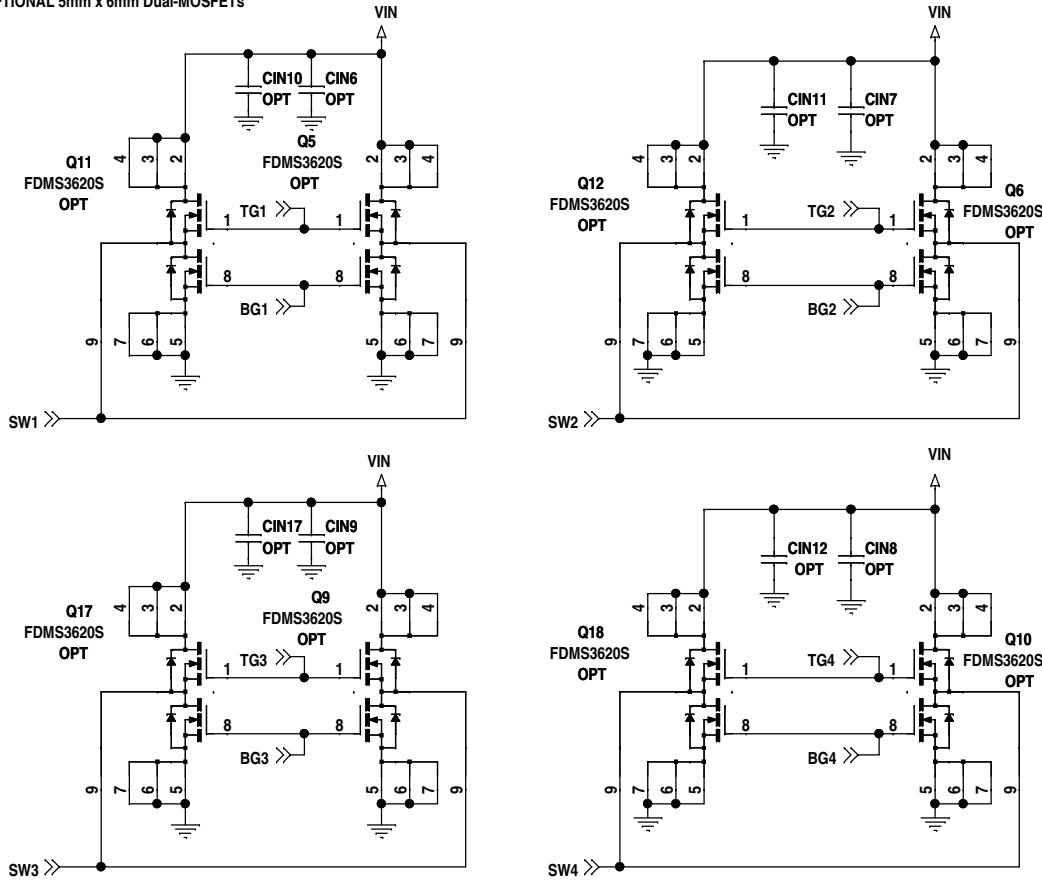
DUAL-OUTPUT CONFIG	R83	R84	R88	C58	L4	R86	R85	R79	R68
VOUT2 = 1.5V	30.1k	20k	10k	1.5nF	0.33uF/0.32mOHM 744301033	4.64k	931	0	0

<p>CUSTOMER NOTICE</p> <p>LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.</p> <p>THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.</p>		<p>APPROVALS</p> <p>PCB DES. HZ</p> <p>APP ENG. HAORAN W</p>		<p>LINEAR TECHNOLOGY</p> <p>1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only</p>	
<p>TITLE: SCHEMATIC</p> <p>HIGH EFFICIENCY HIGH CURRENT POLYPHASE SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DCR INDUCTOR</p>				<p>REV. 2</p>	
<p>SCALE = NONE</p>		<p>DATE: Tuesday, April 15, 2014</p>		<p>SHEET 2 OF 3</p>	

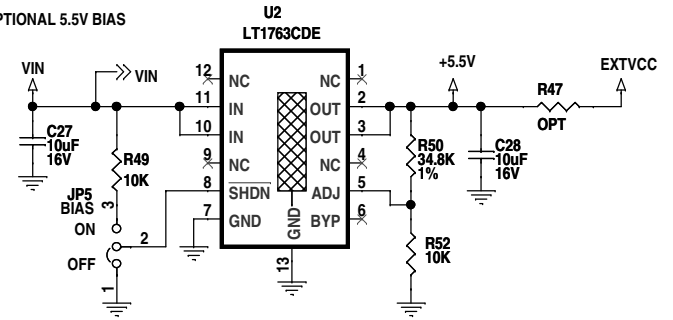
REMOVE ZERO OHM JUMPERS FOR DUAL-OUTPUT OPERATION



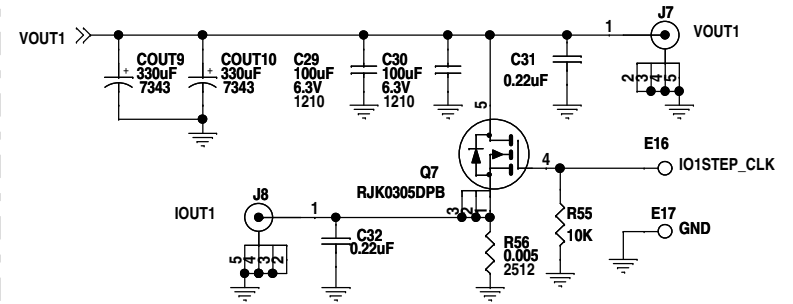
OPTIONAL 5mm x 6mm Dual-MOSFETs



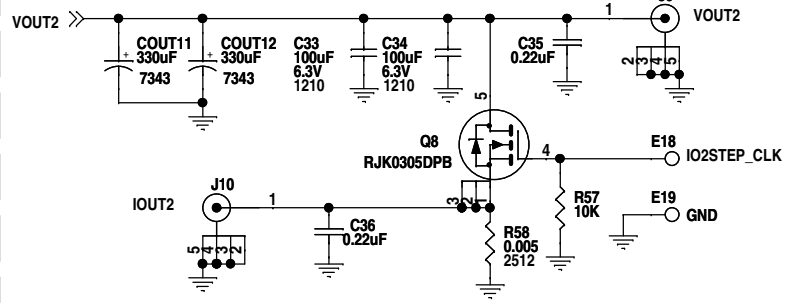
OPTIONAL 5.5V BIAS



LOAD STEP CIRCUIT 1



LOAD STEP CIRCUIT 2



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APPROVALS

PCB DES.	HZ
APP ENG.	HAORAN W.

SCALE = NONE



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HIGH EFFICIENCY HIGH CURRENT POLYPHASE SYNCHRONOUS
BUCK CONVERTER WITH VERY LOW DCR INDUCTOR

SIZE IC NO. **LTC3875EUJ** REV. **2**
B **DEMO CIRCUIT 2055A**

DATE: Tuesday, April 15, 2014

SHEET 3 OF 3