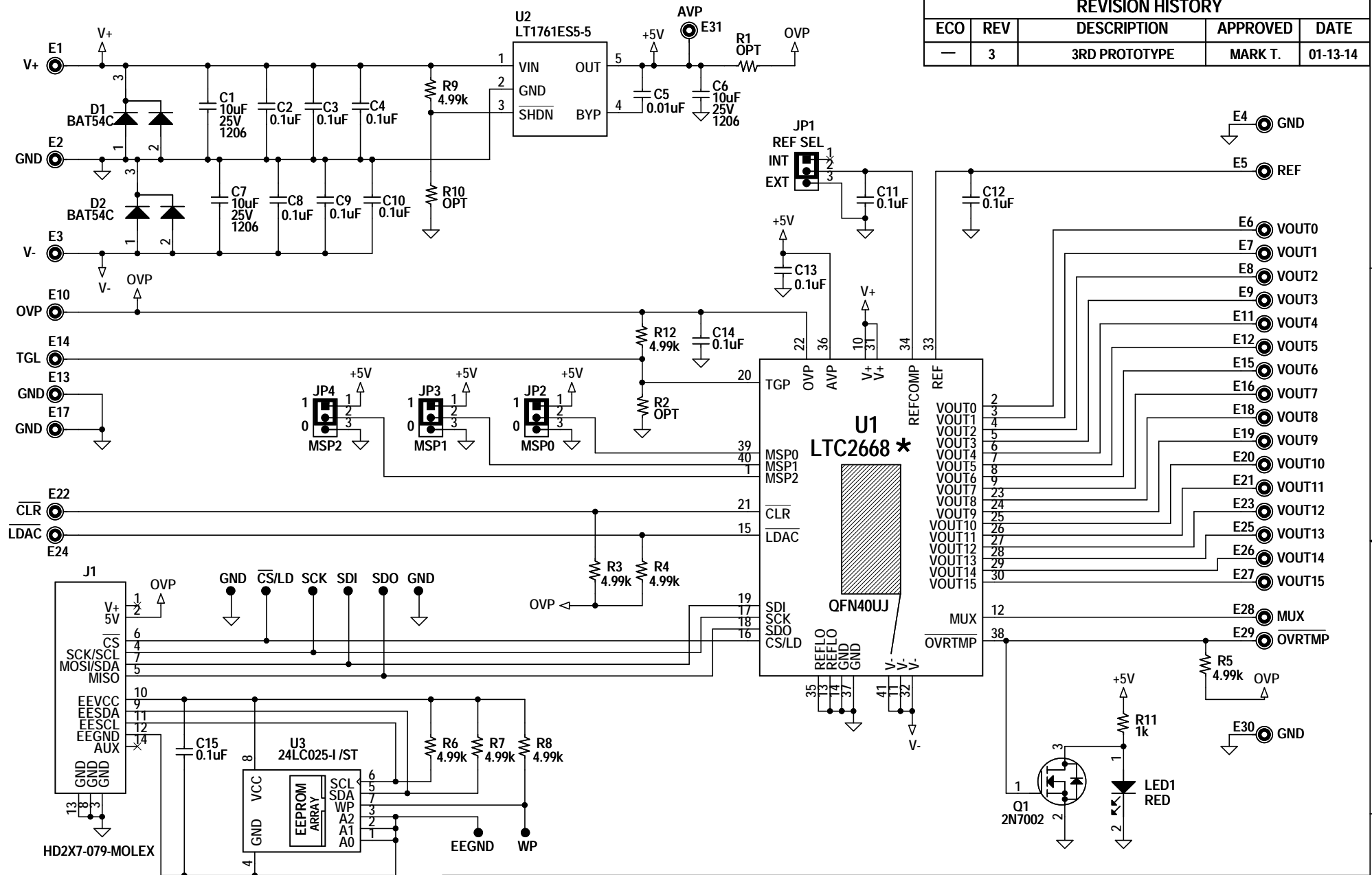


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	3RD PROTOTYPE	MARK T.	01-13-14



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL CAPACITORS ARE IN MICROFARADS, 0603.

* ASSY	U1	SUFFIX	-Bit
A	LTC2668-16	16	16
B	LTC2668-12	12	12

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	MARK T.
SCALE = NONE	

1630 McCarthy Blvd.  
 Milpitas, CA 95035  
 Phone: (408)432-1900 www.linear.com  
 Fax: (408)434-0507  
 LTC Confidential-For Customer Use Only

**TITLE: SCHEMATIC**

**16-CHANNEL, 16/12-BIT SPI +/-10V  
 VOUT DAC WITH 10ppm REFERENCE**

SIZE N/A	IC NO. LTC2668CUJ - 16 / 12	REV. 3
DATE: 01/13/2014, 12:06 PM		SHEET 1 OF 1