



NOTES:

[1] CIN IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC2005A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

[2] FOR FCM OPERATION, STUFF R9 AND R10 WITH 1MEG OHM RESISTOR.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	MI
APP ENG.	GINA LE

SCALE = NONE



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TITLE: SCHEMATIC
MONOLITHIC SYNCHRONOUS BUCK REGULATOR
WITH ULTRA-LOW QUIESCENT CURRENT

SIZE	IC NO.	REV.
N/A	LTC3624EDD DEMO CIRCUIT 2005A	2

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