DC590 SPI INTERFACE

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

NOTE: EEPROM FOR BOARD IDENTIFICATION

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**SCH**

**TITLE:** LOW PHASE NOISE, TRIPLE OUTPUT CLOCK DISTRIBUTION DIVIDER/DRIVER

**IC NO:** LTC6954IUFF-1/-2/-3/-4

**DATE:** 10/03/2013, 10:30 AM

**PAGE:** 1 of 2

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**REVISION:** 2

**DISTRIBUTION DIVIDER/DRIVER**

**APP ENG:** MICHEL A.

**TITLE:** SCHEMATIC

**SCALE:** NONE

**PROB DES:** KMT.

**APPROVALS:**

**FAX:** (408)434-0507

**PHONE:** (408)432-1900

**LOC:** 1630 McCarthy Blvd. Milpitas, CA 95035

**WEB:** www.linear.com

**NOTE:** EEPROM FOR BOARD IDENTIFICATION