

REVISION HISTORY

ECO	REV	DESCRIPTION	DATE	APPROVED
	A2	INITIAL RELEASE	21JUL08	

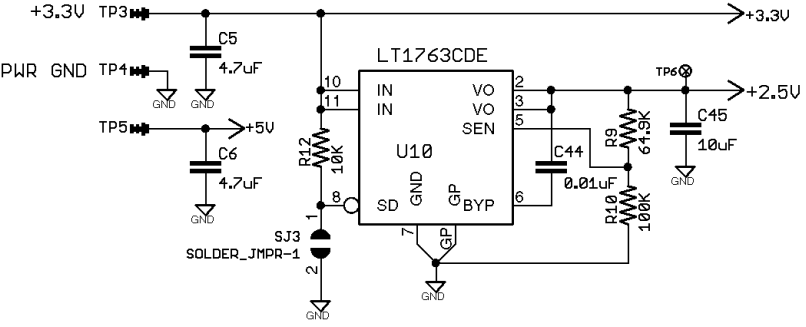
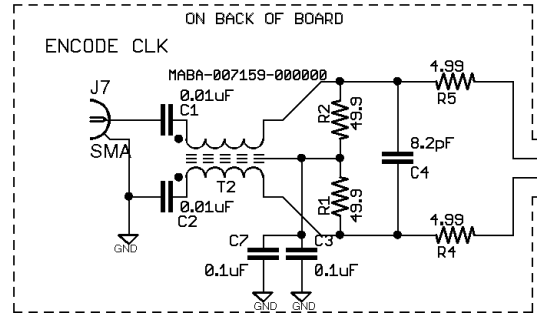
C39:  
For 600 MHz: install 5.6pF  
For 2 GHz: install 0.5pF  
For 2.3, 2.5, 3.5 GHz: install 0.5pF

L5:  
For 600 MHz: install 6.8nH  
For 2 GHz: install 2.7nH  
For 2.3, 2.5, 3.5 GHz: install 1.0nH

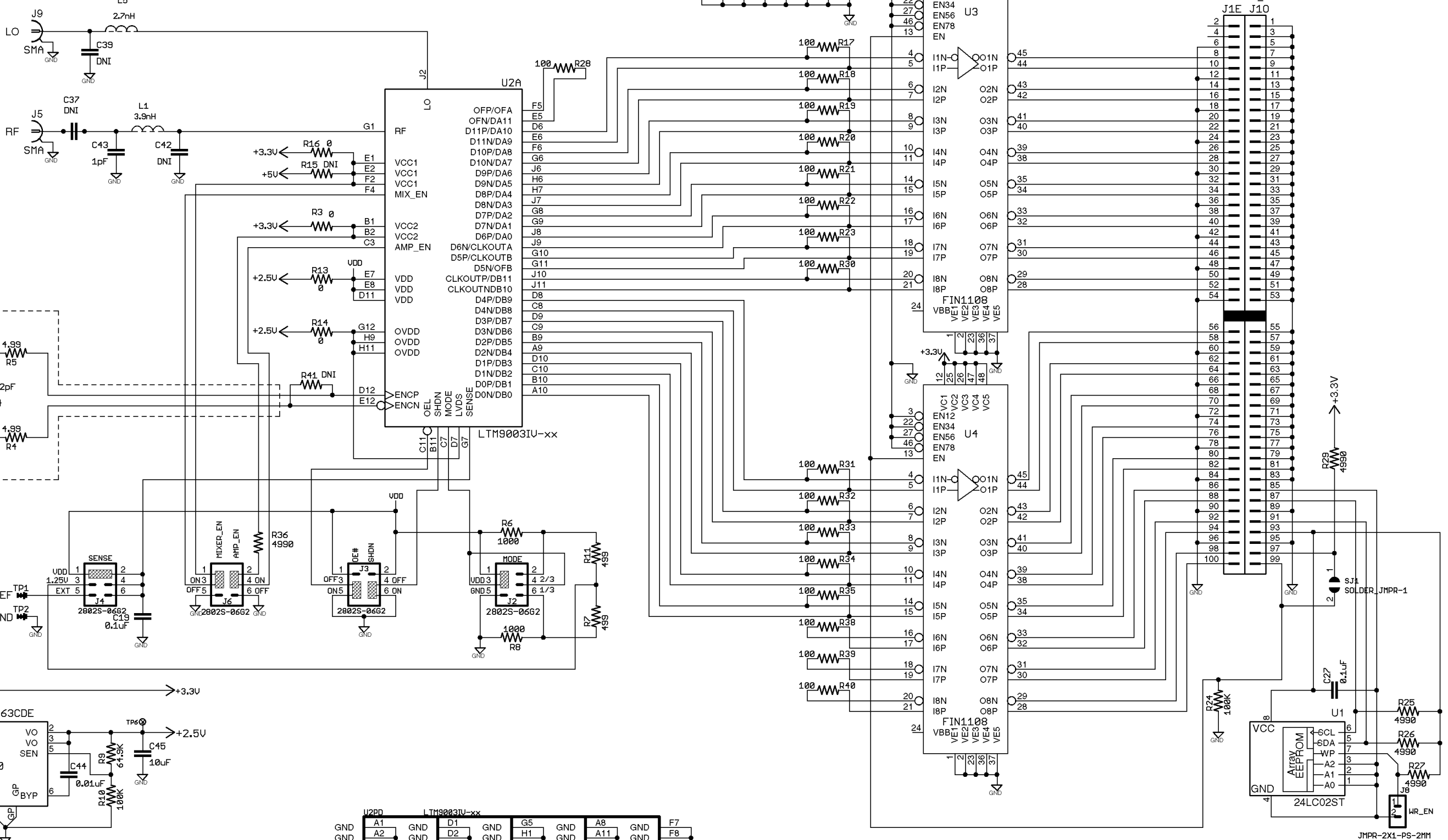
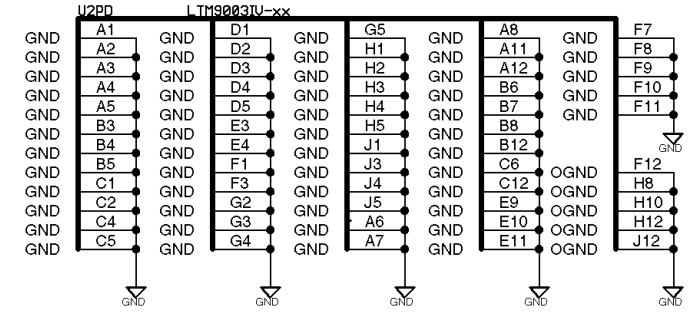
C42:  
For 800 MHz: install 3.3pF  
For 2, 2.3, 2.5 GHz: DNI  
For 3.5 GHz: install 1.5pF

L1:  
For 2 GHz: install 3.9nH  
For 2.3 GHz: install 2.7nH  
For 2.5 GHz: install 2.2nH  
For 800 MHz, 3.5 GHz: install 0 ohm

C43:  
For 800 MHz: DNI  
For 2, 2.3, 2.5 GHz: install 1.0pF  
For 3.5 GHz: DNI



Assy.	U2	R15	R16
-AA	LTM9003-AA	DNI	0 ohm
-AB	LTM9003-AB	0 ohm	DNI
-AC	LTM9003-AC	DNI	0 ohm



CUSTOMER NOTICE		CONTRACT NO.		1630 McCarthy Blvd. Milpitas, CA 95035	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMERS RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS DRAWN E.TRELEWICZ CHECKED APPROVED ENGINEER E.TRELEWICZ DESIGNER		TITLE: SCHEMATIC LTM9003 PRE-DISTORTION RECEIVER, LVDS OUTPUT	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS		FILENAME: DC1437B	DATE: 6/02/2010 11:24:34a	SIZE B DWG NO. DC1437	REV: B SHEET 1 OF 1