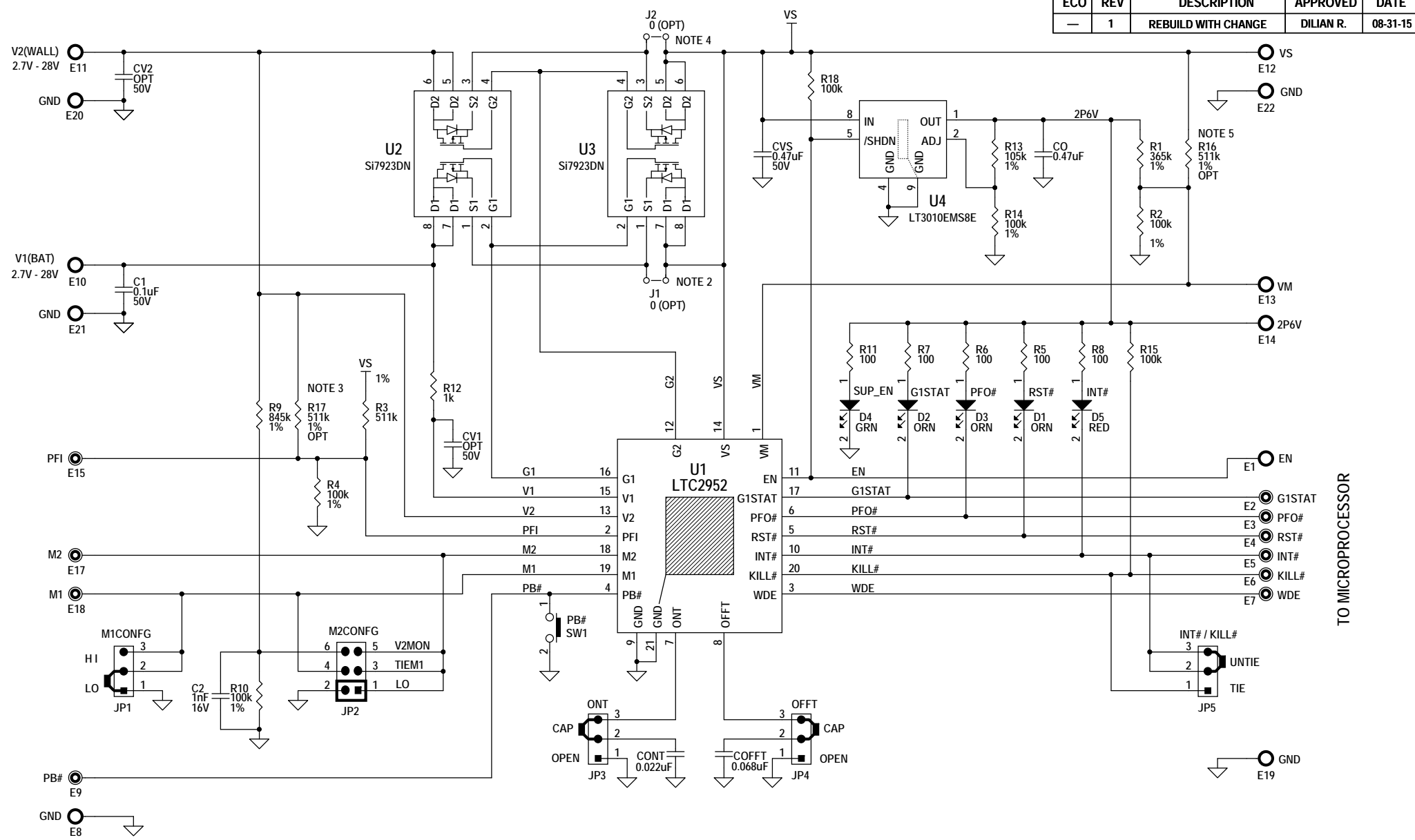


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	REBUILD WITH CHANGE	DILIAN R.	08-31-15



**NOTE: UNLESS OTHERWISE SPECIFIED**

1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. BYPASS V1 SECOND FET OPTION WITH 0 OHM RESISTOR.
3. BYPASS V2 SECOND FET OPTION WITH 0 OHM RESISTOR.
4. REMOVE R3 AND INCLUDE R17 TO MONITOR V2 AT PFI.
5. REMOVE R1 AND INCLUDE R16 FOR VM TO MONITOR VS INSTEAD OF DC/DC OUTPUT VOLTAGE.
6. CV1, CV2 SHOULD BE SMALL IF NEEDED.

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	DILIAN R.
SCALE = NONE	

		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
		TITLE: SCHEMATIC <b>PUSHBUTTON ON/OFF POWERPATH CONTROLLER</b>	
SIZE N/A	IC NO. LTC2952CUF DEMO CIRCUIT 1033B	REV. 1	
DATE: Monday, August 31, 2015		SHEET 1 OF 1	

TO MICROPROCESSOR