



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS ON JP1-JP7 PIN 1 AND 2.
3. U7 MULTIPLEXES THE SPI AND I2C BUSSES AND IS FOR COMPATIBILITY WITH THE DC590 CONTROLLER BOARD ONLY.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: MARK T.

DESIGNER:



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TITLE: SCHEMATIC
16-BIT DUAL RAIL-TO-RAIL DAC WITH I2C INTERFACE

SIZE A	DWG NO. DC934A-LTC2607CDE	REV A
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DATE: Monday, September 26, 2005	SHEET 1 OF 1
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