



* VERSION TABLE

ASSEMBLY VERSION	U1	Vin	Vout / Iout
DC761A-A	LTC3204EDC-5	3.1V-5.5V	5V/150mA
		2.7V-5.5V	5V/65mA
DC761A-B	LTC3204EDC-3.3	1.9V-4.5V	3.3V/50mA
		1.8V-4.5V	3.3V/40mA

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS DATE

DRAWN June Wu 9/2/04

CHECKED

APPROVED

ENGINEER Walker Bai 9/2/04

DESIGNER

Thursday, November 04, 2004



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TITLE

LTC3204EDC, Low Noise Regulated Charge Pump in 2x2 DFN

SIZE

CAGE CODE

DWG NO

DC761A

REV

A

SCALE:

FILENAME:

SHEET 1 OF 1