


## NOTES: UNLESS OTHERWISE SPECIFIED

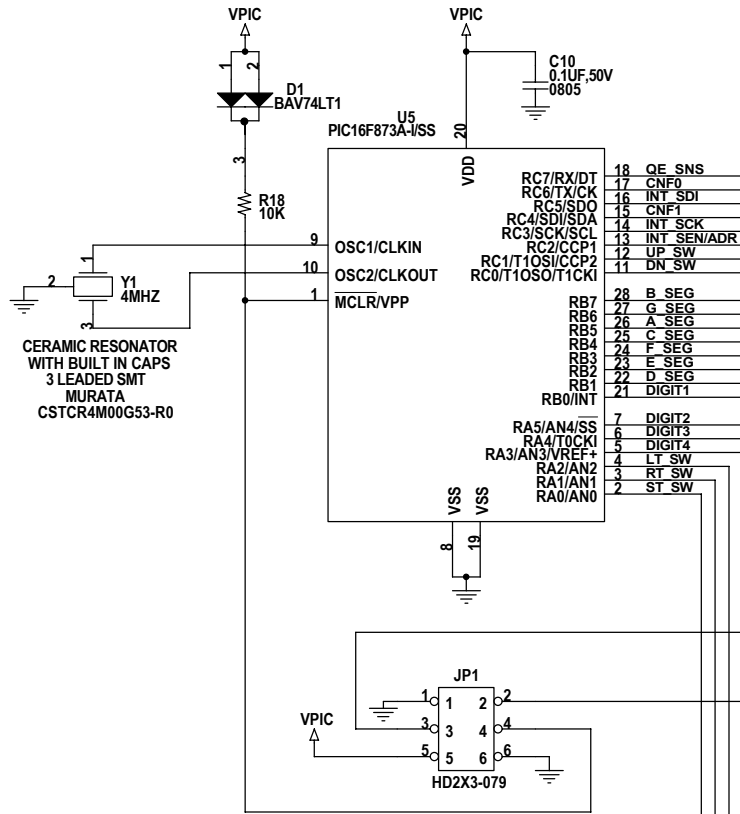
1. ALL RESISTORS ARE IN OHMS, 0402.
2. THERE ARE 2 VERSIONS FOR ASSEMBLY:

ASSEMBLY	U1	R39, R40
DC726A-A	LTC6903CMS8	OPT
DC726A-B	LTC6904CMS8	10K

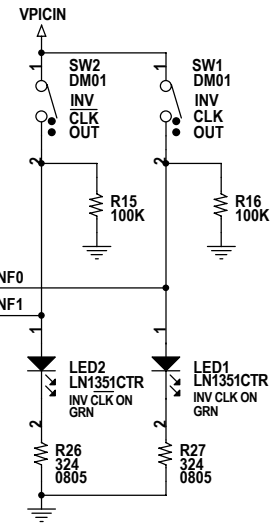
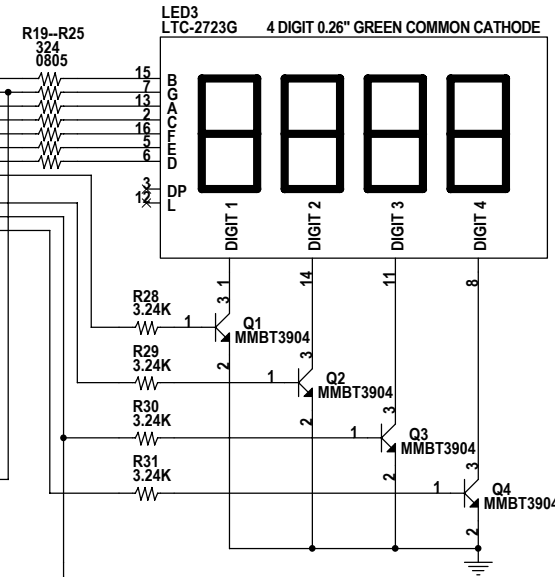
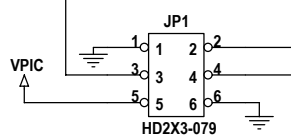
3. INSTALL 3 RESISTORS, 10K, 0805, ON JP3 PIN 1 AND 2, PIN 4 AND 5, PIN 7 AND 8 AS SHOWN ON LAYOUT.
4. R4 AND R8 ARE OPT FOR EXTERNAL I2C CONTROL.

CUSTOMER NOTICE		CONTRACT NO.	 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS DRAWN: KIM T. CHECKED: APPROVED: ENGINEER: PHILIP K. DESIGNER:	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SIZE <b>A</b>	DWG NO. <b>DC726A-1 * LTC6903CMS8/ LTC6904CMS8</b>
		DATE:	REV <b>A</b>
		Wednesday, April 07, 2004	SHEET 1 OF 2

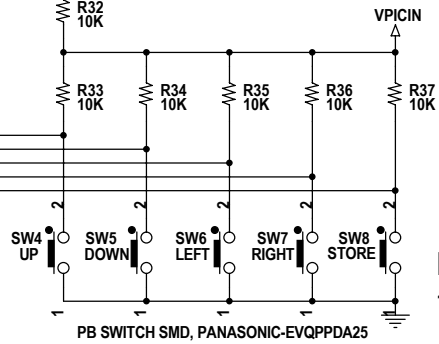
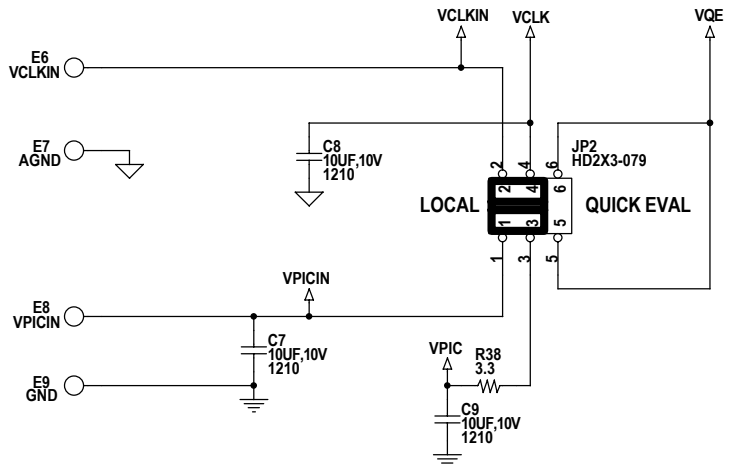
INT\_SEN/ADR << INT\_SEN/ADR  
 INT\_SCK << INT\_SCK  
 INT\_SDI << INT\_SDI



CERAMIC RESONATOR  
 WITH BUILT IN CAPS  
 3 LEADED SMT  
 MURATA  
 CSTCR4M00G53-R0



OUTPUT SELECT  
 INDICATORS &  
 SWITCHES



**NOTES: UNLESS OTHERWISE SPECIFIED**  
 1. INSTALL ONE 2X2 SHUNT ON JP2 PIN 1 AND 3, PIN 2 AND 4 AS SHOWN.  
 NO SHUNT ON JP1.

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		DRAWN: KIM T.			
		CHECKED:			
		APPROVED:			
		ENGINEER: PHILIP K.			
DESIGNER:	TITLE: SCHEMATIC	<b>PROGRAMABLE CLOCK 1KHz TO 68MHz</b>			
DATE: Wednesday, April 07, 2004	SIZE: A			DWG NO. DC726A-1 * LTC6903CMS8/ LTC6904CMS8	REV: A
		SHEET 2 OF 2			