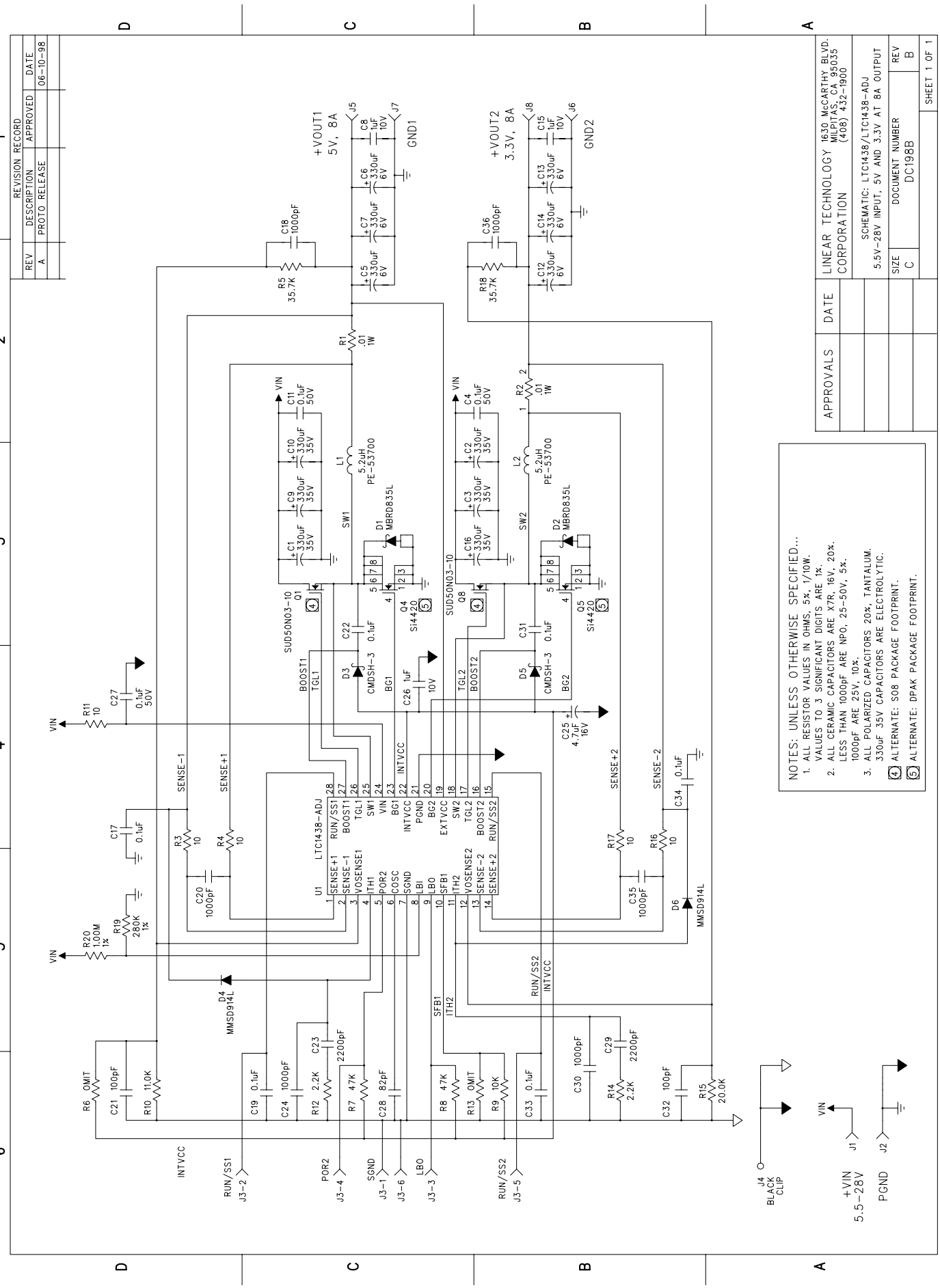
REVISION RECORD
REV | DESCRIPTION | APPROVED | DATE
A | PROTO RELEASE | | 06-10-98
+VOUT1
5V, 8A
+VOUT2
3.3V, 8A
GND1
GND2
VIN
LTC1438-ADJ
SUD50N03-10
MBRD835L
Si4420
CMDSH-3
MMSD914L
NOTES: UNLESS OTHERWISE SPECIFIED...
1. ALL RESISTOR VALUES IN OHMS, 5%, 1/10W.
VALUES TO 3 SIGNIFICANT DIGITS ARE 1%.
2. ALL CERAMIC CAPACITORS ARE X7R, 16V, 20%.
LESS THAN 1000pF ARE NPO, 25-50V, 5%.
1000pF ARE 25V, 10%.
3. ALL POLARIZED CAPACITORS 20%, TANTALUM.
330uF 35V CAPACITORS ARE ELECTROLYTIC.
4. ALTERNATE: SO8 PACKAGE FOOTPRINT.
5. ALTERNATE: DPAK PACKAGE FOOTPRINT.
APPROVALS | DATE
LINEAR TECHNOLOGY CORPORATION
1630 McCARTHY BLVD. MILPITAS, CA 95035 (408) 432-1900
SCHEMATIC: LTC1438/LTC1438-ADJ
5.5V-28V INPUT, 5V AND 3.3V AT 8A OUTPUT
SIZE C | DOCUMENT NUMBER DC198B | REV B
SHEET 1 OF 1
INTVCC
RUN/SS1
POR2
SGND
LBO
RUN/SS2
+VIN 5.5-28V
PGND
BLACK CLIP