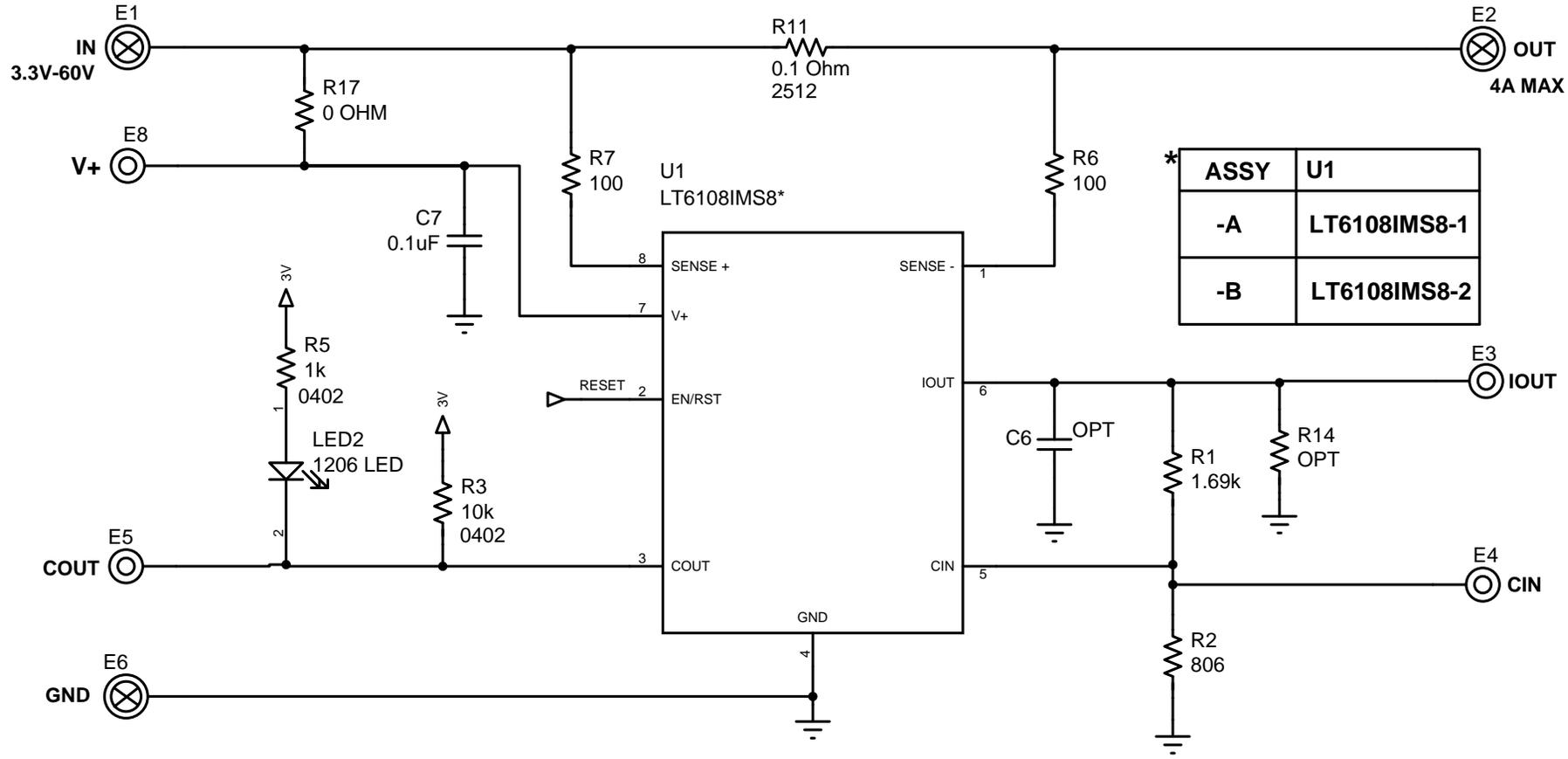


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
	1	PRODUCTION	CUYLER L.	07/01/2010



ASSY	U1
-A	LT6108IMS8-1
-B	LT6108IMS8-2

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603
2. ALL CAPACITORS ARE IN MICRO FARADS, 0603
3. INSTALL SHUNTS AT J1 PIN 1+2

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	CL
APP ENG.	CUYLER L.

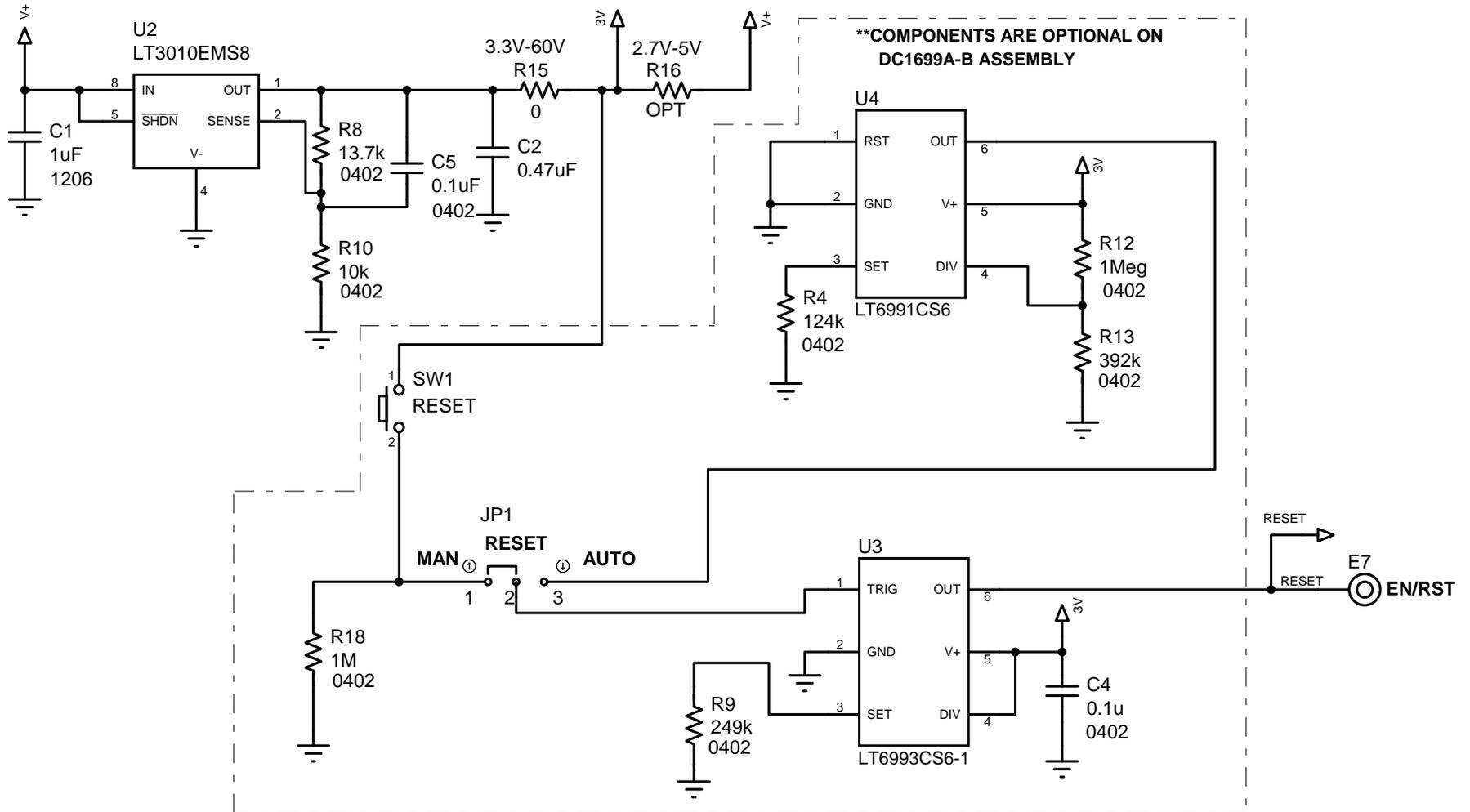
SCALE = NONE



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TITLE: SCHEMATIC
PRECISION CURRENT SENSE WITH COMPARATOR AND REFERENCE

SIZE N/A	IC NO. LT6108IMS8-1/-2 DEMO CIRCUIT 1699A	REV 1
DATE: 07/2010	SH 1 of 2	



CUSTOMER NOTICE

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APPROVALS

PCB DES.	CL
APP ENG.	CUYLER L.

SCALE = NONE



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PRECISION CURRENT SENSE WITH COMPARATOR AND REFERENCE

SIZE N/A	IC NO. LT6108IMS8-1/-2 DEMO CIRCUIT 1699A	REV 1
DATE:	07/2010	SH 2 of 2