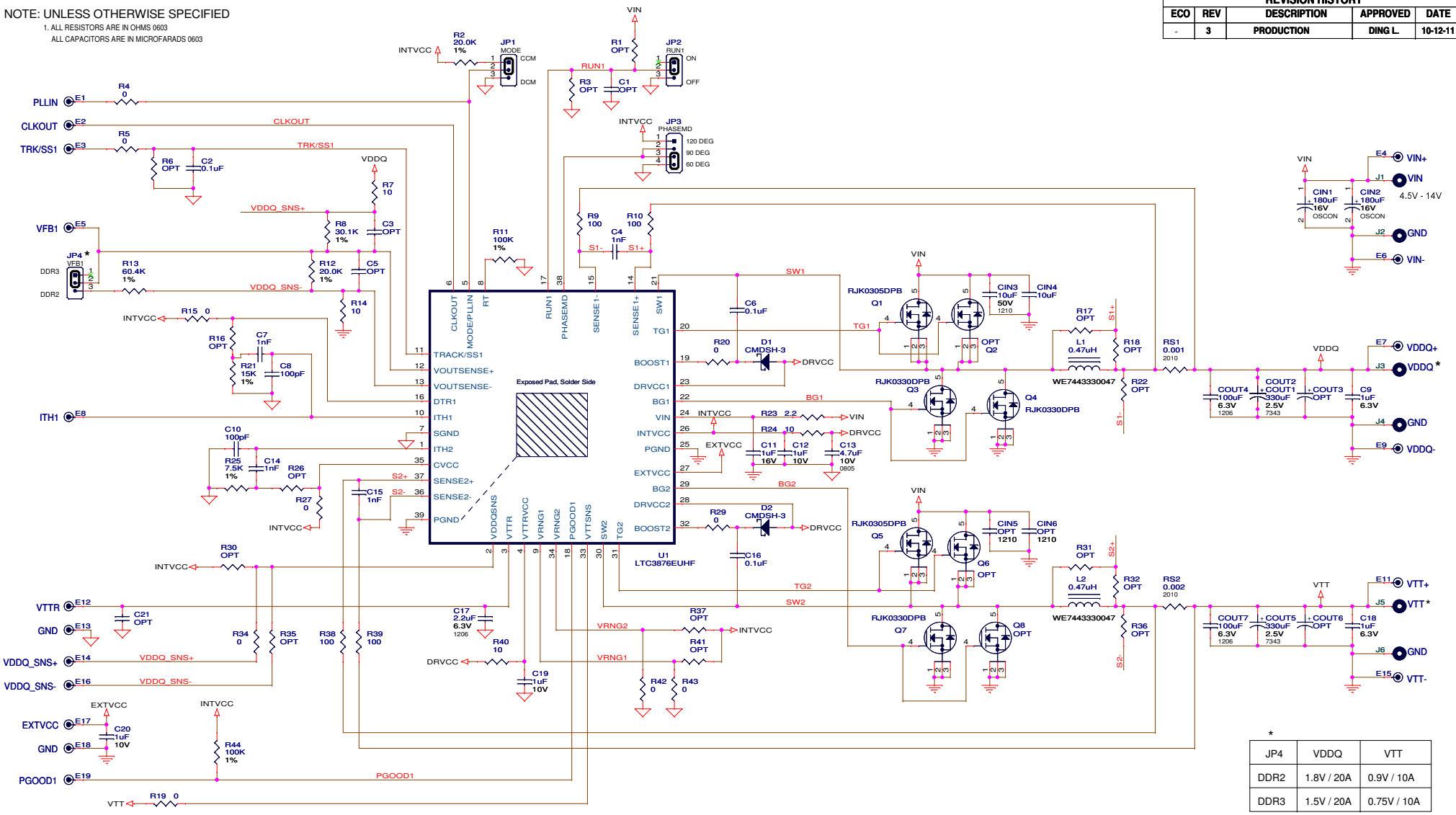
NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS 0603
ALL CAPACITORS ARE IN MICROFARADS 0603

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	DING L.	10-12-11



JP4	VDDQ	VTT
DDR2	1.8V / 20A	0.9V / 10A
DDR3	1.5V / 20A	0.75V / 10A

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

PCB DES. HZ

ENG. DING L.

LINEAR TECHNOLOGY
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Milpitas, CA 95035
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Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

HIGH EFFICIENCY, DUAL-OUTPUT DDR POWER SUPPLY

SIZE	IC NO.	REV
N/A	LTC3876EUHF DEMO CIRCUIT 1631A	3

DATE: Thursday, January 05, 2012 SHEET 1 OF 1