

Precision 10V Reference

DESCRIPTION

The RH1021-10 is a precision 10V reference with ultralow drift and noise, extremely good long-term stability and almost total immunity to input voltage variations. The reference output will source and sink up to 10mA. This reference can also be used as a shunt regulator (2-terminal Zener). Unique circuit design makes the RH1021-10 the first IC reference to offer ultralow drift without the use of high power on-chip heaters.

The wafer lots are processed to Linear Technology's in-house Class S flow to yield circuits usable in stringent military applications.

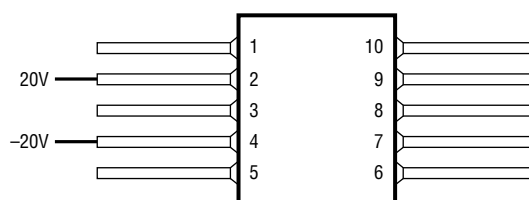
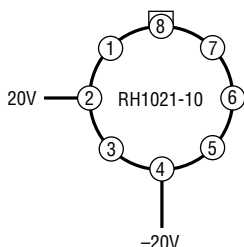
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ABSOLUTE MAXIMUM RATINGS

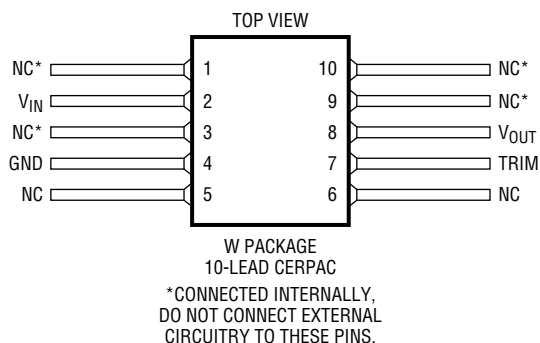
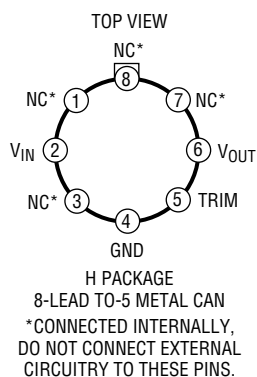
(Note 10)

Input Voltage.....	40V
Input/Output Voltage Differential	35V
Output to Ground Voltage (Shunt Mode Current Limit)	16V
Trim Pin to Ground Voltage Positive.....	Equal to V_{OUT}
Negative.....	-20V
Output Short-Circuit Duration $V_{IN} = 35V$	10 sec
$V_{IN} \leq 20V$	Indefinite
Operating Temperature Range	-55°C to 125°C
Maximum Junction Temperature	150°C
Storage Temperature Range	-65°C to 150°C
Lead Temperature (Soldering, 10 sec).....	300°C

BURN-IN CIRCUITS



PIN CONFIGURATION



RH1021-10

ORDER INFORMATION

LEAD BASED FINISH	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	TEMPERATURE RANGE
RH1021BMH-10	RH1021BMH-10#TR		8-Lead TO-5 Metal Can	–55°C to 125°C
RH1021CMH-10	RH1021CMH-10#TR		8-Lead TO-5 Metal Can	–55°C to 125°C
RH1021DMH-10	RH1021DMH-10#TR		8-Lead TO-5 Metal Can	–55°C to 125°C
RH1021CMW-10	RH1021CMW-10#TR		10-Lead CERPAC	–55°C to 125°C

Contact the factory for parts specified with wider operating temperature ranges.

[Tape and reel specifications.](#)

TABLE 1: ELECTRICAL CHARACTERISTICS (Preirradiation) (Note 9)

SYMBOL	PARAMETER	CONDITIONS	NOTES	T _A = 25°C			SUB-GROUP	–55°C ≤ T _A ≤ 125°C			SUB-GROUP	UNITS
				MIN	TYP	MAX		MIN	TYP	MAX		
V _{OUT}	Output Voltage	RH1021CM-10 RH1021BM-10, DM-10	1 1	9.995		10.005	1 1					V V
TCV _{OUT}	Output Voltage Temperature Coefficient	RH1021BM-10 RH1021CM-10, DM-10	2 2							5 20	2, 3 2, 3	ppm/°C ppm/°C
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Line Regulation	11.5V ≤ V _{IN} ≤ 14.5V 14.5V ≤ V _{IN} ≤ 40V	3 3			4 2	1 1			6 4	2, 3 2, 3	ppm/V ppm/V
$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	Load Regulation (Sourcing Current)	0 ≤ I _{OUT} ≤ 10mA	3			25	1			40	2, 3	ppm/mA
	Load Regulation (Shunt Mode)	1.7mA ≤ I _{OUT} ≤ 10mA	3, 4			100	1			150	2, 3	ppm/mA
I _S	Supply Current (Series Mode)					1.7	1			2.0	2, 3	mA
I _{MIN}	Minimum Current (Shunt Mode)	V _{IN} is Open				1.5	1			1.7	2, 3	mA
	Output Voltage Noise	0.1Hz ≤ f ≤ 10Hz 10Hz ≤ f ≤ 1kHz	5 5		6							μV _{P-P} μV _{RMS}
	Long-Term Stability of V _{OUT}	ΔT = 1000 Hrs Noncumulative	6		15							ppm
	Temperature Hysteresis of V _{OUT}	ΔT = ±25°C			5							ppm

TABLE 1: ELECTRICAL CHARACTERISTICS (Postirradiation) (Note 7, 11)

SYMBOL	PARAMETER	CONDITIONS	NOTES	10Krad(Si)		20Krad(Si)		50Krad(Si)		100Krad(Si)		200Krad(Si)		UNITS
				MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
V _{OUT}	Output Voltage	RH1021CM-10 RH1021BM-10, DM-10	1	9.992	10.008	9.99	10.01	9.987	10.013	9.985	10.015	9.98	10.02	V
			1	9.95	10.05	9.945	10.055	9.942	10.06	9.938	10.06	9.935	10.065	V
TCV _{OUT}	Output Voltage Temperature Coefficient	RH1021BM-10 RH1021CM-10, DM-10	2		5		5		5		7		10	ppm/°C
			2		20		20		20		22		25	ppm/°C
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Line Regulation	11.5V ≤ V _{IN} ≤ 14.5V 14.5V ≤ V _{IN} ≤ 40V	3		4		4		4.5		5		6	ppm/V
			3		2		2		2		2		3	ppm/V
$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	Load Regulation (Sourcing Current)	0 ≤ I _{OUT} ≤ 10mA	3, 8		25		25		25		25		25	ppm/mA
			3, 4		100		100		100		100		150	ppm/mA
I _{MIN}	Minimum Current (Shunt Mode)	V _{IN} is Open			1.5		1.5		1.5		1.5		1.5	mA
I _S	Supply Current (Series Mode)				1.7		1.7		1.7		1.7		1.7	mA

TABLE 1A: ELECTRICAL CHARACTERISTICS

Note 1: Output voltage is measured immediately after turn-on. Changes due to chip warm-up are typically less than 0.005%.

Note 2: Temperature coefficient is measured by dividing the change in output voltage over the temperature range by the change in temperature. Separate tests are done for hot and cold; T_{MIN} to 25°C and 25°C to T_{MAX}. Incremental slope is also measured at 25°C.

Note 3: Line and load regulation are measured on a pulse basis. Output changes due to die temperature change must be taken into account separately. Package thermal resistance is 150°C/W for the TO-5 (H) package and 170°C/W for the 10-lead flatpack (W) package.

Note 4: Shunt mode regulation is measured with the input open. With the input connected, shunt mode current can be reduced to 0mA. Load regulation will remain the same.

Note 5: RMS noise is measured with a 2-pole highpass filter at 10Hz and a 2-pole lowpass filter at 1kHz. The resulting output is full wave rectified

and then integrated for a fixed period, making the final reading an average as opposed to RMS. Correction factors are used to convert from average to RMS and to correct for the nonideal bandpass of the filters. Peak-to-peak noise is measured with a single highpass filter at 0.1Hz and a 2-pole lowpass filter at 10Hz. The unit is enclosed in a still-air environment to eliminate thermocouple effects on the leads. Test time is 10 seconds.

Note 6: Consult factory for units with long term stability data.

Note 7: V_{IN} = 15V, I_{OUT} = 0, T_A = 25°C, unless otherwise noted.

Note 8: I_{OUT(MAX)} (Sourcing) is 5mA for exposures greater than 100Krad (Si).

Note 9: V_{IN} = 15V, I_{OUT} = 0, unless otherwise noted.

Note 10: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 11: Device is characterized at 10Krad, 20Krad, 50Krad, 100Krad and 200Krad, and is production tested at 100Krad only.

TABLE 2: ELECTRICAL TEST REQUIREMENTS

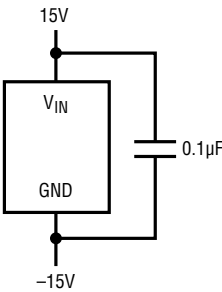
MIL-STD-883 TEST REQUIREMENTS	SUBGROUP
Final Electrical Test Requirements	1*, 2, 3, 4
Group A Test Requirements	1, 2, 3, 4
Group C End Point Electrical Parameters	1, 2, 3
Group D for End Point Electrical Parameters	1, 2, 3
Group D for End Point Electrical Parameters	1

*PDA Applies to subgroup 1. See PDA Test Notes.

PDA Test Notes

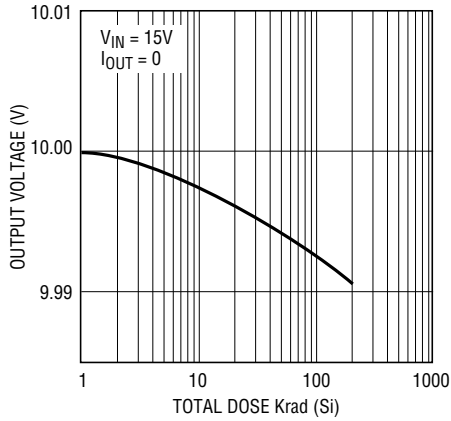
The PDA is specified as 5% based on failures from group A, subgroup 1, tests after cooldown as the final electrical test in accordance with method 5004 of MIL-STD-883. The verified failures of group A, subgroup 1, after burn-in divided by the total number of devices submitted for burn-in in that lot shall be used to determine the percent for the lot.
Linear Technology Corporation reserves the right to test to tighter limits than those given.

TOTAL DOSE BIAS CIRCUIT



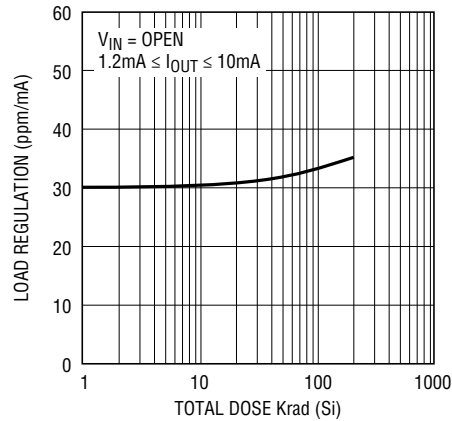
TYPICAL PERFORMANCE CHARACTERISTICS

Output Voltage



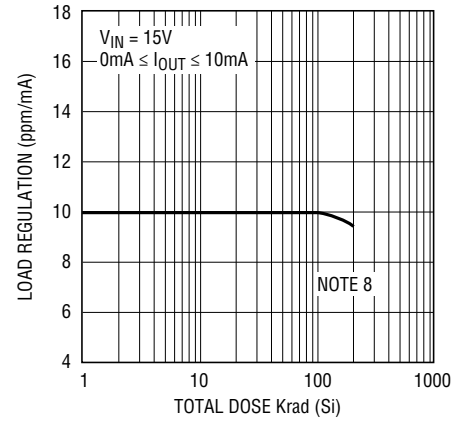
RH1021-10 G01

Load Regulation (Shunt Mode)



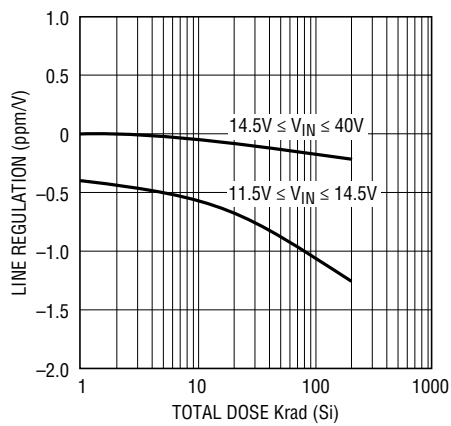
RH1021-10 G02

Load Regulation (Sourcing)



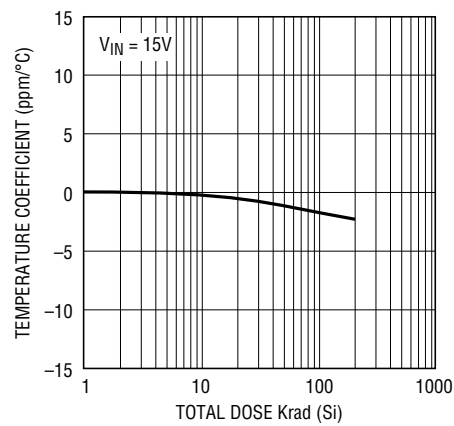
RH1021-10 G03

Line Regulation



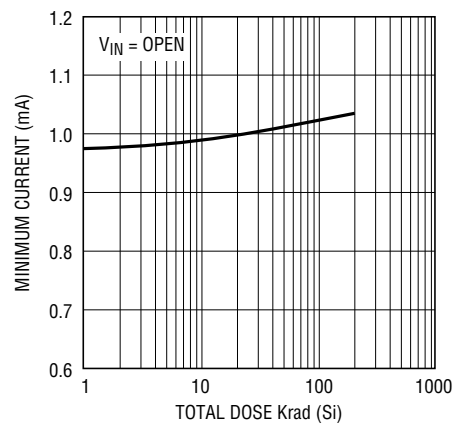
RH1021-10 G04

Temperature Coefficient



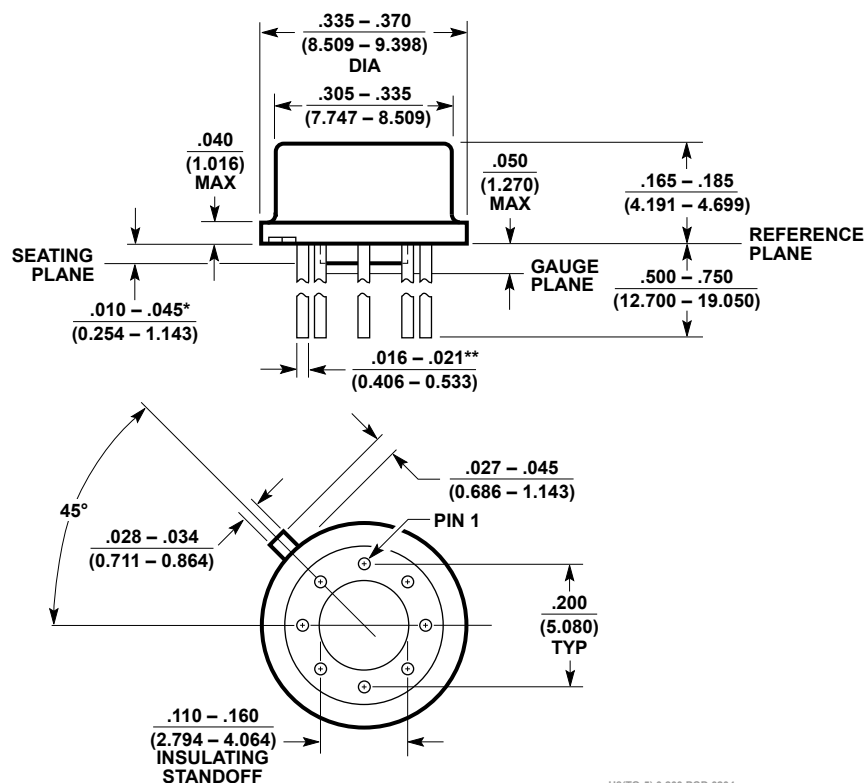
RH1021-10 G05

Minimum Current (Shunt Mode)



RH1021-10 G06

PACKAGE OUTLINE DRAWINGS

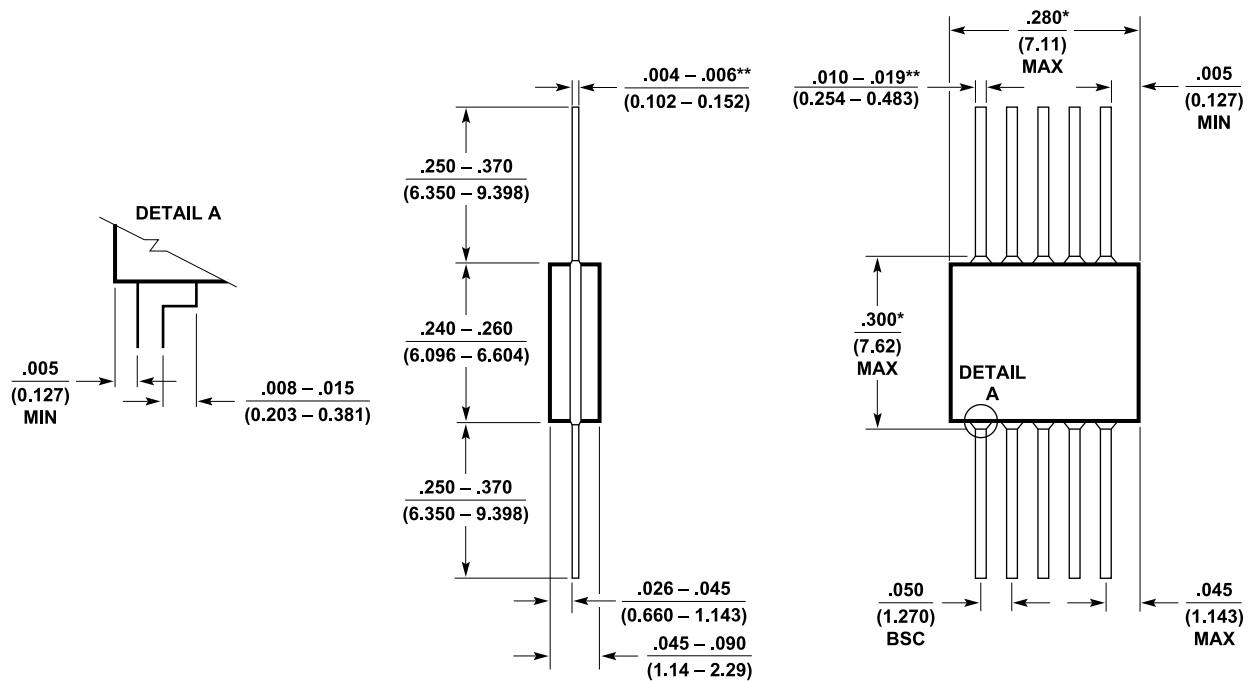


*LEAD DIAMETER IS UNCONTROLLED BETWEEN THE REFERENCE PLANE AND THE SEATING PLANE

**FOR SOLDER DIP LEAD FINISH, LEAD DIAMETER IS $\frac{.016 - .024}{(0.406 - 0.610)}$

H Package
8-Lead TO-5 Metal Can (.200 Inch PCD)
(Reference LTC DWG # 05-08-1320)

PACKAGE OUTLINE DRAWINGS



NOTES:

*THIS DIMENSION ALLOWS FOR OFF-CENTER LID, MENISCUS AND GLASS OVERRUN

**INCREASE DIMENSIONS BY 0.003 INCHES (0.076 mm) WHEN LEAD FINISH A IS APPLIED (SOLDER DIPPED)

W10 (GLASS) 0603

W Package
10-Lead Flatpak Glass Sealed (Hermetic)
(Reference LTC DWG # 05-08-1130)

REVISION HISTORY (Revision history begins at Rev F)

REV	DATE	DESCRIPTION	PAGE NUMBER
F	10/18	Maximum Junction Temperature added.	1
G	12/25	Changes to Table 1: Electrical Characteristics	3
		Changes to Table 2: Electrical Test Requirements	4
		Added Package Outline Drawings	6-7