

Quad 1.5Gbps GMSL Deserializer with Coax or STP Input and CSI-2 Output

General Description

The MAX9286 Gigabit multimedia serial link (GMSL) deserializer receives data from up to four GMSL serializers over 50Ω coax or 100Ω shielded twisted-pair (STP) cables and output data on four CSI-2 lanes. Each serial link has an embedded control channel operating from 9.6kbps to 1Mbps in UART-to-UART, UART-to-I²C, and I²Cto-I²C mode. Using the control channel, a μC can program the serializers, deserializer, and peripheral device registers at any time, independent of video timing. A maskable broadcast write speeds programming of image sensor registers.

For use with longer cables, the deserializer has a programmable cable equalizer and programmable error detection and correction. The serial input meets ISO 10605 and IEC 61000-4-2 ESD standards. The core supply is 1.7V to 1.9V and the I/O supply is 1.7V to 3.6V.

The device is available in lead(Pb)-free, 56-pin, 8mm x 8mm SWTQFN and TQFN packages with exposed pad and 0.5mm lead pitch.

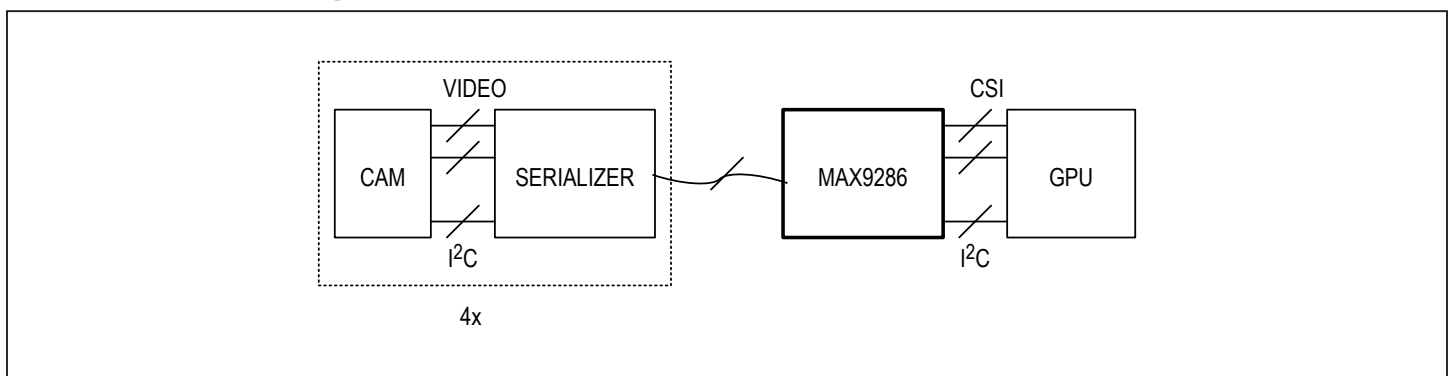
Applications

- Surround View Camera Systems
- Machine Vision Systems
- 3D Camera Systems

Benefits and Features

- Ideal for Multicamera Stream Applications
 - Works with Low-Cost 50Ω Coax Cable and FAKRA Connectors or 100Ω STP
 - Data from Image Sensors Are Synchronized to the Same Pixel
 - Automatic Internal/External Generation of Camera Sync
 - Equalization Allows 15m Length Cable Operation at Full Speed
- Multiple Input/Output Features for System Flexibility
 - 1 to 4 Lane CSI-2 Output with 80Mbps to 1200Mbps Per Lane
 - Swappable/Selectable Serial Input/Output with Swappable Polarity
 - 9.6kbps to 1Mbps Control Channel in UART, Mixed UART/I²C, or I²C Mode with Clock-Stretch Capability
- Peripheral Features for System Power-Up and Verification
 - Built-In PRBS Tester for BER Testing of the Serial Link
 - Programmable Choice of Nine Default Device Addresses
 - Two Dedicated GPIO Ports
 - High-Immunity Mode for Maximum Control-Channel Noise Rejection
- Meets Rigorous Automotive and Industrial Requirements
 - -40°C to +105°C Operating Temperature
 - ±8kV Contact, ±20kV Air ISO 10605 and ±8kV Contact, ±12kV IEC 61000-4-2 ESD Protection

Simplified Block Diagram



Visit [Web Support](#) to complete the nondisclosure agreement (NDA) required to receive additional product information.

Rev. 4

DOCUMENT FEEDBACK

TECHNICAL SUPPORT

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

