



20 Gbps Clocked COMPARATOR with RSCML OUTPUT STAGE

Application Circuits: CLK, $\overline{\text{CLK}}$ Interfacing

Figure A1: Resistor Network

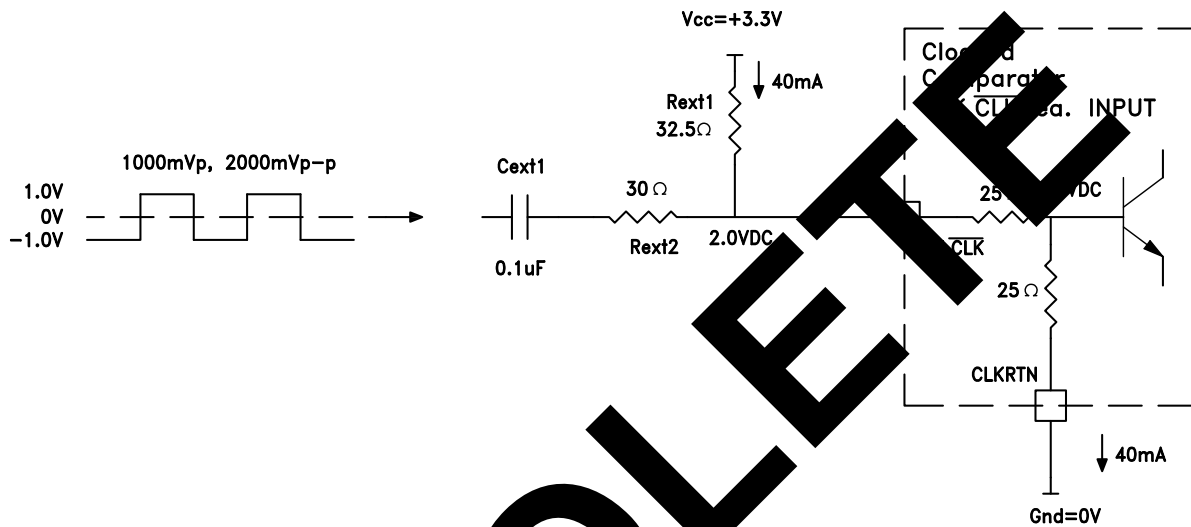
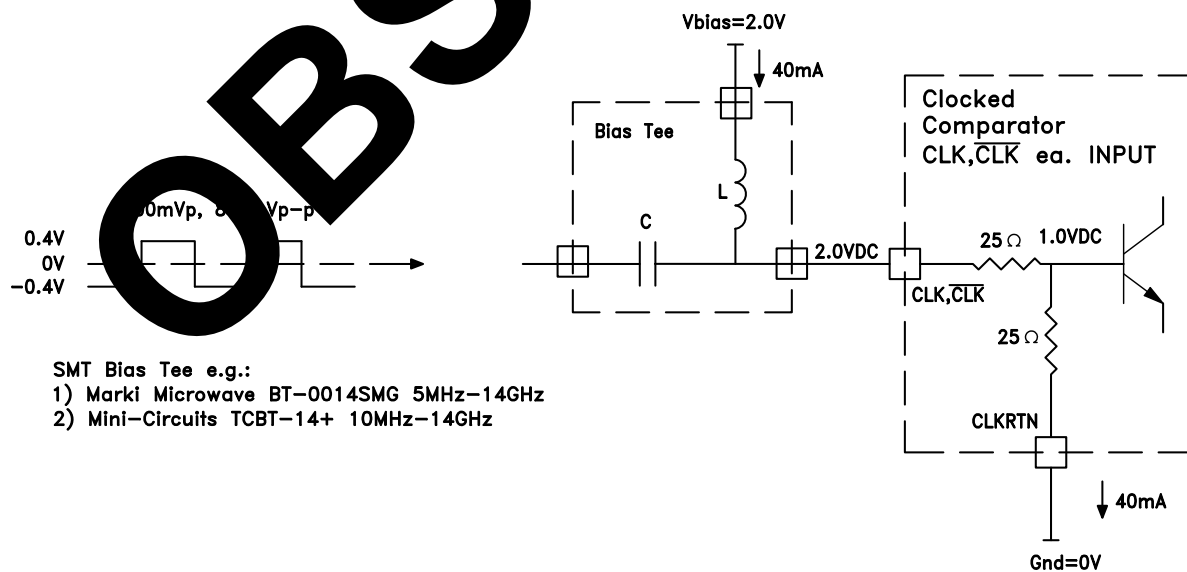


Figure A2: Bias Tee



- SMT Bias Tee e.g.:
- 1) Marki Microwave BT-0014SMG 5MHz-14GHz
 - 2) Mini-Circuits TCBT-14+ 10MHz-14GHz