FEATURES
Compact 5-lead TSOT package
Low temperature coefficient
B grade: 9 ppm/°C
A grade: 25 ppm/°C
Initial accuracy
B grade: ±4 mV maximum (ADR391)
A grade: ±6 mV maximum
Ultralow output noise: 5 μV p-p (0.1 Hz to 10 Hz)
Low dropout: 300 mV
Low supply current
3 μA maximum in shutdown
140 μA maximum in operation
No external capacitor required
Output current: 5 mA
Automotive grade available
Wide temperature range: −40°C to +125°C

APPLICATIONS
Battery-powered instrumentation
Portable medical instrumentation
Data acquisition systems
Industrial process controls
Automotive

GENERAL DESCRIPTION
The ADR391/ADR392/ADR395 are precision 2.5 V, 4.096 V, and 5 V band gap voltage references, respectively, featuring low power and high precision in a tiny footprint. Using patented temperature drift curvature correction techniques from Analog Devices, Inc., the ADR39x references achieve a low 9 ppm/°C of temperature drift in the TSOT package.

The ADR39x family of micropower, low dropout voltage references provides a stable output voltage from a minimum supply of 300 mV above the output. Their advanced design eliminates the need for external capacitors, which further reduces board space and system cost. The combination of low power operation, small size, and ease of use makes the ADR39x precision voltage references ideally suited for battery-operated applications.

Table 1.

<table>
<thead>
<tr>
<th>Model</th>
<th>Output Voltage (V0)</th>
<th>Temperature Coefficient (ppm/°C)</th>
<th>Accuracy (mV)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADR391B</td>
<td>2.5</td>
<td>9</td>
<td>±4</td>
</tr>
<tr>
<td>ADR391A</td>
<td>2.5</td>
<td>25</td>
<td>±6</td>
</tr>
<tr>
<td>ADR392B</td>
<td>4.096</td>
<td>9</td>
<td>±5</td>
</tr>
<tr>
<td>ADR392A</td>
<td>4.096</td>
<td>25</td>
<td>±6</td>
</tr>
<tr>
<td>ADR395B</td>
<td>5.0</td>
<td>9</td>
<td>±5</td>
</tr>
<tr>
<td>ADR395A</td>
<td>5.0</td>
<td>25</td>
<td>±6</td>
</tr>
</tbody>
</table>

PIN CONFIGURATION

![PIN CONFIGURATION Diagram](https://www.analog.com/en/technical-documentation/data-sheets/adr391-001.pdf)
TABLE OF CONTENTS

Features ........................................................................................................ 1
Applications .................................................................................................. 1
Pin Configuration ......................................................................................... 1
General Description .................................................................................... 1
Revision History .......................................................................................... 2
Specifications ............................................................................................... 3
  ADR391 Electrical Characteristics ........................................................... 3
  ADR392 Electrical Characteristics ........................................................... 4
  ADR395 Electrical Characteristics ........................................................... 5
Absolute Maximum Ratings ......................................................................... 6
  Thermal Resistance ................................................................................... 6

REVISION HISTORY

  Change to General Description Section .................................................. 1
  Added Figure 19; Renumbered Sequentially .............................................. 9
  Changes to Shutdown Mode Operation Section ....................................... 14
  Added Figure 35 and Figure 36 ................................................................. 14
  Deleted Figure 40; Renumbered Sequentially .......................................... 17
  Changes to Ordering Guide ...................................................................... 18
  Updated Outline Dimensions ................................................................. 18

  Deleted ADR390 ..................................................................................... Universal
  Changes to Ordering Guide Section ........................................................ 18

2/2008—Rev. F to Rev. G
  Changes to Ripple Rejection Ration Parameter (Table 2) ..................... 3
  Changes to Ripple Rejection Ration Parameter (Table 3) ..................... 4
  Changes to Ripple Rejection Ration Parameter (Table 4) ..................... 5
  Changes to Ripple Rejection Ration Parameter (Table 5) ..................... 6
  Changes to Figure 7 ................................................................................ 9
  Changes to Outline Dimensions ............................................................ 19
  Changes to Ordering Guide .................................................................... 19

5/2005—Rev. E to Rev. F
  Changes to Table 5 ................................................................................ 7
  Changes to Figure 2 ................................................................................. 9

  Changes to ADR390—Specifications ..................................................... 3
  Changes to ADR391—Specifications ..................................................... 4
  Changes to ADR392—Specifications ..................................................... 5
  Changes to ADR395—Specifications ..................................................... 6

  Updated Format ....................................................................................... Universal
  Changes to Title ....................................................................................... 1
  Changes to Features ................................................................................ 1

ESD Caution ............................................................................................... 6
  Typical Performance Characteristics ..................................................... 7
  Terminology ............................................................................................ 13
  Theory of Operation ............................................................................... 14
  Device Power Dissipation Considerations .......................................... 14
  Shutdown Mode Operation ................................................................... 14
  Applications Information ....................................................................... 15
  Basic Voltage Reference Connection ................................................. 15
  Capacitors .............................................................................................. 17
  Outline Dimensions ................................................................................ 18
  Ordering Guide ....................................................................................... 18

Changes to Applications ............................................................................. 1
Changes to General Description ............................................................... 1
Changes to Table 1 .................................................................................... 1
Changes to ADR390—Specifications ......................................................... 3
Changes to ADR391—Specifications ......................................................... 4
Changes to ADR392—Specifications ......................................................... 5
Changes to ADR395—Specifications ......................................................... 6
Changes to Absolute Maximum Ratings .................................................. 7
Changes to Thermal Resistance ............................................................... 7
Moved ESD Caution ................................................................................... 7
Changes to Figure 3, Figure 4, Figure 7, and Figure 8 ............................ 9
Changes to Figure 11, Figure 12, Figure 13, and Figure 14 ................. 10
Changes to Figure 15, Figure 16, Figure 19, and Figure 20 ............... 11
Changes to Figure 23 and Figure 24 ....................................................... 12
Changes to Figure 27 ................................................................................ 13
Changes to Ordering Guide ..................................................................... 19
Updated Outline Dimensions ................................................................. 19

  Add parts ADR392 and ADR395 .......................................................... Universal
  Changes to Features ................................................................................ 1
  Changes to General Description ............................................................ 1
  Additions to Table 1 ................................................................................ 1
  Changes to Specifications ....................................................................... 2
  Changes to Ordering Guide .................................................................... 4
  Changes to Absolute Maximum Ratings ................................................. 4
  New TPCs 3, 4, 7, 8, 11, 12, 15, 16, 19, and 20 .................................... 6
  New Figures 4 and 5 ................................................................................. 13
  Deleted A Negative Precision Reference without Precision Resistors Section .......................................................... 13
  Edits to General-Purpose Current Source Section ............................ 13
  Updated Outline Dimensions ................................................................. 15

  Edits to Layout ....................................................................................... Universal
  Changes to Figure 6 ................................................................................. 13
## SPECIFICATIONS

### ADR391 ELECTRICAL CHARACTERISTICS

$V_{IN} = 2.8$ V to 15 V, $T_A = 25^\circ$C, unless otherwise noted.

Table 2.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>OUTPUT VOLTAGE</td>
<td>$V_O$</td>
<td>A grade</td>
<td>2.494</td>
<td>2.5</td>
<td>2.506</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td>2.496</td>
<td>2.5</td>
<td>2.504</td>
<td>V</td>
</tr>
<tr>
<td>INITIAL ACCURACY</td>
<td>$V_{OERR}$</td>
<td>A grade</td>
<td>6</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>A grade</td>
<td>0.24</td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td>4</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td>0.16</td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>TEMPERATURE COEFFICIENT</td>
<td>$TCV_O$</td>
<td>A grade, $-40^\circ$C $&lt; T_A &lt; +125^\circ$C</td>
<td>25</td>
<td></td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade, $-40^\circ$C $&lt; T_A &lt; +125^\circ$C</td>
<td>9</td>
<td></td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td>SUPPLY VOLTAGE HEADROOM</td>
<td>$V_{IN} - V_O$</td>
<td></td>
<td>300</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>LINE REGULATION</td>
<td>$\Delta V_O/\Delta V_{IN}$</td>
<td>$V_{IN} = 2.8$ V to 15 V, $-40^\circ$C $&lt; T_A &lt; +125^\circ$C</td>
<td>10</td>
<td>25</td>
<td></td>
<td>ppm/V</td>
</tr>
<tr>
<td>LOAD REGULATION</td>
<td>$\Delta V_O/\Delta I_{LOAD}$</td>
<td>$I_{LOAD} = 0$ mA to 5 mA, $-40^\circ$C $&lt; T_A &lt; +85^\circ$C, $V_{IN} = 3$ V</td>
<td>60</td>
<td></td>
<td>140</td>
<td>ppm/mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{LOAD} = 0$ mA to 5 mA, $-40^\circ$C $&lt; T_A &lt; +125^\circ$C, $V_{IN} = 3$ V</td>
<td></td>
<td></td>
<td></td>
<td>ppm/mA</td>
</tr>
<tr>
<td>QUIESCENT CURRENT</td>
<td>$I_{IN}$</td>
<td>No load, $-40^\circ$C $&lt; T_A &lt; +125^\circ$C</td>
<td>120</td>
<td></td>
<td>140</td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$-40^\circ$C $&lt; T_A &lt; +125^\circ$C</td>
<td></td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>VOLTAGE NOISE</td>
<td>$e_{n,p-p}$</td>
<td>0.1 Hz to 10 Hz</td>
<td>5</td>
<td></td>
<td></td>
<td>μV p-p</td>
</tr>
<tr>
<td>TURN-ON SETTLING TIME</td>
<td>$t_R$</td>
<td></td>
<td>20</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>LONG-TERM STABILITY$^1$</td>
<td>$\Delta V_O$</td>
<td>1000 hours</td>
<td>50</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>OUTPUT VOLTAGE HYSTERESIS</td>
<td>$\Delta V_{O,HYS}$</td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>RIPPLE REJECTION RATIO</td>
<td>$RRR$</td>
<td>$f_{IN} = 60$ Hz</td>
<td></td>
<td></td>
<td>$-80$</td>
<td>dB</td>
</tr>
<tr>
<td>SHORT CIRCUIT TO GND</td>
<td>$I_{SC}$</td>
<td>$V_{IN} = 5$ V</td>
<td>25</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{IN} = 15$ V</td>
<td>30</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>SHUTDOWN PIN</td>
<td>$I_{SHDN}$</td>
<td>Shutdown Supply Current</td>
<td>3</td>
<td>500</td>
<td>3</td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>$I_{LOGIC}$</td>
<td>Shutdown Logic Input Current</td>
<td>0.8</td>
<td></td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td></td>
<td>$V_{NL}$</td>
<td>Shutdown Logic Low</td>
<td>0.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>$V_{NH}$</td>
<td>Shutdown Logic High</td>
<td>2.4</td>
<td></td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

$^1$ The long-term stability specification is noncumulative. The drift of subsequent 1000 hour periods is significantly lower than in the first 1000 hour period.
ADR392 ELECTRICAL CHARACTERISTICS

$V_{IN} = 4.3\, \text{V to 15\,\text{V}}, T_A = 25\, ^\circ\text{C}$, unless otherwise noted.

### Table 3.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>OUTPUT VOLTAGE</td>
<td>$V_O$</td>
<td>A grade</td>
<td>4.090</td>
<td>4.096</td>
<td>4.102</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td>4.091</td>
<td>4.096</td>
<td>4.101</td>
<td>V</td>
</tr>
<tr>
<td>INITIAL ACCURACY</td>
<td>$V_{O\text{ERR}}$</td>
<td>A grade</td>
<td>6</td>
<td>0.15</td>
<td>5</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td>5</td>
<td>0.12</td>
<td>9</td>
<td>ppm/°C</td>
</tr>
<tr>
<td>TEMPERATURE COEFFICIENT</td>
<td>$T_{CV_O}$</td>
<td>A grade, $-40, ^\circ\text{C} &lt; T_A &lt; +125, ^\circ\text{C}$</td>
<td>25</td>
<td>9</td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td>SUPPLY VOLTAGE HEADROOM</td>
<td>$V_{IN} - V_O$</td>
<td></td>
<td>300</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>LINE REGULATION</td>
<td>$\Delta V_O/\Delta V_{IN}$</td>
<td>$V_{IN} = 4.3, \text{V to 15,\text{V}}, -40, ^\circ\text{C} &lt; T_A &lt; +125, ^\circ\text{C}$</td>
<td>10</td>
<td>25</td>
<td></td>
<td>ppm/V</td>
</tr>
<tr>
<td>LOAD REGULATION</td>
<td>$\Delta V_O/\Delta I_{LOAD}$</td>
<td>$I_{LOAD} = 0, \text{mA to 5,\text{mA}}, -40, ^\circ\text{C} &lt; T_A &lt; +125, ^\circ\text{C}, V_{IN} = 5, \text{V}$</td>
<td>140</td>
<td></td>
<td></td>
<td>ppm/mA</td>
</tr>
<tr>
<td>QUIESCENT CURRENT</td>
<td>$I_N$</td>
<td>No load, $-40, ^\circ\text{C} &lt; T_A &lt; +125, ^\circ\text{C}$</td>
<td>120</td>
<td>140</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>VOLTAGE NOISE</td>
<td>$e_{n\text{-p-p}}$</td>
<td>0.1 Hz to 10 Hz</td>
<td>7</td>
<td></td>
<td></td>
<td>μV p-p</td>
</tr>
<tr>
<td>TURN-ON SETTLING TIME</td>
<td>$t_R$</td>
<td></td>
<td>20</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>LONG-TERM STABILITY$^1$</td>
<td>$\Delta V_O$</td>
<td>1000 hours</td>
<td>50</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>OUTPUT VOLTAGE HYSTERESIS</td>
<td>$\Delta V_{O\text{-HYS}}$</td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>RIPPLE REJECTION RATIO</td>
<td>$RRR$</td>
<td>$f_{IN} = 60, \text{Hz}$</td>
<td>–80</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>SHORT CIRCUIT TO GND</td>
<td>$I_{SC}$</td>
<td>$V_{IN} = 5, \text{V}$</td>
<td>25</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{IN} = 15, \text{V}$</td>
<td>30</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>SHUTDOWN PIN</td>
<td>$I_{SHDN}$</td>
<td>Shutdown Supply Current</td>
<td>3</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>$I_{LOGIC}$</td>
<td>Shutdown Logic Input Current</td>
<td>500</td>
<td></td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td></td>
<td>$V_{INL}$</td>
<td>Shutdown Logic Low</td>
<td>0.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>$V_{INH}$</td>
<td>Shutdown Logic High</td>
<td>2.4</td>
<td></td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

$^1$ The long-term stability specification is noncumulative. The drift of subsequent 1000 hour periods is significantly lower than in the first 1000 hour period.
### ADR395 ELECTRICAL CHARACTERISTICS

\( V_{IN} = 5.3 \) V to 15 V, \( T_A = 25^\circ C \), unless otherwise noted.

**Table 4.**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>OUTPUT VOLTAGE</td>
<td>( V_O )</td>
<td>A grade</td>
<td></td>
<td>4.994</td>
<td>5.000</td>
<td>5.006</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td></td>
<td>4.995</td>
<td>5.000</td>
<td>5.005</td>
<td>V</td>
</tr>
<tr>
<td>INITIAL ACCURACY</td>
<td>( V_{OERR} )</td>
<td>A grade</td>
<td></td>
<td>6</td>
<td>0.12</td>
<td>0.10</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>A grade</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td></td>
<td>5</td>
<td>0.10</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>TEMPERATURE COEFFICIENT</td>
<td>( TCV_O )</td>
<td>A grade, ( -40^\circ C &lt; T_A &lt; +125^\circ C )</td>
<td></td>
<td>25</td>
<td>9</td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>B grade, ( -40^\circ C &lt; T_A &lt; +125^\circ C )</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td>SUPPLY VOLTAGE HEADROOM</td>
<td>( V_{IN} - V_O )</td>
<td></td>
<td></td>
<td>300</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>LINE REGULATION</td>
<td>( \Delta V_O/\Delta V_{IN} )</td>
<td>( V_{IN} = 4.3 ) V to 15 V, ( -40^\circ C &lt; T_A &lt; +125^\circ C )</td>
<td></td>
<td>10</td>
<td>25</td>
<td></td>
<td>ppm/V</td>
</tr>
<tr>
<td>LOAD REGULATION</td>
<td>( \Delta V_O/\Delta I_{LOAD} )</td>
<td>( I_{LOAD} = 0 ) mA to 5 mA, ( -40^\circ C &lt; T_A &lt; +125^\circ C ), ( V_{IN} = 6 ) V</td>
<td></td>
<td>140</td>
<td></td>
<td></td>
<td>ppm/mA</td>
</tr>
<tr>
<td>QUIESCENT CURRENT</td>
<td>( I_{IN} )</td>
<td>No load</td>
<td></td>
<td>120 μA</td>
<td>140 μA</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(-40^\circ C &lt; T_A &lt; +125^\circ C )</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>VOLTAGE NOISE</td>
<td>( \varepsilon_{p-p} )</td>
<td>0.1 Hz to 10 Hz</td>
<td></td>
<td>8</td>
<td></td>
<td></td>
<td>μV p-p</td>
</tr>
<tr>
<td>TURN-ON SETTLING TIME</td>
<td>( t_R )</td>
<td></td>
<td></td>
<td>20</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>LONG-TERM STABILITY</td>
<td>( \Delta V_O )</td>
<td>1000 hours</td>
<td></td>
<td>50</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>OUTPUT VOLTAGE HYSTERESIS</td>
<td>( \Delta V_{O,HYS} )</td>
<td></td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>RIPPLE REJECTION RATIO</td>
<td>RRR</td>
<td>( f_{IN} = 60 ) Hz</td>
<td></td>
<td>-80</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>SHORT CIRCUIT TO GND</td>
<td>( I_{SC} )</td>
<td>( V_{IN} = 5 ) V</td>
<td></td>
<td>25</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{IN} = 15 ) V</td>
<td></td>
<td>30</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>SHUTDOWN PIN</td>
<td>( I_{SDN} )</td>
<td></td>
<td></td>
<td>3 μA</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>( I_{LOGIC} )</td>
<td></td>
<td></td>
<td>500 nA</td>
<td></td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td></td>
<td>( V_{INL} )</td>
<td></td>
<td></td>
<td>0.8 V</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>( V_{INH} )</td>
<td></td>
<td></td>
<td>2.4</td>
<td></td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

1 The long-term stability specification is noncumulative. The drift of subsequent 1000 hour periods is significantly lower than in the first 1000 hour period.
ABSOLUTE MAXIMUM RATINGS

At 25°C, unless otherwise noted.

Table 5.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>18 V</td>
</tr>
<tr>
<td>Output Short-Circuit Duration to GND</td>
<td>See derating curves</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>−65°C to +125°C</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>−40°C to +125°C</td>
</tr>
<tr>
<td>Junction Temperature Range</td>
<td>−65°C to +125°C</td>
</tr>
<tr>
<td>Lead Temperature (Soldering, 60 sec)</td>
<td>300°C</td>
</tr>
</tbody>
</table>

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

THERMAL RESISTANCE

θJA is specified for the worst-case conditions, that is, for a device soldered in a circuit board for surface-mount packages.

Table 6.

<table>
<thead>
<tr>
<th>Package Type</th>
<th>θJA</th>
<th>θJC</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>TSOT (UJ-5)</td>
<td>230</td>
<td>146</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.
TYPICAL PERFORMANCE CHARACTERISTICS

Figure 2. ADR391 Output Voltage ($V_{\text{out}}$) vs. Temperature

Figure 3. ADR392 Output Voltage ($V_{\text{out}}$) vs. Temperature

Figure 4. ADR395 Output Voltage ($V_{\text{out}}$) vs. Temperature

Figure 5. ADR391 Supply Current vs. Input Voltage

Figure 6. ADR392 Supply Current vs. Input Voltage

Figure 7. ADR395 Supply Current vs. Input Voltage
Figure 8. ADR391 Load Regulation vs. Temperature

Figure 11. ADR391 Line Regulation vs. Temperature

Figure 9. ADR392 Load Regulation vs. Temperature

Figure 12. ADR392 Line Regulation vs. Temperature

Figure 10. ADR395 Load Regulation vs. Temperature

Figure 13. ADR395 Line Regulation vs. Temperature
**Figure 14. ADR391 Minimum Input Voltage (Vin) vs. Load Current**

**Figure 15. ADR392 Minimum Input Voltage (Vin) vs. Load Current**

**Figure 16. ADR395 Minimum Input Voltage (Vin) vs. Load Current**

**Figure 17. ADR391 Vout Hysteresis Distribution**

**Figure 18. Voltage Noise Density vs. Frequency**

**Figure 19. ADR391 Typical Long-Term Drift Over 1000 Hours**
Figure 20. ADR391 Typical Voltage Noise 0.1 Hz to 10 Hz

Figure 21. ADR391 Line Transient Response

Figure 22. ADR391 Line Transient Response

Figure 23. ADR391 Voltage Noise 10 Hz to 10 kHz

Figure 24. ADR391 Load Transient Response

Figure 25. ADR391 Load Transient Response
Figure 26. ADR391 Load Transient Response

Figure 27. ADR391 Turn-On Response Time at 15 V

Figure 28. ADR391 Turn-Off Response at 15 V

Figure 29. ADR391 Turn-On/Turn-Off Response at 5 V with Capacitance

Figure 30. ADR391 Turn-On/Turn-Off Response at 5 V with Resistor Load

Figure 31. ADR391 Turn-On/Turn-Off Response at 5 V
Figure 32. Ripple Rejection vs. Frequency

Figure 33. Output Impedance vs. Frequency
**TERMINOLOGY**

**Temperature Coefficient**
The change of output voltage with respect to operating temperature changes normalized by the output voltage at 25°C. This parameter is expressed in ppm/°C and can be determined by

\[
TCV_o[\text{ppm/°C}] = \frac{V_o(T_2) - V_o(T_1)}{V_o(25°C) \times (T_2 - T_1)} \times 10^6
\]  

(1)

where:
- \( V_o(25°C) \) is \( V_o \) at 25°C.
- \( V_o(T_1) \) is \( V_o \) at Temperature 1.
- \( V_o(T_2) \) is \( V_o \) at Temperature 2.

**Line Regulation**
The change in output voltage due to a specified change in input voltage. This parameter accounts for the effects of self-heating. Line regulation is expressed in either percent per volt, parts-per-million per volt, or microvolts per volt change in input voltage.

**Load Regulation**
The change in output voltage due to a specified change in load current. This parameter accounts for the effects of self-heating. Load regulation is expressed in either microvolts per milliampere, parts-per-million per milliampere, or ohms of dc output resistance.

**Long-Term Stability**
Typical shift of output voltage at 25°C on a sample of parts subjected to a test of 1000 hours at 25°C.

\[
\Delta V_o = V_o(t_0) - V_o(t_1)
\]

\[
\Delta V_o[\text{ppm}] = \left( \frac{V_o(t_0) - V_o(t_1)}{V_o(t_0)} \times 10^6 \right)
\]

(2)

where:
- \( V_o(t_0) \) is \( V_o \) at 25°C at Time 0.
- \( V_o(t_1) \) is \( V_o \) at 25°C after 1000 hours operation at 25°C.

**Thermally Induced Output Voltage Hysteresis**
The change of output voltage after the device cycles through the temperatures from +25°C to −40°C to +125°C and back to +25°C. This is a typical value from a sample of parts put through such a cycle.

\[
V_{o,HYS} = V_o(25°C) - V_o,TC
\]

(3)

\[
V_{o,HYS}[\text{ppm}] = \frac{V_o(25°C) - V_o,TC}{V_o(25°C)} \times 10^6
\]

(4)

where:
- \( V_o(25°C) \) is \( V_o \) at 25°C.
- \( V_o,TC \) is \( V_o \) at 25°C after a temperature cycle from +25°C to −40°C to +125°C and back to +25°C.
THEORY OF OPERATION

Band gap references are the high performance solution for low supply voltage and low power voltage reference applications, and the ADR391/ADR392/ADR395 are no exception. The uniqueness of these devices lies in the architecture. As shown in Figure 34, the ideal zero TC band gap voltage is referenced to the output, not to ground. Therefore, if noise exists on the ground line, it is greatly attenuated on VOUT. The band gap cell consists of the PNP pair, Q51 and Q52, running at unequal current densities. The difference in VBE results in a voltage with a positive TC, which is amplified by a ratio of

\[ 2 \times \frac{R_{58}}{R_{54}} \]

This PTAT voltage, combined with VBEs of Q51 and Q52, produces a stable band gap voltage.

Reduction in the band gap curvature is performed by the ratio of Resistors R44 and R59, one of which is linearly temperature dependent. Precision laser trimming and other patented circuit techniques are used to further enhance the drift performance.

 DEVICE POWER DISSIPATION CONSIDERATIONS

The ADR391/ADR392/ADR395 are capable of delivering load currents to 5 mA, with an input voltage that ranges from 2.8 V (ADR391 only) to 15 V. When these devices are used in applications with large input voltages, care should be taken to avoid exceeding the specified maximum power dissipation or junction temperature because it could result in premature device failure. The following formula should be used to calculate the maximum junction temperature or dissipation of the device:

\[ P_D = \frac{T_J - T_A}{\theta_{JA}} \]  

(5)

where:

- \( T_J \) and \( T_A \) are, respectively, the junction and ambient temperatures.
- \( P_D \) is the device power dissipation.
- \( \theta_{JA} \) is the device package thermal resistance.

SHUTDOWN MODE OPERATION

The ADR391/ADR392/ADR395 include a shutdown feature that is TTL/CMOS level compatible. A logic low or a 0 V condition on the SHDN pin is required to turn the devices off. During shutdown mode, the output of the reference becomes a high impedance state, where its potential is determined by external circuitry. If the ADR39x is powered on with the SHDN pin held low during power on, one of the following conditions must be met:

- Capacitor placed between VIN and SHDN as shown in Figure 35, or
- Low pass filter the input as shown in Figure 36, or
- \( \geq 200 \text{ ms} \) power supply ramp rate to VIN.

If the shutdown feature is not used, the SHDN pin must be connected to VIN (Pin 2).
APPLICATIONS INFORMATION
BASIC VOLTAGE REFERENCE CONNECTION

The circuit shown in Figure 37 illustrates the basic configuration for the ADR39x family. Decoupling capacitors are not required for circuit stability. The ADR39x family is capable of driving capacitive loads from 0 µF to 10 µF. However, a 0.1 µF ceramic output capacitor is recommended to absorb and deliver the charge, as required by a dynamic load.

Figure 37. Basic Configuration for the ADR39x Family

Stacking Reference ICs for Arbitrary Outputs

Some applications may require two reference voltage sources, which are a combined sum of standard outputs. Figure 38 shows how this stacked output reference can be implemented.

Figure 38. Stacking Voltage References with the ADR391/ADR392/ADR395

<table>
<thead>
<tr>
<th>U1/U2</th>
<th>VOUT1 (V)</th>
<th>VOUT2 (V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADR391/ADR391</td>
<td>2.5</td>
<td>5.0</td>
</tr>
<tr>
<td>ADR392/ADR392</td>
<td>4.096</td>
<td>8.192</td>
</tr>
<tr>
<td>ADR395/ADR395</td>
<td>5</td>
<td>10</td>
</tr>
</tbody>
</table>

Two reference ICs are used, fed from an unregulated input, V_IN. The outputs of the individual ICs are connected in series, which provide two output voltages, V_OUT1 and V_OUT2. V_OUT1 is the terminal voltage of U1, while V_OUT2 is the sum of this voltage and the terminal voltage of U2. U1 and U2 are chosen for the two voltages that supply the required outputs (see the Output Table in Figure 38). For example, if both U1 and U2 are ADR391s, V_OUT1 is 2.5 V and V_OUT2 is 5.0 V.

While this concept is simple, a precaution is required. Because the lower reference circuit must sink a small bias current from U2 plus the base current from the series PNP output transistor in U2, either the external load of U1 or an external resistor must provide a path for this current. If the U1 minimum load is not well defined, the external resistor should be used and set to a value that conservatively passes 600 µA of current with the applicable V_OUT1 across it. Note that the two U1 and U2 reference circuits are treated locally as macrocells; each has its own bypasses at input and output for best stability. Both U1 and U2 in this circuit can source dc currents up to their full rating. The minimum input voltage, V_IN, is determined by the sum of the outputs, V_OUT2, plus the dropout voltage of U2.

A Negative Precision Reference without Precision Resistors

A negative reference can be easily generated by adding an A1 op amp and is configured as shown in Figure 39. V_OUT (FORCE) and V_OUT (SENSE) are at virtual ground and, therefore, the negative reference can be taken directly from the output of the op amp. The op amp must be dual-supply, low offset, and rail-to-rail if the negative supply voltage is close to the reference output.

Figure 39. Negative Reference
**General-Purpose Current Source**

Many times in low power applications, the need arises for a precision current source that can operate on low supply voltages. The ADR391/ADR392/ADR395 can be configured as a precision current source. As shown in Figure 40, the circuit configuration is a floating current source with a grounded load. The reference output voltage is bootstrapped across RSET, which sets the output current into the load. With this configuration, circuit precision is maintained for load currents in the range from the reference supply current, typically 90 μA to approximately 5 mA.

![Figure 40. A General-Purpose Current Source](image)

**High Power Performance with Current Limit**

In some cases, the user may want higher output current delivered to a load and still achieve better than 0.5% accuracy out of the ADR39x. The accuracy for a reference is normally specified on the data sheet with no load. However, the output voltage changes with load current.

The circuit shown in Figure 41 provides high current without compromising the accuracy of the ADR39x. The series pass transistor, Q1, provides up to 1 A load current. The ADR39x delivers only the base drive to Q1 through the force pin. The sense pin of the ADR39x is a regulated output and is connected to the load.

The Transistor Q2 protects Q1 during short-circuit limit faults by robbing its base drive. The maximum current is

\[ I_{\text{LMAX}} \approx 0.6 \text{ V}/R_S \]  

(6)

![Figure 41. ADR39x for High Power Performance with Current Limit](image)

A similar circuit function can also be achieved with the Darlington transistor configuration, as shown in Figure 42.

![Figure 42. ADR39x for High Output Current with Darlington Drive Configuration](image)
CAPACITORS

Input Capacitor
Input capacitors are not required on the ADR39x. There is no limit for the value of the capacitor used on the input, but a 1 μF to 10 μF capacitor on the input improves transient response in applications where the supply suddenly changes. An additional 0.1 μF in parallel also helps reduce noise from the supply.

Output Capacitor
The ADR39x does not require output capacitors for stability under any load condition. An output capacitor, typically 0.1 μF, filters out any low level noise voltage and does not affect the operation of the part. On the other hand, the load transient response can improve with the addition of a 1 μF to 10 μF output capacitor in parallel. A capacitor here acts as a source of stored energy for a sudden increase in load current. The only parameter that degrades by adding an output capacitor is the turn-on time, and it depends on the size of the capacitor chosen.
OUTLINE DIMENSIONS

Figure 43. 5-Lead Thin Small Outline Transistor Package [TSOT] (UJ-5)
Dimensions shown in millimeters

ORDERING GUIDE

<table>
<thead>
<tr>
<th>Model1,2</th>
<th>Output Voltage (V_O)</th>
<th>Initial Accuracy (mV)</th>
<th>Temperature Coefficient (ppm/°C)</th>
<th>Package Description</th>
<th>Package Option</th>
<th>Marking Code</th>
<th>Ordering Quantity</th>
<th>Temperature Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADR391AUJZ-REEL7</td>
<td>2.5</td>
<td>±6</td>
<td>0.24</td>
<td>25</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>R1A</td>
<td>3000</td>
</tr>
<tr>
<td>ADR391AUJZ-R2</td>
<td>2.5</td>
<td>±6</td>
<td>0.24</td>
<td>25</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>R1A</td>
<td>250</td>
</tr>
<tr>
<td>ADR391BUJZ-REEL7</td>
<td>2.5</td>
<td>±4</td>
<td>0.16</td>
<td>9</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>R1B</td>
<td>3000</td>
</tr>
<tr>
<td>ADR392AUJZ-REEL7</td>
<td>4.096</td>
<td>±6</td>
<td>0.15</td>
<td>25</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>RCA</td>
<td>3000</td>
</tr>
<tr>
<td>ADR392BUJZ-REEL7</td>
<td>4.096</td>
<td>±5</td>
<td>0.12</td>
<td>9</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>RCB</td>
<td>3000</td>
</tr>
<tr>
<td>ADR392WBUJZ-R7</td>
<td>4.096</td>
<td>±5</td>
<td>0.12</td>
<td>9</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>RCB</td>
<td>3000</td>
</tr>
<tr>
<td>ADR395AUJZ-REEL7</td>
<td>5.0</td>
<td>±6</td>
<td>0.12</td>
<td>25</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>RDA</td>
<td>3000</td>
</tr>
<tr>
<td>ADR395BUJZ-REEL7</td>
<td>5.0</td>
<td>±5</td>
<td>0.10</td>
<td>9</td>
<td>5-Lead TSOT</td>
<td>UJ-5</td>
<td>RDB</td>
<td>3000</td>
</tr>
</tbody>
</table>

1 Z = RoHS Compliant Part.
2 The ADR392WBUJZ-R7 is an automotive grade model.