3.3 V Supply, Voltage Output Temperature Sensor with Signal Conditioning

AD22103*

FEATURES
3.3 V, Single Supply Operation
Temperature Coefficient of 28 mV/°C
100°C Temperature Span (0°C to +100°C)
Accuracy Better Than 2.5% of Full Scale
Linearity Better Than 0.5% of Full Scale
Output Proportional to Temperature × V_S
Minimal Self-Heating
High Level, Low Impedance Output
Reverse Supply Protected

APPLICATIONS
Microprocessor Thermal Management
Battery and Low Powered Systems
Power Supply Temperature Monitoring
System Temperature Compensation
Board Level Temperature Sensing

MARKETS
Computers
Portable Electronic Equipment
Industrial Process Control
Instrumentation

GENERAL DESCRIPTION
The AD22103 is a monolithic temperature sensor with on-chip signal conditioning. It can be operated over the temperature range 0°C to +100°C, making it ideal for use in numerous 3.3 V applications.

The signal conditioning eliminates the need for any trimming, buffering or linearization circuitry, greatly simplifying the system design and reducing the overall system cost.

The output voltage is proportional to the temperature times the supply voltage (ratiometric). The output swings from 0.25 V at 0°C to +3.05 V at +100°C using a single +3.3 V supply.

Due to its ratiometric nature, the AD22103 offers a cost effective solution when interfacing to an analog-to-digital converter. This is accomplished by using the ADC’s power supply as a reference to both the ADC and the AD22103 (See Figure 1), eliminating the need for and cost of a precision reference.

*Protected by U.S. Patent No. 5243319.

Figure 1. Application Circuit
AD22103—SPECIFICATIONS

(\(T_A = +25^\circ C\) and \(V_s = +2.7 V\) to +3.6 V unless otherwise noted)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>AD22103K Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>TRANSFER FUNCTION</td>
<td>(V_{OUT} = (V_s/3.3 V) \times [0.25 V + (28 \text{ mV/}^\circ C) \times T_A])</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>TEMPERATURE COEFFICIENT</td>
<td>((V_s/3.3 V) \times 28)</td>
<td>mV/(^\circ C)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>TOTAL ERROR</td>
<td>±0.5</td>
<td>±2.0</td>
<td></td>
<td>℃</td>
</tr>
<tr>
<td>Initial Error</td>
<td>(T_A = +25^\circ C)</td>
<td>±0.75</td>
<td>±2.5</td>
<td>℃</td>
</tr>
<tr>
<td>Error over Temperature</td>
<td>(T_A = T_{MIN} \text{ to } T_{MAX})</td>
<td>±0.75</td>
<td>±2.5</td>
<td>℃</td>
</tr>
<tr>
<td>Nonlinearity</td>
<td>(T_A = T_{MIN} \text{ to } T_{MAX})</td>
<td>0.1</td>
<td>0.5</td>
<td>% FS(^1)</td>
</tr>
<tr>
<td>OUTPUT CHARACTERISTICS</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Nominal Output Voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(V_s = 3.3 V, T_A = 0^\circ C)</td>
<td>0.25</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>(V_s = 3.3 V, T_A = +25^\circ C)</td>
<td>0.95</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>(V_s = 3.3 V, T_A = +100^\circ C)</td>
<td>3.05</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>POWER SUPPLY</td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>+2.7</td>
<td>+3.3</td>
<td>+3.6</td>
<td>V</td>
</tr>
<tr>
<td>Quiescent Current</td>
<td>350</td>
<td>500</td>
<td>600</td>
<td>µA</td>
</tr>
<tr>
<td>TEMPERATURE RANGE</td>
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<td></td>
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</tr>
<tr>
<td>Guaranteed Temperature Range</td>
<td>0</td>
<td>+100</td>
<td>+100</td>
<td>℃</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>0</td>
<td>+100</td>
<td>+100</td>
<td>℃</td>
</tr>
<tr>
<td>PACKAGE</td>
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<td></td>
<td></td>
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</tr>
<tr>
<td>TO-92</td>
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<td></td>
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</tr>
<tr>
<td>SOIC</td>
<td></td>
<td></td>
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</tbody>
</table>

NOTES

\(^1\)FS (Full Scale) is defined as that of the operating temperature range, 0°C to +100°C. The listed max specification limit applies to the guaranteed temperature range.

For example, the AD22103K has a nonlinearity of \((0.5\%) \times (100^\circ C) = 0.5^\circ C\) over the guaranteed temperature range of 0°C to +100°C.

Specifications subject to change without notice.
ABSOLUTE MAXIMUM RATINGS*
Supply Voltage ........................................... +10 V
Reversed Continuous Supply Voltage ............... –10 V
Operating Temperature ................................. 0°C to +100°C
Storage Temperature ................................... –65°C to +160°C
Output Short Circuit to V_S or Ground ............... Indefinite
Lead Temperature (Soldering, 10 sec) ............... +300°C

*Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only; the functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ORDERING GUIDE

<table>
<thead>
<tr>
<th>Model/Grade</th>
<th>Guaranteed Temperature Range</th>
<th>Package Description</th>
<th>Package Option</th>
</tr>
</thead>
<tbody>
<tr>
<td>AD22103KRZ</td>
<td>0°C to +100°C</td>
<td>SOIC_N</td>
<td>R-8</td>
</tr>
<tr>
<td>AD22103KRZ-R7</td>
<td>0°C to +100°C</td>
<td>SOIC_N</td>
<td>R-8</td>
</tr>
<tr>
<td>AD22103KTZ</td>
<td>0°C to +100°C</td>
<td>TO-92</td>
<td>T-3-1</td>
</tr>
</tbody>
</table>

\[Z = \text{RoHS Compliant Part.}\]

CAUTION
ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the AD22103 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

Typical Performance Curves

Figure 2. Thermal Response vs. Air Flow Rate
Figure 3. Thermal Resistance vs. Air Flow Rate
**AD22103**

**THEORY OF OPERATION**

The AD22103 is a ratiometric temperature sensor IC whose output voltage is proportional to power supply voltage. The heart of the sensor is a proprietary temperature-dependent resistor, similar to an RTD, which is built into the IC. Figure 4 shows a simplified block diagram of the AD22103.

![Figure 4. Simplified Block Diagram](image)

The temperature-dependent resistor, labeled $R_T$, exhibits a change in resistance that is nearly linearly proportional to temperature. This resistor is excited with a current source that is proportional to power supply voltage. The resulting voltage across $R_T$ is therefore both supply voltage proportional and linearly varying with temperature. The remainder of the AD22103 consists of an op amp signal conditioning block that takes the voltage across $R_T$ and applies the proper gain and offset to achieve the following output voltage function:

$$V_{OUT} = \left(\frac{V_S}{3.3} V\right) \times \left[0.25 V + (28.0 \text{ mV/°C}) \times T_A\right]$$

**ABSOLUTE ACCURACY AND NONLINEARITY SPECIFICATIONS**

Figure 5 graphically depicts the guaranteed limits of accuracy for the AD22103 and shows the performance of a typical part. As the output is very linear, the major sources of error are offset, i.e., error at room temperature, and span error, i.e., deviation from the theoretical 28.0 mV/°C. Demanding applications can achieve improved performance by calibrating these offset and gain errors so that only the residual nonlinearity remains as a source of error.

![Figure 5. Typical AD22103 Performance](image)

**OUTPUT STAGE CONSIDERATIONS**

As previously stated, the AD22103 is a voltage output device. A basic understanding of the nature of its output stage is useful for proper application. Note that at the nominal supply voltage of 3.3 V, the output voltage extends from 0.25 V at 0°C to +3.05 V at +100°C. Furthermore, the AD22103 output pin is capable of withstanding an indefinite short circuit to either ground or the power supply. These characteristics are provided by the output stage structure shown in Figure 6.

![Figure 6. Output Stage Structure](image)

The active portion of the output stage is a PNP transistor with its emitter connected to the $V_S$ supply and collector connected to the output node. This PNP transistor sources the required amount of output current. A limited pull-down capability is provided by a fixed current sink of about –100 µA. Here, “fixed” means the current sink is fairly insensitive to either supply voltage or output loading conditions. The current sink capability is a function of temperature, increasing its pull-down capability at lower temperatures.

Due to its limited current sinking ability, the AD22103 is incapable of driving loads to the $V_S$ power supply and is instead intended to drive grounded loads. A typical value for short circuit current limit is 7 mA, so devices can reliably source 1 mA or 2 mA. However, for best output voltage accuracy and minimal internal self-heating, output current should be kept below 1 mA. Loads connected to the $V_S$ power supply should be avoided as the current sinking capability of the AD22103 is very limited. These considerations are typically not a problem when driving a microcontroller analog to digital converter input pin (see MICROPROCESSOR A/D INTERFACE ISSUES).

**MOUNTING CONSIDERATIONS**

If the AD22103 is thermally attached and properly protected, it can be used in any measuring situation where the maximum range of temperatures encountered is between 0°C and +100°C. Because plastic IC packaging technology is employed, excessive mechanical stress must be avoided when fastening the device with a clamp or screw-on heat tab. Thermally conductive epoxy or glue is recommended for typical mounting conditions. In wet or corrosive environments, an electrically isolated metal or ceramic well should be used to shield the AD22103. Because the part has a voltage output (as opposed to current), it offers modest immunity to leakage errors, such as those caused by conden- sation at low temperatures.
THERMAL ENVIRONMENT EFFECTS
The thermal environment in which the AD22103 is used determines two performance traits: the effect of self-heating on accuracy and the response time of the sensor to rapid changes in temperature. In the first case, a rise in the IC junction temperature above the ambient temperature is a function of two variables; the power consumption of the AD22103 and the thermal resistance between the chip and the ambient environment \( \theta_{JA} \). Self-heating error in degrees Celsius can be derived by multiplying the power dissipation by \( \theta_{JA} \). Because errors of this type can vary widely for surroundings with different heat sinking capacities, it is necessary to specify \( \theta_{JA} \) under several conditions. Table I shows how the magnitude of self-heating error varies relative to the environment. A typical part will dissipate about 1.5 mW at room temperature with a 3.3 V supply and negligible output loading. In still air, without a “heat sink,” the table below indicates a \( \theta_{JA} \) of 190°C/W, yielding a temperature rise of 0.285°C. Thermal rise will be considerably less in either moving air or with direct physical connection to a solid (or liquid) body.

<table>
<thead>
<tr>
<th>Medium</th>
<th>( \theta_{JA} ) (°C/Watt)</th>
<th>( \tau ) (sec)*</th>
</tr>
</thead>
<tbody>
<tr>
<td>Aluminum Block</td>
<td>60</td>
<td>2</td>
</tr>
<tr>
<td>Moving Air**</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Without Heat Sink</td>
<td>75</td>
<td>3.5</td>
</tr>
<tr>
<td>Still Air</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Without Heat Sink</td>
<td>190</td>
<td>15</td>
</tr>
</tbody>
</table>

*The time constant \( \tau \) is defined as the time to reach 63.2% of the final temperature change.

**1200 CFM.

Response of the AD22103 output to abrupt changes in ambient temperature can be modeled by a single time constant \( \tau \) exponential function. Figure 7 shows typical response time plots for a few media of interest.

**Figure 7. Response Time**

The time constant \( \tau \) is dependent on \( \theta_{JA} \) and the specific heat capacities of the chip and the package. Table I lists the effective \( \tau \) (time to reach 63.2% of the final value) for a few different media. Copper printed circuit board connections were neglected in the analysis; however, they will sink or conduct heat directly through the AD22103’s solder plated copper leads. When faster response is required, a thermally conductive grease or glue between the AD22103 and the surface temperature being measured should be used.

MICROPROCESSOR A/D INTERFACE ISSUES
The AD22103 is especially well suited to providing a low cost temperature measurement capability for microprocessor/ microcontroller based systems. Many inexpensive 8-bit microprocessors now offer an onboard 8-bit ADC capability at a modest cost premium. Total “cost of ownership” then becomes a function of the voltage reference and analog signal conditioning necessary to mate the analog sensor with the microprocessor ADC. The AD22103 can provide an ideal low cost system by eliminating the need for a precision voltage reference and any additional active components. The ratiometric nature of the AD22103 allows the microprocessor to use the same power supply as its ADC reference. Variations of hundreds of millivolts in the supply voltage have little effect as both the AD22103 and the ADC use the supply as their reference. The nominal AD22103 signal range of 0.25 V to 3.05 V (0°C to +100°C) makes good use of the input range of a 0 V to 3.3 V ADC. A single resistor and capacitor are recommended to provide immunity to the high speed charge dump glitches seen at many microprocessor ADC inputs (see Figure 1).

An 8-bit ADC with a reference of 3.3 V will have a least significant bit (LSB) size of 3.3 V/256 = 12.9 mV. This corresponds to a nominal resolution of about 0.46°C/bit.

USE WITH A PRECISION REFERENCE AS THE SUPPLY VOLTAGE
While the ratiometric nature of the AD22103 allows for system operation without a precision voltage reference, it can still be used in such systems. Overall system requirements involving other sensors or signal inputs may dictate the need for a fixed precision ADC reference. The AD22103 can be converted to absolute voltage operation by using a precision reference as the supply voltage. For example, a 3.3 V reference can be used to power the AD22103 directly. Supply current will typically be 500 µA which is usually within the output capability of the reference. A large number of AD22103s may require an additional op amp buffer, as would scaling down a 10.00 V reference that might be found in “instrumentation” ADCs typically operating from ±15 V supplies.

USING THE AD22103 WITH ALTERNATIVE SUPPLY VOLTAGES
Because of its ratiometric nature the AD22103 can be used at other supply voltages. Its nominal transfer function can be calculated based on the new supply voltage. For instance, if using the AD22103 at \( V_S = 5 \) V the transfer function would be given by:

\[
V_O = \frac{V_S}{5} \left( 0.25 V + \frac{28 \text{ mV}}{\circ C} \times T_A \right)
\]

\[
V_O = \frac{V_S}{5} \left( 0.378 V + \frac{42.42 \text{ mV}}{\circ C} \times T_A \right)
\]
OUTLINE DIMENSIONS

3-Pin Plastic Header-Style Package [TO-92]
(T-3-1)
Dimensions shown in inches and (millimeters)

8-Lead Standard Small Outline Package [SOIC_N]
Narrow Body
(R-8)
Dimensions shown in millimeters and (inches)

COMPLIANT TO JEDEC STANDARDS TO-226-AA
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS
(IN PARENTHESES) ARE ROUNDED-OFF EQUIVALENTS FOR
REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

COMPLIANT TO JEDEC STANDARDS MS-012-AA
CONTROLLING DIMENSIONS ARE IN MILLIMETERS; INCH DIMENSIONS
(IN PARENTHESES) ARE ROUNDED-OFF MILLIMETER EQUIVALENTS FOR
REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.
REVISION HISTORY

7/2017—Rev. A to Rev. B
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Changes to Ordering Guide ...................................................... 3

8/2016—Rev. 0 to Rev. A
Changes to Ordering Guide ...................................................... 3
Updated Outline Dimensions .................................................... 6

3/1995—Revision 0: Initial Version