The LTC®4096/LTC4096X are standalone linear chargers that are capable of charging a single-cell Li-Ion or Li-Polymer battery from both wall adapter and USB inputs. The chargers can detect power at the inputs and automatically select the appropriate power source for charging.

No external sense resistor or blocking diode is required for charging due to the internal MOSFET architecture. Internal thermal feedback regulates the battery charge current to maintain a constant die temperature during high power operation or high ambient temperature conditions. The float voltage is fixed at 4.2V and the charge current is programmed with an external resistor. The LTC4096 terminates the charge cycle when the charge current drops below the user programmed termination threshold after the final float voltage is reached. The LTC4096 can be put into shutdown mode reducing the DCIN supply current to 20µA, the USBIN supply current to 10µA, and the battery drain current to less than 2µA even with power applied to both inputs.

Other features include trickle charge (LTC4096 only), automatic recharge, undervoltage lockout, charge status output and power present output with 120mA drive capability.

**Applications**

- Cellular Telephones
- MP3 Players
- Portable Handheld Devices

**Typical Application**

Dual Input Battery Charger for Single-Cell Li-Ion Battery

![Typical Application Diagram](image-url)

**Complete Charge Cycle (1100mAh Battery)**

![Charge Cycle Graph](image-url)
**ABSOLUTE MAXIMUM RATINGS**

(Note 1,7)

\( V_{DCIN}, V_{USBIN} \)
- \( t < 1 \text{ms} \) and Duty Cycle < 1% .............. –0.3V to 7V
- Steady State............................................. –0.3V to 6V

BAT, CHRG, SUSP............................................. –0.3V to 6V

IDC, IUSB, ITERM ........................................... –0.3V to VCC + 0.3V

BAT Short-Circuit Duration.........................Continuous

PWR Short-Circuit Duration..........................Continuous

BAT, DCIN Pin Current (Note 6)......................1.25A

USBIN Pin Current (Note 6)...........................1.1A

IDC, IUSB, ITERM Pin Current.........................1.25mA

Junction Temperature ..................................... 125°C

Operating Temperature Range (Note 2) ... –40°C to 85°C

Storage Temperature Range ...................... –65°C to 125°C

\( V_{DCIN} \) and \( V_{USBIN} \)
- 4.25V to 5.5V

\( I_{DCIN} \) and \( I_{USBIN} \)
- 250 \( \mu A \) to 800 \( \mu A \)

**ELECTRICAL CHARACTERISTICS**

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at \( T_A = 25°C \). \( V_{DCIN} = 5V, V_{USBIN} = 5V, R_{IDC} = 1k\Omega, R_{IUSB} = 2k\Omega, R_{ITERM} = 2k\Omega \) unless otherwise noted.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{DCIN} )</td>
<td>Adapter Supply Voltage</td>
<td>●</td>
<td>4.25</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>( V_{USBIN} )</td>
<td>USB Supply Voltage</td>
<td>●</td>
<td>4.25</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>( I_{DCIN} )</td>
<td>DCIN Supply Current</td>
<td>Charge Mode (Note 4), ( R_{DC} = 10k )</td>
<td>●</td>
<td>250</td>
<td>800</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standby Mode; Charge Terminated</td>
<td></td>
<td>50</td>
<td>100</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Shutdown Mode (SUSP = 5V)</td>
<td></td>
<td>20</td>
<td>40</td>
<td>( \mu A )</td>
</tr>
<tr>
<td>( I_{USBIN} )</td>
<td>USBIN Supply Current</td>
<td>Charge Mode (Note 5), ( R_{IUSB} = 10k, V_{DCIN} = 0V )</td>
<td>●</td>
<td>250</td>
<td>800</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standby Mode; Charge Terminated, ( V_{DCIN} = 0V )</td>
<td></td>
<td>50</td>
<td>100</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Shutdown (( V_{DCIN} = 0V, SUSP = 5V ))</td>
<td></td>
<td>20</td>
<td>40</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{DCIN} &gt; V_{USBIN} )</td>
<td></td>
<td>10</td>
<td>20</td>
<td>( \mu A )</td>
</tr>
<tr>
<td>( V_{FLOAT} )</td>
<td>Regulated Output (Float) Voltage</td>
<td>( I_{BAT} = 1mA )</td>
<td>4.179</td>
<td>4.2</td>
<td>4.221</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( I_{BAT} = 1mA, 0°C \leq T_A \leq 85°C )</td>
<td>4.158</td>
<td>4.2</td>
<td>4.242</td>
<td>V</td>
</tr>
<tr>
<td>( I_{BAT} )</td>
<td>BAT Pin Current</td>
<td>( R_{IDC} = 1.25k, ) Constant-Current Mode</td>
<td>750</td>
<td>800</td>
<td>850</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( R_{IUSB} = 2.1k, ) Constant-Current Mode</td>
<td>450</td>
<td>476</td>
<td>500</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( R_{IDC} = 10k ) or ( R_{IUSB} = 10k )</td>
<td>88</td>
<td>100</td>
<td>112</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standby Mode, Charge Terminated</td>
<td>–5</td>
<td>–8</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Shutdown Mode (Charger Disabled)</td>
<td>–2</td>
<td>–4</td>
<td>( \mu A )</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Sleep Mode (( V_{DCIN} = 0V, V_{USBIN} = 0V ))</td>
<td>–5</td>
<td>–8</td>
<td>( \mu A )</td>
<td></td>
</tr>
<tr>
<td>( V_{IDC} )</td>
<td>IDC Pin Regulated Voltage</td>
<td>Constant-Current Mode, ( R_{IDC} = 1.25k )</td>
<td>1</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>( V_{IUSB} )</td>
<td>IUSB Pin Regulated Voltage</td>
<td>Constant-Current Mode, ( R_{IUSB} = 2k )</td>
<td>1</td>
<td>V</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$. $V_{DCIN} = 5V$, $V_{USBIN} = 5V$, $R_{IDC} = 1k\Omega$, $R_{IUSB} = 2k\Omega$, $R_{ILERM} = 2k\Omega$ unless otherwise noted.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
</table>
| $I_{TERMINATE}$ | Charge Current Termination Threshold | $R_{TERMINATE} = 1k$  
$R_{TERMINATE} = 2k$  
$R_{TERMINATE} = 10k$ | 88   | 100  | 112  | mA    |
| $I_{TERMINATE}$ | Trickle Charge Current (LTC4096 Only)     | $V_{BAT} < V_{TERMINATE}$  
$R_{TERMINATE} = 1k$  
$V_{BAT} < V_{TERMINATE}$  
$R_{TERMINATE} = 2k$ | 85   | 100  | 115  | mA    |
| $I_{TRIKL}$ | Trickle Charge Threshold Voltage (LTC4096 Only) | $V_{BAT}$ Rising  
Hysteresis | 2.8  | 2.9  | 3    | V     |
| $V_{UVDC}$  | DCIN Undervoltage Lockout Voltage         | From Low to High  
Hysteresis | 4    | 4.22 | 4.4  | V     |
| $V_{UVUSB}$ | USBIN Undervoltage Lockout Voltage        | From Low to High  
Hysteresis | 3.8  | 4    | 4.2  | V     |
| $V_{ASD-DC}$ | $V_{DCIN} - V_{BAT}$ Lockout Threshold Voltage | $V_{DCIN}$ from High to Low, $V_{BAT} = 4.3V$  
$V_{DCIN}$ from Low to High, $V_{BAT} = 4.3V$ | 5    | 30   | 55   | mV    |
| $V_{ASD-USB}$ | $V_{USBIN} - V_{BAT}$ Lockout Threshold Voltage | $V_{USBIN}$ from High to Low, $V_{BAT} = 4.3V$  
$V_{USBIN}$ from Low to High, $V_{BAT} = 4.3V$ | 5    | 30   | 55   | mV    |
| $V_{SUSP}$ | $V_{IL}$, Logic Low Voltage | 0.5 | V     |
| $R_{SUSP}$ | SUSP PulldownResistance | 1.2 | V     |
| $I_{CHRG}$ | CHRG Output Low Voltage | $I_{CHRG} = 5mA$ | 1.3  | 3.4  | 7    | MΩ    |
| $AV_{RECHRG}$ | Recharge Battery Threshold Voltage | $V_{FLOAT} - V_{RECHRG}$ | 62   | 50   | 150  | mV    |
| $t_{RECHRG}$ | Recharge Comparator Filter Time | $V_{BAT}$ from High to Low | 1.6  | 1.6  | 8    | ms    |
| $R_{ON-DC}$ | Power FET “ON” Resistance (Between DCIN and BAT) | $I_{BAT}$ Drops Below Termination Threshold | 3    | 3    |      | ms    |
| $R_{ON-USB}$ | Power FET “ON” Resistance (Between USBIN and BAT) | $V_{DCIN} = 5V$, $V_{USBIN} = 0V$ | 420  |      |      | mΩ    |
| $R_{DC-PWR}$ | Power FET “ON” Resistance (Between DCIN and PWR) | $V_{DCIN} = 5V$, $V_{USBIN} = 0V$ | 470  |      |      | mΩ    |
| $R_{USB-PWR}$ | Power FET “ON” Resistance (Between USBIN and PWR) | $V_{DCIN} = 0V$, $V_{USBIN} = 5V$ | 15   |      |      | Ω     |
| $T_{LIM}$  | Junction Temperature in Constant-Temperature Mode | 115 | °C    |

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LTC4096 is guaranteed to meet the performance specifications from 0°C to 85°C. Specifications over the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

**Note 3:** Failure to correctly solder the Exposed Pad of the package to the PC board will result in a thermal resistance much higher than 40°C/W. See Thermal Considerations.

**Note 4:** Supply current includes IDC and ITERM pin current (approximately 100µA each) but does not include any current delivered to the battery through the BAT pin.

**Note 5:** Supply current includes IUSB and ITERM pin current (approximately 100µA each) but does not include any current delivered to the battery through the BAT pin.

**Note 6:** Guaranteed by long term current density limitations.

**Note 7:** $V_{DC}$ is greater of DCIN or USBIN.
**TYPICAL PERFORMANCE CHARACTERISTICS**  
$T_A = 25^\circ C$, unless otherwise noted.

**Battery Regulated Output (Float)**  
- **Voltage vs Charge Current**
- **Voltage vs DCIN Voltage**
- **Voltage vs USBIN Voltage**
- **Charge Current vs IDC Pin Voltage**
- **IDC Pin Voltage vs Temperature** (Constant-Current Mode)
- **IUSB Pin Voltage vs Temperature** (Constant-Current Mode)

**Charge Current vs IUSB Pin Voltage**

---

**Battery Regulated Output (Float)**  
- **Voltage vs Charge Current**
- **Voltage vs DCIN Voltage**
- **Voltage vs USBIN Voltage**
- **Charge Current vs IDC Pin Voltage**
- **IDC Pin Voltage vs Temperature** (Constant-Current Mode)
- **IUSB Pin Voltage vs Temperature** (Constant-Current Mode)

**Charge Current vs IUSB Pin Voltage**

---

**Battery Regulated Output (Float)**  
- **Voltage vs Charge Current**
- **Voltage vs DCIN Voltage**
- **Voltage vs USBIN Voltage**
- **Charge Current vs IDC Pin Voltage**
- **IDC Pin Voltage vs Temperature** (Constant-Current Mode)
- **IUSB Pin Voltage vs Temperature** (Constant-Current Mode)

**Charge Current vs IUSB Pin Voltage**

---

**Battery Regulated Output (Float)**  
- **Voltage vs Charge Current**
- **Voltage vs DCIN Voltage**
- **Voltage vs USBIN Voltage**
- **Charge Current vs IDC Pin Voltage**
- **IDC Pin Voltage vs Temperature** (Constant-Current Mode)
- **IUSB Pin Voltage vs Temperature** (Constant-Current Mode)

**Charge Current vs IUSB Pin Voltage**
TYPICAL PERFORMANCE CHARACTERISTICS  \( T_A = 25^\circ C \), unless otherwise noted.
TYPICAL PERFORMANCE CHARACTERISTICS

$T_A = 25^\circ C$, unless otherwise noted.

**DCIN Power FET On-Resistance vs Temperature**

$V_{DCIN} = 4V$
$IBAT = 200mA$

**USBIN Power FET On-Resistance vs Temperature**

$V_{USBIN} = 4V$
$IBAT = 200mA$

**PWR-DCIN and PWR-USBIN Power FET On-Resistance vs Temperature**

$V_{DCIN} = 5V$
$IPWR = 30mA$
$V_{USBIN} = 5V$
$IPWR = 30mA$

**$V_{PWR}$ vs $I_{PWR}$**

$V_{DCIN} = 5V$
$V_{USBIN} = 5V$

**CHRG Pin I-V Curve**

$V_{DCIN} = V_{USBIN} = 5V$
$V_{BAT} = 4V$

**SUSP Pin Threshold Voltage (On-to-Off) vs Temperature**

$V_{DCIN} = V_{USBIN} = 5V$

**SUSP Pin Pulldown Resistance vs Temperature**

$V_{DCIN} = V_{USBIN} = 5V$

**Shutdown Supply Current vs Temperature and $V_{DCIN}$**

$SUSP = V_{DCIN}$
$V_{USBIN} = V_{DCIN}$

$V_{DCIN} = 5.9V$
$V_{DCIN} = 4.25V$
PIN FUNCTIONS

DCIN (Pin 1): Wall Adapter Input Supply Pin. Provides power to the battery charger. The maximum supply current is 1.2A. This pin should be bypassed with a 1µF capacitor.

USBIN (Pin 2): USB Input Supply Pin. Provides power to the battery charger. The maximum supply current is 1A. This pin should be bypassed with a 1µF capacitor.

PWR (Pin 3): Power Present Output. When the DCIN or USBIN pin voltage is sufficient to begin charging (i.e., when the DCIN or USBIN supply is greater than the undervoltage lockout thresholds and at least 100mV or 150mV, respectively, above the battery terminal), the PWR pin is connected to the appropriate input through an internal P-channel MOSFET. If sufficient voltage to charge is not present on DCIN or USBIN the PWR pin is high impedance. This output is able to source up to 120mA.

CHRG (Pin 4): Open-Drain Charge Status Output. When the LTC4096 is charging, the CHRG pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, CHRG becomes high impedance. This output is capable of sinking up to 10mA, making it suitable for driving an LED.

SUSP (Pin 5): Charge Enable Input. A logic low on this pin enables the charger. If left floating, an internal 3.4MΩ pull-down resistor defaults the LTC4096 to charge mode. Pull this pin high for shutdown.

ITERM (Pin 6): Charge Termination Current Threshold Program. The termination current threshold, ITERMINATE, is set by connecting a resistor, RTERM, to ground. ITERMINATE is set by the following formula:

\[ \text{ITERMINATE} = \frac{100}{R_{\text{TERM}}} \]

When the battery current, IBAT, falls below the termination threshold, charging stops and the CHRG output becomes high impedance.

IUSB (Pin 7): Charge Current Program for USB Power. The charge current is set by connecting a resistor, RUSB, to ground. When charging in constant-current mode, this pin servos to 1V. The voltage on this pin can be used to measure the battery current delivered from the USBIN input using the following formula:

\[ I_{\text{BAT}} = \frac{V_{\text{USB}}}{R_{\text{USB}}} \times 1000 \]
**PIN FUNCTIONS**

**GND (Pin 8):** Ground.

**IDC (Pin 9):** Charge Current Program for Wall Adapter Power. The charge current is set by connecting a resistor, $R_{IDC}$, to ground. When charging in constant-current mode, this pin servos to 1V. The voltage on this pin can be used to measure the battery current delivered from the DCIN input using the following formula:

$$I_{BAT} = \frac{V_{IDC}}{R_{IDC}}$$

**BAT (Pin 10):** Charger Output. This pin provides charge current to the battery and regulates the final float voltage to 4.2V.

**Exposed Pad (Pin 11):** GND. The exposed backside of the package is ground and must be soldered to PC board ground for electrical connection and maximum heat transfer.

---

**BLOCK DIAGRAM**

[Diagram of LTC4096/LTC4096X block diagram, showing various components and connections.]

---

**PIN FUNCTIONS**

**GND (Pin 8):** Ground.

**IDC (Pin 9):** Charge Current Program for Wall Adapter Power. The charge current is set by connecting a resistor, $R_{IDC}$, to ground. When charging in constant-current mode, this pin servos to 1V. The voltage on this pin can be used to measure the battery current delivered from the DCIN input using the following formula:

$$I_{BAT} = \frac{V_{IDC}}{R_{IDC}}$$

**BAT (Pin 10):** Charger Output. This pin provides charge current to the battery and regulates the final float voltage to 4.2V.

**Exposed Pad (Pin 11):** GND. The exposed backside of the package is ground and must be soldered to PC board ground for electrical connection and maximum heat transfer.
**OPERATION**

The LTC4096 is designed to efficiently manage charging a single-cell lithium-ion battery from two separate voltage sources: a wall adapter and USB power bus. Using the constant-current/constant-voltage algorithm, the charger can deliver up to 1.2A of charge current from the wall adapter supply or up to 1A of charge current from the USB supply with a final float voltage accuracy of ±0.6%. The LTC4096 has two internal P-channel power MOSFETs, thermal regulation and shut down circuitry. No blocking diodes or external sense resistors are required.

**Power Source Selection**

The LTC4096 can charge a battery from either the wall adapter input or the USB port input. The LTC4096 automatically senses the presence of voltage at each input. If both voltage sources are present, the LTC4096 defaults to the wall adapter source provided sufficient voltage is present at the DCIN input. “Sufficient voltage” is defined as:

- Supply voltage is greater than the UVLO threshold.
- Supply voltage is greater than the battery voltage by 30mV (100mV or 150mV rising, 30mV falling).

The power present output pin (PWR) indicates that sufficient input voltage is present. Table 1 describes the behavior of this status output.

<table>
<thead>
<tr>
<th>Voltage Condition</th>
<th>PWR Behavior</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDCIN &gt; 4.2V and VUSBIN &gt; BAT + 30mV</td>
<td>Charger powered from wall adapter source; VPWR = VDCIN – RDC-PWR • IPWR USBIN current &lt; 25µA</td>
</tr>
<tr>
<td>VDCIN &lt; 4.2V or VUSBIN &lt; BAT + 30mV</td>
<td>Charger powered from USB source; VPWR = VUSBIN – RUSB-PWR • IPWR</td>
</tr>
</tbody>
</table>

**Programming and Monitoring Charge Current**

The charge current delivered to the battery from the wall adapter supply is programmed using a single resistor from the IDC pin to ground.

\[
R_{\text{IDC}} = \frac{1000V}{I_{\text{CHRG(DC)}}}, \quad I_{\text{CHRG(DC)}} = \frac{1000V}{R_{\text{IDC}}}
\]

Similarly, the charge current from the USB supply is programmed using a single resistor from the IUSB pin to ground.

\[
R_{\text{IUSB}} = \frac{1000V}{I_{\text{CHRG(USB)}}}, \quad I_{\text{CHRG(USB)}} = \frac{1000V}{R_{\text{IUSB}}}
\]

Charge current out of the BAT pin can be determined at any time by monitoring the IDC or IUSB pin voltage and applying the following equations:

\[
I_{\text{BAT}} = \frac{V_{\text{IDC}}}{R_{\text{IDC}}} \times 1000, \text{ (charging from wall adapter)}
\]

\[
I_{\text{BAT}} = \frac{V_{\text{USB}}}{R_{\text{IUSB}}} \times 1000, \text{ (charging from USB supply)}
\]
Programming Charge Termination

The charge cycle terminates when the charge current falls below the programmed termination threshold during constant-voltage mode. This threshold is set by connecting an external resistor, R_{ITERM}, from the ITERM pin to ground.

The charge termination current threshold (I_{TERMINATE}) is set by the following equation:

$$\frac{100\text{V}}{I_{\text{TERMINATE}}} = \frac{100\text{V}}{R_{\text{ITERM}}}$$

The termination condition is detected by using an internal filtered comparator to monitor the ITERM pin. When the ITERM pin voltage drops below 100mV for longer than t_{TERMINATE} (typically 3ms), the charge cycle terminates, charge current latches off and the LTC4096 enters standby mode. When charging, transient loads on the BAT pin can cause the ITERM pin to fall below 100mV for short periods of time before the DC charge current has dropped below the programmed termination current. The 3ms filter time (t_{TERMINATE}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below the programmed termination threshold, the LTC4096 terminates the charge cycle and stops providing any current out of the BAT pin. In this state, any load on the BAT pin must be supplied by the battery.

Low-Battery Charge Conditioning (Trickle Charge)

This feature ensures that deeply discharged batteries are gradually charged before applying full charge current. If the BAT pin voltage is below 2.9V, the LTC4096 supplies 1/10th of the full charge current to the battery until the BAT pin rises above 2.9V. For example, if the charger is programmed to charge at 800mA from the wall adapter input and 500mA from the USB input, the charge current during trickle charge mode would be 80mA and 50mA, respectively.

The LTC4096X has no trickle charge mode.

Automatic Recharge

In standby mode, the charger sits idle and monitors the battery voltage using a comparator with a 1.6ms filter time (t_{RECHRG}). A charge cycle automatically restarts when the battery voltage falls below 4.15V (which corresponds to approximately 80%-90% battery capacity). This ensures that the battery is kept at, or near, a fully charged condition and eliminates the need for periodic charge cycle initiations. If the battery is removed from the charger, a sawtooth waveform appears at the battery output. This is caused by the repeated cycling between termination and recharge events. This cycling results in pulsing at the CHRG output; an LED connected to this pin will exhibit a blinking pattern, indicating to the user that a battery is not present. The frequency of the sawtooth is dependent on the amount of output capacitance.

Status Indicators

The charge status output (CHRG) has two states: pull-down and high impedance. The pull-down state indicates that the LTC4096 is in a charge cycle. Once the charge cycle has terminated or the LTC4096 is disabled, the pin state becomes high impedance. The pull-down state is capable of sinking up to 10mA.

The power present output (PWR) has two states: DCIN/USBIN voltages and high impedance. These states are described in Table 1 and the circuit is shown in Figure 2. The high impedance state indicates that voltage is not present at either DCIN or USBIN, so LTC4096 lacks sufficient power to charge the battery. The PWR present output is capable of sourcing up to 120mA steady state and includes short circuit protection.

*Any external sources that hold the ITERM pin above 100mV will prevent the LTC4096 from terminating a charge cycle.*
### Applications Information

#### Manual Shutdown

The SUSP pin has a 3.4MΩ pulldown resistor to GND. A logic low enables the charger and logic high disables it (the pulldown defaults the charger to the charging state). The DCIN input draws 20µA when the charger is in shutdown. The USBIN input draws 20µA during shutdown if no power is applied to DCIN, but draws only 10µA when \( V_{\text{DCIN}} > V_{\text{USBIN}} \).

#### Thermal Limiting

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 115°C. This feature protects the LTC4096 from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the device. The charge current can be set according to typical (not worst case) ambient temperature with the assurance that the charger will automatically reduce the current in worst case conditions. A safety thermal shutdown circuit will turn off the charger if the die temperature rises above a value of approximately 150°C. DFN power considerations are discussed further in the Applications Information section.

![Figure 1. LTC4096 State Diagram of a Charge Cycle](image)
**APPLICATIONS INFORMATION**

**Using a Single Charge Current Program Resistor**

In applications where the programmed wall adapter charge current and USB charge current are the same, a single program resistor can be used to set both charge currents. Figure 3 shows a charger circuit that uses one charge current program resistor. In this circuit, one resistor programs the same charge current for each input supply.

\[ I_{\text{CHRG(DC)}} = I_{\text{CHRG(USB)}} = \frac{1000V}{R_{\text{SET}}} \]

The LTC4096 can also program the wall adapter charge current and USB charge current independently using two program resistors, \( R_{\text{IDC}} \) and \( R_{\text{IUSB}} \). Figure 4 shows a charger circuit that sets the wall adapter charge current to 800mA and the USB charge current to 500mA.

**Stability Considerations**

The constant-voltage mode feedback loop is stable without any compensation provided a battery is connected to the charger output. However, a 4.7µF capacitor with a 1Ω series resistor is recommended at the BAT pin to keep the ripple voltage low when the battery is disconnected. When the charger is in constant-current mode, the charge current program pin (IDC or IUSB) is in the feedback loop, not the battery. The constant-current mode stability is affected by the impedance at the charge current program pin. With no additional capacitance on this pin, the charger is stable with program resistor values as high as 20KΩ (\( I_{\text{CHRG}} = 50\text{mA} \)); however, additional capacitance on these nodes reduces the maximum allowed program resistor.

---

*Figure 2. Input Power Present Output (PWR) Circuit*
Power Dissipation

When designing the battery charger circuit, it is not necessary to design for worst-case power dissipation scenarios because the LTC4096 automatically reduces the charge current during high power conditions. The conditions that cause the LTC4096 to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Most of the power dissipation is generated from the internal MOSFET pass device. Thus, the power dissipation is calculated to be:

\[ P_D = (V_{IN} - V_{BAT}) \times I_{BAT} \]

\( P_D \) is the power dissipated, \( V_{IN} \) is the input supply voltage (either DCIN or USBIN), \( V_{BAT} \) is the battery voltage and \( I_{BAT} \) is the charge current. The approximate ambient temperature at which the thermal feedback begins to protect the IC is:

\[ T_A = 115°C - P_D \times \theta_{JA} \]

\[ T_A = 115°C - (V_{IN} - V_{BAT}) \times I_{BAT} \times \theta_{JA} \]

Example: An LTC4096 operating from a 5V wall adapter (on the DCIN input) is programmed to supply 800mA full-scale current to a discharged Li-Ion battery with a voltage of 3.3V. Assuming \( \theta_{JA} \) is 40°C/W (see Thermal Considerations), the ambient temperature at which the LTC4096 will begin to reduce the charge current is approximately:

\[ T_A = 115°C - 800mA \times 5V - 3.3V \times 40°C/W \]

\[ T_A = 115°C - 1.36W \times 40°C/W = 115°C - 54.4°C \]

\[ T_A = 60.6°C \]

The LTC4096 can be used above 60.6°C ambient, but the charge current will be reduced from 800mA. The approximate current at a given ambient temperature can be approximated by:

\[ I_{BAT} = \frac{105°C - T_A}{(V_{IN} - V_{BAT}) \times \theta_{JA}} \]

Using the previous example with an ambient temperature of 70°C, the charge current will be reduced to approximately:

\[ I_{BAT} = \frac{105°C - 60°C}{(5V - 3.3V) \times 40°C/W} = \frac{45°C}{68°C/A} \]

\[ I_{BAT} = 662mA \]

It is important to remember that LTC4096 applications do not need to be designed for worst-case thermal conditions, since the IC will automatically reduce power dissipation when the junction temperature reaches approximately 115°C. Moreover a thermal shut down protection circuit around 150°C safely prevents any damage putting LTC4096 into shut down mode.
**APPLICATIONS INFORMATION**

**Thermal Considerations**

In order to deliver maximum charge current under all conditions, it is critical that the exposed metal pad on the backside of the LTC4096 package is properly soldered to the PC board ground. When correctly soldered to a 2500mm² double sided 1oz copper board, the LTC4096 has a thermal resistance of approximately 40°C/W. Failure to make thermal contact between the exposed pad on the backside of the package and the copper board will result in thermal resistances far greater than 40°C/W. As an example, a correctly soldered LTC4096 can deliver over 800mA to a battery from a 5V supply at room temperature. Without a good backside thermal connection, this number would drop to much less than 500mA.

**Protecting the USB Pin and Wall Adapter Input from Overvoltage Transients**

Caution must be exercised when using ceramic capacitors to bypass the USBIN pin or the wall adapter inputs. High voltage transients can be generated when the USB or wall adapter is hot plugged. When power is supplied via the USB bus or wall adapter, the cable inductance along with the self resonant and high Q characteristics of ceramic capacitors can cause substantial ringing which could exceed the maximum voltage ratings and damage the LTC4096. Refer to Linear Technology Application Note 88, entitled “Ceramic Input Capacitors Can Cause Overvoltage Transients” for a detailed discussion of this problem.

Always use an oscilloscope to check the voltage waveforms at the USBIN and DCIN pins during USB and wall adapter hot-plug events to ensure that overvoltage transients have been adequately removed.

**Reverse Polarity Input Voltage Protection**

In some applications, protection from reverse polarity voltage on the input supply pins is desired. If the supply voltage is high enough, a series blocking diode can be used. In other cases where the voltage drop must be kept low, a P-channel MOSFET can be used (as shown in Figure 5).

![Figure 5. Low Loss Input Reverse Polarity Protection](image-url)
PACKAGE DESCRIPTION

DD Package
10-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1698)

NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE MO-229 VARIATION OF (WEED-2).
   CHECK THE LTC WEBSITE DATA SHEET FOR CURRENT STATUS OF VARIATION ASSIGNMENT
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADDED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE TOP AND BOTTOM OF PACKAGE
**RELATED PARTS**

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC3455</td>
<td>Dual DC/DC Converter with USB Power Management and Li-Ion Battery Charger</td>
<td>Efficiency &gt;96%, Accurate USB Current Limiting (500mA/100mA), 4mm × 4mm QFN-24 Package</td>
</tr>
<tr>
<td>LTC4053</td>
<td>USB Compatible Monolithic Li-Ion Battery Charger</td>
<td>Standalone Charger with Programmable Timer, Up to 1.25A Charge Current</td>
</tr>
<tr>
<td>LTC4054/LTC4054X</td>
<td>Standalone Linear Li-Ion Battery Charger with Integrated Pass Transistor in ThinSOT™</td>
<td>Thermal Regulation Prevents Overheating, C/10 Termination, C/10 Indicator, Up to 800mA Charge Current</td>
</tr>
<tr>
<td>LTC4055</td>
<td>USB Power Controller and Battery Charger</td>
<td>Charges Single-Cell Li-Ion Batteries Directly from USB Port, Thermal Regulation, 4mm × 4mm QFN-16 Package</td>
</tr>
<tr>
<td>LTC4058/LTC4058X</td>
<td>Standalone 950mA Lithium-Ion Charger in DFN</td>
<td>C/10 Charge Termination, Battery Kelvin Sensing, ±7% Charge Accuracy</td>
</tr>
<tr>
<td>LTC4061</td>
<td>Standalone Li-Ion Charger with Thermistor Interface</td>
<td>4.2V, ±0.35% Float Voltage, Up to 1A Charge Current, 3mm × 3mm DFN-10 Package</td>
</tr>
<tr>
<td>LTC4061-4.4</td>
<td>Standalone Li-Ion Charger with Thermistor Interface</td>
<td>4.4V, ±0.4% Float Voltage, Up to 1A Charge Current, 3mm × 3mm DFN-10 Package</td>
</tr>
<tr>
<td>LTC4062</td>
<td>Standalone Li-Ion Charger with Micropower Comparator</td>
<td>4.2V, ±0.35% Float Voltage, Up to 1A Charge Current, 3mm × 3mm DFN-10 Package</td>
</tr>
<tr>
<td>LTC4065/LTC4065A</td>
<td>Standalone 750mA Li-Ion Charger in 2mm × 2mm DFN</td>
<td>4.2V, ±0.6% Float Voltage, Up to 750mA Charge Current, 2mm × 2mm DFN-6 Package</td>
</tr>
<tr>
<td>LTC4066</td>
<td>USB Power Controller and Li-Ion Linear Battery Charger with Low-Loss Ideal Diode</td>
<td>Seamless Transition Between Input Power Sources: Li-Ion Battery, USB and Wall Adapter, Low-Loss (50mΩ) Ideal Diode, 4mm × 4mm QFN-24 Package</td>
</tr>
<tr>
<td>LTC4068/LTC4068X</td>
<td>Standalone Linear Li-Ion Battery Charger with Programmable Termination</td>
<td>Charge Current up to 950mA, Thermal Regulation, 3mm × 3mm DFN-8 Package</td>
</tr>
<tr>
<td>LTC4069</td>
<td>Standalone Li-Ion Battery Charger with NTC Thermistor Input in 2mm × 2mm DFN</td>
<td>4.2V, ±0.6% Float Voltage, Up to 750mA Charge Current, Timer Termination + C/10 Detection Output</td>
</tr>
<tr>
<td>LTC4075</td>
<td>Dual Input Standalone Li-Ion Battery Charger</td>
<td>Charges Single-Cell Li-Ion Batteries from Wall Adapter and USB Inputs with Automatic Input Power Detection and Selection, 950mA Charger Current, Thermal Regulation, C/X Charge Termination, 3mm × 3mm DFN Package</td>
</tr>
<tr>
<td>LTC4076</td>
<td>Dual Input Standalone Li-Ion Battery Charger</td>
<td>Charges Single-Cell Li-Ion Batteries from Wall Adapter and USB Inputs with Automatic Input Power Detection and Selection, 950mA Charger Current, Thermal Regulation, C/X Charge Termination, 3mm × 3mm DFN Package</td>
</tr>
<tr>
<td>LTC4077</td>
<td>Dual Input Standalone Li-Ion Battery Charger</td>
<td>Charges Single-Cell Li-Ion Batteries from Wall Adapter and USB Inputs with Automatic Input Power Detection and Selection, 950mA Charger Current, Thermal Regulation, C/10 Charge Termination, 3mm × 3mm DFN Package</td>
</tr>
<tr>
<td>LTC4085</td>
<td>USB Power Manager with Ideal Diode Controller and Li-Ion Charger</td>
<td>Charges Single-Cell Li-Ion Batteries Directly from a USB Port, Thermal Regulation, 200mΩ Ideal Diode with &lt;50mΩ option, 4mm × 3mm DFN-14 Package</td>
</tr>
<tr>
<td>LTC4089/ LTC4089-5</td>
<td>USB Power Manager with Ideal Diode Controller and High Efficiency Li-Ion Battery Charger</td>
<td>High Efficiency 1.2A Charger from 6V to 36V (40V Max) Input, Charges Single Cell Li-Ion Batteries Directly from a USB Port, Thermal Regulation; 200mΩ Ideal Diode with &lt;50mΩ Option, 4mm × 3mm DFN-14 Package, Bat-Track Adaptive Output Control (LTC4089); Fixed 5V Output (LTC4089-5)</td>
</tr>
<tr>
<td>LTC4410</td>
<td>USB Power Manager and Battery Charger</td>
<td>Manages Total Power Between a USB Peripheral and Battery Charger, Ultralow Battery Drain: 1μA, ThinSOT Package</td>
</tr>
<tr>
<td>LTC4411/LTC4412</td>
<td>Low Loss PowerPath™ Controller in ThinSOT</td>
<td>Automatic Switching Between DC Sources, Load Sharing, Replaces ORing Diodes</td>
</tr>
</tbody>
</table>

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