



ERRATA SHEET

DS34T101/DS34T102/DS34T104/DS34T108

Revision B1 Errata

The errata listed below describe situations where DS34T101/DS34T102/DS34T104/DS34T108 revision B1 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS34T101/DS34T102/DS34T104/DS34T108 revision B1 components. Revision B1 components are branded on the topside of the package with a six-digit code in the form yywwB1, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS34T101/DS34T102/DS34T104/DS34T108 die revision, visit our website at www.maxim-ic.com/errata.

SECTION 1: CIRCUIT EMULATION ENGINE

1) UDP CHECKSUM IS NOT SUPPORTED

Description:

DS34T10x rev B1 devices do not calculate the UDP checksum when preparing UDP packets for transmission or when receiving UDP packets. RFC-791 makes the UDP checksum optional for IPv4 nodes, but RFC-2460 requires the checksum when UDP packets are originated or received by an IPv6 node.

Workaround:

Use off-chip resources to calculate UDP checksums and provide them to the DS34T10x device for inclusion in the UDP headers of transmitted packets and for comparison with the checksums in the UDP headers of received packets.

OR

Include a fixed, nonzero checksum in the UDP headers of transmitted packets. If intermediate IPv6 nodes are only discarding packets with zero checksums in the UDP headers, this workaround will allow two DS34T10x devices to exchange UDP packets through an IPv6 network.

SECTION 2: T1/E1/J1 TRANSCEIVER, CLOCK RATE ADAPTER (CLAD), ETHERNET MAC

There are no known errata for these functional blocks on rev B1 devices.