1.1A Step-Up DC/DC Converter with Integrated Soft-Start

TYPICAL APPLICATION

**FEATURES**
- 1.3MHz Switching Frequency (LT3467)
- 2.1MHz Switching Frequency (LT3467A)
- Low $V_{CESAT}$ Switch: 330mV at 1.1A
- High Output Voltage: Up to 40V
- Wide Input Range: 2.4V to 16V
- Dedicated Soft-Start Pin
- 5V at 540mA from 3.3V Input (LT3467)
- 5V at 430mA from 3.3V Input (LT3467A)
- 12V at 270mA from 5V Input (LT3467)
- 12V at 260mA from 5V Input (LT3467A)
- Uses Small Surface Mount Components
- Low Shutdown Current: <1μA
- Pin-for-Pin Compatible with the LT1930 and LT1613
- Low Profile (1mm) ThinSOT™ Package
- Low Profile (0.75mm) 8-Lead (3mm × 2mm) DFN Package

**APPLICATIONS**
- Digital Cameras
- White LED Power Supplies
- Cellular Phones
- Medical Diagnostic Equipment
- Local 5V or 12V Supplies
- TFT-LCD Bias Supplies
- xDSL Power Supplies

**DESCRIPTION**

The LT®3467/LT3467A switching regulators combine a 42V, 1.1A switch with a soft-start function. Pin compatible with the LT1930, its low $V_{CESAT}$ bipolar switch enables the device to deliver high current outputs in a small footprint. The LT3467 switches at 1.3MHz, allowing the use of tiny, low cost and low height inductors and capacitors. The LT3467A switches at 2.1MHz, allowing the use of even smaller components. High inrush current at start-up is eliminated using the programmable soft-start function. A single external capacitor sets the current ramp rate. A constant frequency current mode PWM architecture results in low, predictable output noise that is easy to filter.

The high voltage switch on the LT3467/LT3467A is rated at 42V, making the devices ideal for boost converters up to 40V as well as SEPIC and flyback designs. The LT3467 can generate 5V at up to 540mA from a 3.3V supply or 5V at 450mA from four alkaline cells in a SEPIC design. The LT3467A can generate 5V at up to 430mA from a 3.3V supply or 15V at 135mA from a 3.3V supply. The LT3467/LT3467A are available in a low profile (1mm) 6-lead SOT-23 package and tiny 3mm × 2mm DFN package.

**TYPICAL APPLICATION**

Single Li-Ion Cell to 5V Boost Converter

![Efficiency Graph]

**Efficiency**

<table>
<thead>
<tr>
<th>$V_{IN}$ (V)</th>
<th>Efficiency (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.6V</td>
<td>95</td>
</tr>
<tr>
<td>3.3V</td>
<td>90</td>
</tr>
<tr>
<td>4.2V</td>
<td>85</td>
</tr>
</tbody>
</table>

$V_{OUT} = 5V$

765mA at $V_{IN} = 4.2V$
540mA at $V_{IN} = 3.3V$
360mA at $V_{IN} = 2.6V$

$V_{OUT}$ at VIN = 2.6V

$V_{OUT}$ at VIN = 3.3V

$V_{OUT}$ at VIN = 4.2V
LT3467/LT3467A

**ABSOLUTE MAXIMUM RATINGS** *(Note 1)*

- **VIN Voltage** ........................................... 16V
- **SW Voltage** ........................................... −0.4V to 42V
- **FB Voltage** ........................................... 2.5V
- **Current Into FB Pin** .............................. ±1mA
- **SHDN Voltage** .................................... 16V
- **Maximum Junction Temperature** .......... 125°C

**Operating Junction Temperature Range** *(Note 2)*

- **E Grade** ............................................. −40°C to 85°C
- **I Grade** ............................................. −40°C to 125°C
- **Storage Temperature Range** ................... −65°C to 150°C
- **Lead Temperature (Soldering, 10 sec)** ...... TSOT ..................................................... 300°C

**PIN CONFIGURATION**

![TOP VIEW]

**ORDER INFORMATION**

<table>
<thead>
<tr>
<th>LEAD FREE FINISH</th>
<th>TAPE AND REEL</th>
<th>PART MARKING*</th>
<th>PACKAGE DESCRIPTION</th>
<th>TEMPERATURE RANGE</th>
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<tbody>
<tr>
<td>LT3467EDDB#PBF</td>
<td>LT3467EDDB#TRPBF</td>
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<td>8-Lead (3mm × 2mm) Plastic DFN</td>
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<td>LTACH</td>
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<td>LTBC</td>
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<tr>
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<td>LT3467AIS6#TRPBF</td>
<td>LTBC</td>
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</tbody>
</table>

**LEAD BASED FINISH**

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<td>−40°C to 85°C</td>
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<td>LT3467IDDB</td>
<td>LT3467IDDB#TR</td>
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<td>8-Lead (3mm × 2mm) Plastic DFN</td>
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<td>LTBC</td>
<td>6-Lead Plastic TSOT-23</td>
<td>−40°C to 125°C</td>
</tr>
</tbody>
</table>

Consult LTC Marketing for parts specified with wider operating temperature ranges. *The temperature grade is identified by a label on the shipping container.

For more information on lead free part marking, go to: [http://www.linear.com/leadfree/](http://www.linear.com/leadfree/)

For more information on tape and reel specifications, go to: [http://www.linear.com/tapeandreel/](http://www.linear.com/tapeandreel/)
# Electrical Characteristics

The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at \( T_A = 25°C \). \( V_{IN} = 3V \), \( V_{SHDN} = V_{IN} \) unless otherwise noted. Specifications are for both the LT3467 and LT3467A unless otherwise noted.

## Parameter Conditions

<table>
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<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<td>Minimum Operating Voltage</td>
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<td>2.4</td>
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<td>V</td>
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<tr>
<td>Maximum Operating Voltage</td>
<td></td>
<td></td>
<td>16</td>
<td></td>
<td>V</td>
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<tr>
<td>Feedback Voltage</td>
<td></td>
<td>1.230</td>
<td>1.255</td>
<td>1.270</td>
<td>V</td>
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<td></td>
<td>•</td>
<td>1.220</td>
<td>1.280</td>
<td></td>
<td>V</td>
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<tr>
<td>FB Pin Bias Current</td>
<td>(Note 3)</td>
<td>10</td>
<td>50</td>
<td></td>
<td>nA</td>
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<tr>
<td>Quiescent Current</td>
<td>( V_{SHDN} = 2.4V, ) Not Switching</td>
<td>1.2</td>
<td>2</td>
<td></td>
<td>mA</td>
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<tr>
<td>Quiescent Current in Shutdown</td>
<td>( V_{SHDN} = 0.5V, V_{IN} = 3V )</td>
<td>0.01</td>
<td>1</td>
<td></td>
<td>µA</td>
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<td>Reference Line Regulation</td>
<td>( 2.6V \leq V_{IN} \leq 16V )</td>
<td>0.01</td>
<td>0.05</td>
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<td>%/V</td>
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<td>Switching Frequency</td>
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<td>1.3</td>
<td>1.6</td>
<td>MHz</td>
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<tr>
<td></td>
<td>LT3467A</td>
<td>1.6</td>
<td>2.1</td>
<td>2.7</td>
<td>MHz</td>
</tr>
<tr>
<td></td>
<td>•</td>
<td>1.6</td>
<td></td>
<td></td>
<td>MHz</td>
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<tr>
<td>Maximum Duty Cycle</td>
<td>LT3467</td>
<td>88</td>
<td>94</td>
<td></td>
<td>%</td>
</tr>
<tr>
<td></td>
<td>LT3467A</td>
<td>87</td>
<td>88</td>
<td></td>
<td>%</td>
</tr>
<tr>
<td></td>
<td>•</td>
<td>82</td>
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<td></td>
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<td>%</td>
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<tr>
<td>Minimum Duty Cycle</td>
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<td>10</td>
<td></td>
<td></td>
<td>%</td>
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<tr>
<td>Switch Current Limit</td>
<td>At Minimum Duty Cycle</td>
<td>1.4</td>
<td>1.8</td>
<td>2.5</td>
<td>A</td>
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<tr>
<td></td>
<td>At Maximum Duty Cycle (Note 4)</td>
<td>0.8</td>
<td>1.2</td>
<td>1.9</td>
<td>A</td>
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<tr>
<td>Switch ( V_{CESAT} )</td>
<td>( I_{SW} = 1.1A )</td>
<td>330</td>
<td>500</td>
<td></td>
<td>mV</td>
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<td>Switch Leakage Current</td>
<td>( V_{SW} = 5V )</td>
<td>0.01</td>
<td>1</td>
<td></td>
<td>µA</td>
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<tr>
<td>SHDN Input Voltage High</td>
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<td>2.4</td>
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<td></td>
<td>V</td>
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<tr>
<td>SHDN Input Voltage Low</td>
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<td>0.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>SHDN Pin Bias Current</td>
<td>( V_{SHDN} = 3V )</td>
<td>16</td>
<td>32</td>
<td></td>
<td>µA</td>
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<tr>
<td></td>
<td>( V_{SHDN} = 0V )</td>
<td>0</td>
<td>0.1</td>
<td></td>
<td>µA</td>
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<tr>
<td>SS Charging Current</td>
<td>( V_{SS} = 0.5V )</td>
<td>2</td>
<td>3</td>
<td>4.5</td>
<td>µA</td>
</tr>
</tbody>
</table>

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LT3467E/LT3467AE are guaranteed to meet performance specifications from 0°C to 85°C, junction temperature. Specifications over the –40°C to 85°C operating junction temperature range are assured by design, characterization and correlation with statistical process controls. The LT3467I/LT3467AI are guaranteed over the full –40°C to 125°C operating junction temperature range.

**Note 3:** Current flows out of the pin.

**Note 4:** See Typical Performance Characteristics for guaranteed current limit vs duty cycle.
TYPICAL PERFORMANCE CHARACTERISTICS

**Quiescent Current vs Temperature**

**FB Pin Voltage vs Temperature**

**SHDN Current vs SHDN Voltage**

**Current Limit vs Duty Cycle**

**Switch Saturation Voltage vs Switch Current**

**Oscillator Frequency vs Temperature**

**Soft-Start Current vs Soft-Start Voltage**

**Peak Switch Current vs Soft-Start Voltage**

**Start-Up Waveform (Figure 2 Circuit)**
**PIN FUNCTIONS** *(DFN/TSOT)*

**FB (Pin 1/Pin 3):** Feedback Pin. Reference voltage is 1.255V. Connect resistive divider tap here. Minimize trace area at FB. Set $V_{OUT} = 1.255V(1 + R1/R2)$.

**GND (Pins 2, 5, 9/Pin 2):** Ground. Tie directly to local ground plane.

**SW (Pins 3, 4/Pin 1):** Switch Pin. (Collector of internal NPN power switch) Connect inductor/diode here and minimize the metal trace area connected to this pin to minimize EMI.

**V_{IN} (Pin 6/Pin 6):** Input Supply Pin. Must be locally bypassed.

**SS (Pin 7/Pin 5):** Soft-Start Pin. Place a soft-start capacitor here. Upon start-up, 4μA of current charges the capacitor to 1.255V. Use a larger capacitor for slower start-up. Leave floating if not in use.

**SHDN (Pin 8/Pin 4):** Shutdown Pin. Tie to 2.4V or more to enable device. Ground to shut down.

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**BLOCK DIAGRAM**

![Block Diagram](image)

**Figure 1. Block Diagram**
OPERATION

The LT3467 uses a constant frequency, current-mode control scheme to provide excellent line and load regulation. Refer to the Block Diagram. At the start of each oscillator cycle, the SR latch is set which turns on the power switch Q1. A voltage proportional to the switch current is added to a stabilizing ramp and the resulting sum is fed into the positive terminal of the PWM comparator A2. When this voltage exceeds the level at the negative input of A2, the SR latch is reset, turning off the power switch. The level at the negative input of A2 is set by the error amplifier A1, and is simply an amplified version of the difference between the feedback voltage and the reference voltage of 1.255V. In this manner, the error amplifier sets the correct peak current level to keep the output in regulation. If the error amplifier’s output increases, more current is delivered to the output. Similarly, if the error decreases, less current is delivered. The soft-start feature of the LT3467 allows for clean start-up conditions by limiting the rate of voltage rise at the output of comparator A1 which, in turn, limits the peak switch current. The soft-start pin is connected to a reference voltage of 1.255V through a 250k resistor, providing 4μA of current to charge the soft-start capacitor. Typical values for the soft-start capacitor range from 10nF to 200nF. The LT3467 has a current limit circuit not shown in the Block Diagram. The switch current is constantly monitored and not allowed to exceed the maximum switch current (typically 1.4A). If the switch current reaches this value, the SR latch is reset regardless of the state of comparator A2. This current limit protects the power switch as well as the external components connected to the LT3467.

The Block Diagram for the LT3467A (not shown) is identical except that the oscillator frequency is 2.1MHz.

APPLICATIONS INFORMATION

Duty Cycle

The typical maximum duty cycle of the LT3467 is 94% (88% for the LT3467A). The duty cycle for a given application is given by:

\[ DC = \frac{|V_{OUT}| + |V_D| - |V_{IN}|}{|V_{OUT}| + |V_D| - |V_{CESAT}|} \]

where \( V_D \) is the diode forward voltage drop and \( V_{CESAT} \) is in the worst case 330mV (at 1.1A).

The LT3467 and LT3467A can be used at higher duty cycles, but must be operated in the discontinuous conduction mode so that the actual duty cycle is reduced.

Setting Output Voltage

R1 and R2 determine the output voltage.

\[ V_{OUT} = 1.255V \times (1 + R1/R2) \]

Switching Frequency and Inductor Selection

The LT3467 switches at 1.3 MHz, allowing for small valued inductors to be used. 4.7μH or 10μH will usually suffice. The LT3467A switches at 2.1MHz, allowing for even smaller valued inductors to be used. 0.9μH to 6.8μH will usually suffice. Choose an inductor that can handle at least 1.2A without saturating, and ensure that the inductor has a low DCR (copper-wire resistance) to minimize I²R power losses. Note that in some applications, the current handling requirements of the inductor can be lower, such as in the SEPIC topology where each inductor only carries one-half of the total switch current. For better efficiency, use similar valued inductors with a larger volume. Many different sizes and shapes are available from various manufacturers. Choose a core material that has low losses at 1.3MHz, (2.1MHz for the LT3467A) such as ferrite core.
APPLICATIONS INFORMATION

![Diagram](image)

**Figure 2. Single Li-Ion Cell to 5V Boost Converter (Same as 1st Page Application)**

**Table 1. Inductor Manufacturers**

<table>
<thead>
<tr>
<th>Manufacturer</th>
<th>Phone</th>
<th>Website</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sumida</td>
<td>(847) 956-0666</td>
<td><a href="http://www.sumida.com">www.sumida.com</a></td>
</tr>
<tr>
<td>TDK</td>
<td>(847) 803-6100</td>
<td><a href="http://www.tdk.com">www.tdk.com</a></td>
</tr>
<tr>
<td>Murata</td>
<td>(714) 852-2001</td>
<td><a href="http://www.murata.com">www.murata.com</a></td>
</tr>
<tr>
<td>FDK</td>
<td>(408) 432-8331</td>
<td><a href="http://www.fdk.co.jp">www.fdk.co.jp</a></td>
</tr>
</tbody>
</table>

**Soft-Start**

The soft-start feature provides a way to limit the inrush current drawn from the supply upon start-up. An internal 250k resistor charges the external soft-start capacitor to 1.255V. After the capacitor reaches 0.15V the rate of voltage rise at the output of the comparator A1 tracks the rate of voltage rise of the soft-start capacitor. This limits the inrush current drawn from the supply during start-up. The soft-start feature plays another important role in applications where the switch will reach levels of 30V or higher. During start-up, excessively high switch current, together with the presence of high voltage can overstress the switch. A properly used soft-start feature will keep the switch current from overshooting. This practice will greatly improve the robustness of such designs. Once the part is shut down, the soft-start capacitor is quickly discharged to 0.4V, then slowly discharged through the 250k resistor to ground. If the part is to be shut down and re-enabled in a short period of time while soft-start is used, you must ensure that the soft-start capacitor has enough time to discharge before re-enabling the part. Typical values of the soft-start capacitor range from 10nF to 200nF.
APPLICATIONS INFORMATION

Capacitor Selection

Low ESR (equivalent series resistance) capacitors should be used at the output to minimize the output ripple voltage. Multi-layer ceramic capacitors are an excellent choice, as they have extremely low ESR and are available in very small packages. X5R dielectrics are preferred, followed by X7R, as these materials retain the capacitance over wide voltage and temperature ranges. A 4.7μF to 15μF output capacitor is sufficient for most applications, but systems with very low output currents may need only a 1μF or 2.2μF output capacitor. Solid tantalum or OS-CON capacitors can be used, but they will occupy more board area than a ceramic and will have a higher ESR. Always use a capacitor with a sufficient voltage rating.

Ceramic capacitors also make a good choice for the input decoupling capacitor, which should be placed as close as possible to the LT3467. A 1μF to 4.7μF input capacitor is sufficient for most applications. Table 2 shows a list of several ceramic capacitor manufacturers. Consult the manufacturers for detailed information on their entire selection of ceramic parts.

<table>
<thead>
<tr>
<th>Ceramic Capacitor Manufacturers</th>
<th>Phone</th>
<th>Website</th>
</tr>
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<tbody>
<tr>
<td>Taiyo Yuden</td>
<td>(408) 573-4150</td>
<td><a href="http://www.t-yuden.com">www.t-yuden.com</a></td>
</tr>
<tr>
<td>AVX</td>
<td>(803) 448-9411</td>
<td><a href="http://www.avxcorp.com">www.avxcorp.com</a></td>
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<tr>
<td>Murata</td>
<td>(714) 852-2001</td>
<td><a href="http://www.murata.com">www.murata.com</a></td>
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</tbody>
</table>

The decision to use either low ESR (ceramic) capacitors or the higher ESR (tantalum or OS-CON) capacitors can affect the stability of the overall system. The ESR of any capacitor, along with the capacitance itself, contributes a zero to the system. For the tantalum and OS-CON capacitors, this zero is located at a lower frequency due to the higher value of the ESR, while the zero of a ceramic capacitor is at a much higher frequency and can generally be ignored.

A phase lead zero can be intentionally introduced by placing a capacitor (C4) in parallel with the resistor (R1) between V\text{OUT} and V\text{FB} as shown in Figure 2. The frequency of the zero is determined by the following equation.

$$f_Z = \frac{1}{2\pi \cdot R1 \cdot C4}$$

By choosing the appropriate values for the resistor and capacitor, the zero frequency can be designed to improve the phase margin of the overall converter. The typical target value for the zero frequency is between 35kHz to 55kHz. Figure 3 shows the transient response of the step-up converter from Figure 8 without the phase lead capacitor C4. Although adequate for many applications, phase margin is not ideal as evidenced by 2-3 “bumps” in both the output voltage and inductor current. A 22pF capacitor for C4 results in ideal phase margin, which is revealed in Figure 4 as a more damped response and less overshoot.

Diode Selection

A Schottky diode is recommended for use with the LT3467 and the LT3467A. The Philips PMEG 2005 is a very good choice. Where the switch voltage exceeds 20V, use the PMEG 3005 (a 30V diode). Where the switch voltage exceeds 30V, use the PMEG 4005 (a 40V diode). These diodes are rated to handle an average forward current of 0.5A. In applications where the average forward current of the diode exceeds 0.5A, a Philips PMEG 2010 rated at 1A is recommended. For higher efficiency, use a diode with better thermal characteristics such as the On Semiconductor MBRM120 (a 20V diode) or the MBRM140 (a 40V diode).
APPLICATIONS INFORMATION

Figure 3. Transient Response of Figure 8’s Step-Up Converter without Phase Lead Capacitor

Figure 4. Transient Response of Figure 8’s Step-Up Converter with a 22pF Phase Lead Capacitor

Setting Output Voltage
To set the output voltage, select the values of R1 and R2 (see Figure 2) according to the following equation.

\[ R1 = R2 \left( \frac{V_{OUT}}{1.255V} - 1 \right) \]

A good value for R2 is 13.3k which sets the current in the resistor divider chain to \( \frac{1.255V}{13.3k} = 94\mu A \).

Layout Hints
The high speed operation of the LT3467/LT3467A demands careful attention to board layout. You will not get advertised performance with careless layout. Figure 5a shows the recommended component placement for the ThinSOT package. Figure 5b shows the recommended component placement for the DFN package. Note the vias under the Exposed Pad. These should connect to a local ground plane for better thermal performance.
Applications Information

Compensation—Theory

Like all other current mode switching regulators, the LT3467/LT3467A needs to be compensated for stable and efficient operation. Two feedback loops are used in the LT3467/LT3467A: a fast current loop which does not require compensation, and a slower voltage loop which does. Standard Bode plot analysis can be used to understand and adjust the voltage feedback loop.

As with any feedback loop, identifying the gain and phase contribution of the various elements in the loop is critical. Figure 6 shows the key equivalent elements of a boost converter. Because of the fast current control loop, the power stage of the IC, inductor and diode have been replaced by the equivalent transconductance amplifier $g_{mp}$. $g_{mp}$ acts as a current source where the output current is proportional to the $V_C$ voltage. Note that the maximum output current of $g_{mp}$ is finite due to the current limit in the IC.

From Figure 6, the DC gain, poles and zeroes can be calculated as follows:

- **Output Pole**: $P_1 = \frac{2}{2 \pi R_L C_{OUT}}$
- **Error Amp Pole**: $P_2 = \frac{1}{2 \pi R_O C_C}$
- **Error Amp Zero**: $Z_1 = \frac{1}{2 \pi R_C C_C}$
- **DC Gain**: $A = \frac{1.255}{V_{OUT}} \cdot \frac{V_{IN}}{g_{ma}} \cdot \frac{R_O}{g_{mp}} \cdot \frac{R_L}{2}$
- **ESR Zero**: $Z_2 = \frac{1}{2 \pi R_{ESR} C_{OUT}}$
- **RHP Zero**: $Z_3 = \frac{V_{IN}^2 \cdot R_L}{2 \pi \cdot V_{OUT}^2 \cdot R_L}$
- **High Frequency Pole**: $P_3 > \frac{f_S}{3}$
- **Phase Lead Zero**: $Z_4 = \frac{1}{2 \pi R_1 C_{PL}}$
- **Phase Lead Pole**: $P_4 = \frac{1}{2 \pi R_2 R_1 C_{PL}}$  

The current mode zero is a right-half plane zero which can be an issue in feedback control design, but is manageable with proper external component selection.

---

**Figure 6. Boost Converter Equivalent Model**
APPLICATIONS INFORMATION

Using the circuit of Figure 2 as an example, the following table shows the parameters used to generate the Bode plot shown in Figure 7.

Table 3. Bode Plot Parameters

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>VALUE</th>
<th>UNITS</th>
<th>COMMENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( R_L )</td>
<td>10.4</td>
<td>( \Omega )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( C_{OUT} )</td>
<td>15</td>
<td>( \mu \text{F} )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( R_{ESR} )</td>
<td>10</td>
<td>( \mu \text{m} \Omega )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( R_D )</td>
<td>0.4</td>
<td>( \Omega )</td>
<td>Not Adjustable</td>
</tr>
<tr>
<td>( C_C )</td>
<td>60</td>
<td>( \text{pF} )</td>
<td>Not Adjustable</td>
</tr>
<tr>
<td>( C_{PL} )</td>
<td>3.3</td>
<td>( \text{pF} )</td>
<td>Adjustable</td>
</tr>
<tr>
<td>( R_C )</td>
<td>100</td>
<td>( \text{k} \Omega )</td>
<td>Not Adjustable</td>
</tr>
<tr>
<td>( R_1 )</td>
<td>402</td>
<td>( \text{k} \Omega )</td>
<td>Adjustable</td>
</tr>
<tr>
<td>( R_2 )</td>
<td>133</td>
<td>( \text{k} \Omega )</td>
<td>Adjustable</td>
</tr>
<tr>
<td>( V_{OUT} )</td>
<td>5</td>
<td>( \text{V} )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( V_{IN} )</td>
<td>3.3</td>
<td>( \text{V} )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( g_{ma} )</td>
<td>35</td>
<td>( \mu \text{mho} )</td>
<td>Not Adjustable</td>
</tr>
<tr>
<td>( g_{mp} )</td>
<td>7.5</td>
<td>( \mu \text{mho} )</td>
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<tr>
<td>( L )</td>
<td>2.7</td>
<td>( \text{mH} )</td>
<td>Application Specific</td>
</tr>
<tr>
<td>( f_S )</td>
<td>1.3*</td>
<td>( \text{MHz} )</td>
<td>Not Adjustable</td>
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</table>

*2.1MHz for LT3467A

From Figure 7, the phase is \(-138^\circ\) when the gain reaches 0dB giving a phase margin of 42°. This is more than adequate. The crossover frequency is 37kHz.

TYPICAL APPLICATIONS

Lithium-Ion to 6V Step-Up DC/DC Converter

Li-Ion to 6V
**TYPICAL APPLICATIONS**

**4-Cell to 5V SEPIC Converter**

- **VIN**: 4V to 6.5V
- **C1**: 2.2μF
- **C2**: X5R or X7R, 10V
- **C3**: 1μF
- **C4**: 0.047μF
- **C5**: 4.7pF
- **L1**: 10μH
- **L2**: 10μH
- **C6**: 0.047μF
- **D1**: PHILIPS PMEG 2010
- **L1, L2**: MURATA LQH32CN100K33L
- **VOUT**: 5V
- **SHDN**: 255mA AT VIN = 4V
- **FB**: 400mA AT VIN = 5V
- **SS**: 450mA AT VIN = 6.5V
- **C1, C3**: X5R or X7R, 10V
- **C2**: X5R or X7R, 6.3V

**5V to 40V Boost Converter**

- **VIN**: 5V
- **C1**: 4.7μF
- **C2**: X5R or X7R, 50V
- **C3**: 0.1μF
- **R1**: 412k
- **R2**: 13.3k
- **R3**: 147k
- **L1**: 2.7μH
- **C4**: 1μF
- **D1**: ON SEMICONDUCTOR, MBRM140
- **L1**: SUMIDA CD43-2R7
- **VOUT**: 40V
- **R1**: 20mA
- **C1**: X5R or X7R, 6.3V
- **C2**: X5R or X7R, 50V

**±15V Dual Output Converter with Output Disconnect**

- **VIN**: 5V
- **C1**: 2.2μF
- **C2**: X5R or X7R, 10V
- **C3**: 2.2μF
- **C4**: 1μF
- **C5**: 1μF
- **C6**: 0.047μF
- **D1**: PHILIPS PMEG 2005
- **R1**: 2.2μF
- **R2**: 13.3k
- **R3**: 147k
- **R4**: 147k
- **R5**: 147k
- **D1**: PHILIPS PMEG 2005
- **D2**: PHILIPS PMEG 2005
- **L1**: SUMIDA CR43-100
- **VOUT**: ±15V
- **R1**: 100mA
- **R2**: 100mA
- **C1**: X5R or X7R, 6.3V
- **C2**: X5R or X7R, 16V
TYPICAL APPLICATIONS

9V, 18V, –9V Triple Output TFT-LCD Bias Supply with Soft-Start

8V, 23V, –8V Triple Output TFT-LCD Bias Supply with Soft-Start
**TYPICAL APPLICATIONS**

**Single Li-Ion Cell to 5V Boost Converter**

- **Input Voltage:** 2.6V to 4.2V
- **Components:**
  - C1: 4.7μF
  - C3: 0.047μF
  - R2: 2.67k
  - \( V_{\text{SW}} \)
  - \( V_{\text{FB}} \)
  - \( V_{\text{OUT}} = 5\) V
  - \( I_{\text{OUT}} = 600\) mA at \( V_{\text{IN}} = 4.2\) V
  - \( I_{\text{OUT}} = 390\) mA at \( V_{\text{IN}} = 3.3\) V
  - \( I_{\text{OUT}} = 250\) mA at \( V_{\text{IN}} = 2.6\) V

*\( C2 \) can be 10μF in a 1210 or larger package with the addition of \( C4 \), otherwise \( C4 \) is optional.

**2.6V-3.3V to 5V Boost Converter**

- **Input Voltage:** 2.6V to 3.3V
- **Components:**
  - C1: 4.7μF
  - C3: 0.047μF
  - R2: 2.67k
  - \( V_{\text{SW}} \)
  - \( V_{\text{FB}} \)
  - \( V_{\text{OUT}} = 5\) V
  - \( I_{\text{OUT}} = 430\) mA at \( V_{\text{IN}} = 3.3\) V
  - \( I_{\text{OUT}} = 270\) mA at \( V_{\text{IN}} = 2.6\) V

**3.3V to 15V, 135mA Step-Up Converter**

- **Input Voltage:** 3.3V
- **Components:**
  - C1: 4.7μF
  - C3: 0.047μF
  - R2: 2.2μF
  - \( V_{\text{SW}} \)
  - \( V_{\text{FB}} \)
  - \( V_{\text{OUT}} = 15\) V
  - \( I_{\text{OUT}} = 135\) mA

**Efficiency Graphs**

- 3.3V to 15V, 135mA Step-Up Converter
- 2.6V-3.3V to 5V Boost Converter
- Single Li-Ion Cell to 5V Boost Converter
PACKAGE DESCRIPTION

DDB Package
8-Lead Plastic DFN (3mm × 2mm)
(Reference LTC DWG # 05-08-1702 Rev B)

NOTE:
1. DRAWING CONFORMS TO VERSION (WECD-1) IN JEDEC PACKAGE OUTLINE M0-229
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE TOP AND BOTTOM OF PACKAGE
LT3467/LT3467A

PACKAGE DESCRIPTION

S6 Package
6-Lead Plastic TSOT-23
(Reference LTC DWG # 05-08-1636)

NOTE:
1. DIMENSIONS ARE IN MILLIMETERS
2. DRAWING NOT TO SCALE
3. DIMENSIONS ARE INCLUSIVE OF PLATING
4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
5. MOLD FLASH SHALL NOT EXCEED 0.254mm
6. JEDEC PACKAGE REFERENCE IS MO-193
## REVISION HISTORY

(Revision history begins at Rev E)

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<th>PAGE NUMBER</th>
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<td>E</td>
<td>04/10</td>
<td>Updated Note 2 in Absolute Maximum Ratings and Electrical Characteristics</td>
<td>2, 3</td>
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**TYPICAL APPLICATIONS**

### RELATED PARTS

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<th>DESCRIPTION</th>
<th>COMMENTS</th>
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<td>LT1615/LT1615-1</td>
<td>300mA/80mA (I(_{SW})), High Efficiency Step-Up DC/DC Converter</td>
<td>V(<em>{IN}) 1V to 15V, V(</em>{OUT})(MAX) = 34V, I(<em>Q) = 20(\mu)A, I(</em>{SD}) &lt; 1(\mu)A, ThinSOT Package</td>
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<td>LT1618</td>
<td>1.5A (I(_{SW})), 1.25MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>90% Efficiency, V(<em>{IN}): 1.6V to 18V, V(</em>{OUT})(MAX) = 35V, I(<em>Q) = 1.8mA, I(</em>{SD}) &lt; 1(\mu)A, MS Package</td>
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<tr>
<td>LTC1700</td>
<td>No R(_{SENSE})™, 530kHz, Synchronous Step-Up DC/DC Controller</td>
<td>95% Efficiency, V(_{IN}): 0.9V to 5V, I(<em>P) = 200(\mu)A, I(</em>{SD}) &lt; 10(\mu)A, MS Package</td>
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<tr>
<td>LTC1871</td>
<td>Wide Input Range, 1MHz, No R(_{SENSE}) Current Mode Boost, Flyback and SEPIC Controller</td>
<td>92% Efficiency, V(_{IN}): 2.5V to 36V, I(<em>Q) = 250(\mu)A, I(</em>{SD}) &lt; 10(\mu)A, MS Package</td>
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<tr>
<td>LT1930/LT1930A</td>
<td>1A (I(_{SW})), 1.2MHz/2.2MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>High Efficiency, V(<em>{IN}): 2.6V to 16V, V(</em>{OUT})(MAX) = 34V, I(<em>Q) = 4.2mA/5.5mA, I(</em>{SD}) &lt; 1(\mu)A, ThinSOT Package</td>
</tr>
<tr>
<td>LT1946/LT1946A</td>
<td>1.5A (I(_{SW})), 1.2MHz/2.7MHz, High Efficiency Step-Up DC/DC Converter with Soft-Start</td>
<td>High Efficiency, V(<em>{IN}): 2.45V to 16V, V(</em>{OUT})(MAX) = 34V, I(<em>Q) = 3.2mA, I(</em>{SD}) &lt; 1(\mu)A, MS Package</td>
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<tr>
<td>LT1961</td>
<td>1.5A (I(_{SW})), 1.25MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>90% Efficiency, V(<em>{IN}): 3V to 25V, V(</em>{OUT})(MAX) = 35V, I(<em>Q) = 0.9mA, I(</em>{SD}) &lt; 6(\mu)A, MS8E Package</td>
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<td>LTC3400/</td>
<td>600mA (I(_{SW})), 1.2MHz, Synchronous Step-Up DC/DC Converter</td>
<td>92% Efficiency, V(<em>{IN}): 0.85V to 5V, V(</em>{OUT})(MAX) = 5V, I(<em>Q) = 19mA/300(\mu)A, I(</em>{SD}) &lt; 1(\mu)A, ThinSOT Package</td>
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<tr>
<td>LTC3401</td>
<td>1A (I(_{SW})), 3MHz, Synchronous Step-Up DC/DC Converter</td>
<td>97% Efficiency, V(<em>{IN}): 0.5V to 5V, V(</em>{OUT})(MAX) = 5.5V, I(<em>Q) = 38(\mu)A, I(</em>{SD}) &lt; 1(\mu)A, MS Package</td>
</tr>
<tr>
<td>LTC3402</td>
<td>2A (I(_{SW})), 3MHz, Synchronous Step-Up DC/DC Converter</td>
<td>97% Efficiency, V(<em>{IN}): 0.5V to 5V, V(</em>{OUT})(MAX) = 5.5V, I(<em>Q) = 38(\mu)A, I(</em>{SD}) &lt; 1(\mu)A, MS Package</td>
</tr>
<tr>
<td>LT3464</td>
<td>85mA (I(_{SW})), High Efficiency Step-Up DC/DC Converter with Integrated Schottky and PNP Disconnect</td>
<td>V(<em>{IN}): 2.3V to 10V, V(</em>{OUT})(MAX) = 34V, I(<em>Q) = 25(\mu)A, I(</em>{SD}) &lt; 1(\mu)A, ThinSOT Package</td>
</tr>
</tbody>
</table>

No R\(_{SENSE}\) is a trademark of Linear Technology Corporation.