**FEATURES**

- RS485/RS422 Transceiver: 2500V\text{RMS} for 1 Minute
- UL-CSA Recognized [File #E151738](#)
- CSA Component Acceptance Notice 5A
- Isolated DC Power: 5V at Up to 200mA
- No External Components Required
- 20Mbps or Low EMI 250kbps Data Rate
- High ESD: ±15kV HBM on Transceiver Interface
- High Common Mode Transient Immunity: 30kV/μs
- Integrated Selectable 120Ω Termination
- 3.3V (LTM2881-3) or 5.0V (LTM2881-5) Operation
- 1.62V to 5.5V Logic Supply Pin for Flexible Digital Interface
- Maximum Continuous Working Voltage: 560V\text{PEAK}
- High Input Impedance Failsafe RS485 Receiver
- Current Limited Drivers and Thermal Shutdown
- Compatible with TIA/EIA-485-A and PROFIBUS
- High Impedance Output During Internal Fault Condition
- Low Current Shutdown Mode (< 10μA)
- General Purpose CMOS Isolated Channel
- 15mm × 11.25mm BGA and LGA Packages

**DESCRIPTION**

The LTM®2881 is a complete galvanically isolated full-duplex RS485/RS422 µModule® (micromodule) transceiver. No external components are required. A single supply powers both sides of the interface through an integrated, isolated, low noise, efficient 5V output DC/DC converter.

Coupled inductors and an isolation power transformer provide 2500V\text{RMS} of isolation between the line transceiver and the logic interface. This device is ideal for systems where the ground loop is broken allowing for large common mode voltage variation. Uninterrupted communication is guaranteed for common mode transients greater than 30kV/μs.

Maximum data rates are 20Mbps or 250kbps in slew limited mode. Transmit data, DI and receive data, RO, are implemented with event driven low jitter processing. The receiver has a one-eighth unit load supporting up to 256 nodes per bus. A logic supply pin allows easy interfacing with different logic levels from 1.62V to 5.5V, independent of the main supply.

Enhanced ESD protection allows this part to withstand up to ±15kV (human body model) on the transceiver interface pins to isolated supplies and ±10kV through the isolation barrier to logic supplies without latch-up or damage.

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**APPLICATIONS**

- Isolated RS485/RS422 Interface
- Industrial Networks
- Breaking RS485 Ground Loops
- Isolated PROFIBUS-DP Networks

**TYPICAL APPLICATION**

Isolated Half-Duplex RS485 µModule Transceiver

LTM2881 Operating Through 35kV/μs CM Transients

Available current: 150mA (LTM2881-5) 100mA (LTM2881-3)

For more information [www.linear.com/LTM2881](http://www.linear.com/LTM2881)
ABSOLUTE MAXIMUM RATINGS

(Note 1)

V_{CC} to GND .......................................................... −0.3V to 6V
V_{CC2} to GND2 ....................................................... −0.3V to 6V
V_{L} to GND ............................................................. −0.3V to 6V

Interface Voltages
(A, B, Y, Z) to GND2 ...................... V_{CC2} −15V to 15V
(A-B) with Terminator Enabled ........... ±6V

Signal Voltages ON, RO, DI, DE,
RE, TE, D_{OUT} to GND ................... −0.3V to V_{L} +0.3V

Signal Voltages SL0,
D_{IN} to GND2 ..............................−0.3V to V_{CC2} +0.3V

Operating Temperature Range
LTM2881C .................................0°C to 70°C
LTM2881I ........................................−40°C to 85°C
LTM2881H ........................................−40°C to 105°C
LTM2881MP ..............................−55°C to 105°C

Maximum Internal Operating Temperature ...... 125°C

Storage Temperature Range .............−55°C to 150°C

Peak Package Body Reflow Temperature ........ 245°C

PIN CONFIGURATION

TOP VIEW

<table>
<thead>
<tr>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
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<tr>
<td>D_{OUT}</td>
<td>TE</td>
<td>DI</td>
<td>DE</td>
<td>RE</td>
<td>RO</td>
<td>V_{L}</td>
<td>ON</td>
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<tr>
<td>A</td>
<td>B</td>
<td>C</td>
<td>D</td>
<td>E</td>
<td>F</td>
<td>G</td>
<td>H</td>
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GND | V_{CC} |

GND2

<table>
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<tr>
<th>9</th>
<th>10</th>
<th>11</th>
<th>12</th>
<th>13</th>
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<th>16</th>
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<tbody>
<tr>
<td>D_{IN}</td>
<td>SLO</td>
<td>Y</td>
<td>Z</td>
<td>B</td>
<td>A</td>
<td>V_{CC2}</td>
<td></td>
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</tbody>
</table>

BGA PACKAGE
32-PIN (15mm × 11.25mm × 3.42mm)

T_{JMAX} = 125°C,
\theta_{JA} = 32.2°C/W,
\theta_{JCTOP} = 27.2°C/W,
\theta_{JCBOTTOM} = 20.9°C/W,
\theta_{JB} = 26.4°C/W,
WEIGHT = 1g

LGA PACKAGE
32-PIN (15mm × 11.25mm × 2.8mm)

T_{JMAX} = 125°C,
\theta_{JA} = 31.1°C/W,
\theta_{JCTOP} = 27.3°C/W,
\theta_{JCBOTTOM} = 19.5°C/W,
\theta_{JB} = 25.1°C/W,
WEIGHT = 1g

For more information www.linear.com/LTM2881
**ORDER INFORMATION**

http://www.linear.com/product/LTM2881#orderinfo

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>INPUT VOLTAGE</th>
<th>PAD OR BALL FINISH</th>
<th>PART MARKING DEVICE</th>
<th>FINISH CODE</th>
<th>PACKAGE TYPE</th>
<th>MSL RATING</th>
<th>TEMPERATURE RANGE</th>
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<tr>
<td>LTM2881CY-3#PBF</td>
<td>3V to 3.6V</td>
<td>SAC305 (RoHS)</td>
<td>LTM2881Y-3</td>
<td>e1</td>
<td>BGA</td>
<td>3</td>
<td>0°C to 70°C</td>
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<tr>
<td>LTM2881IY-3#PBF</td>
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<td>SnPb (63/37)</td>
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<td>e0</td>
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<tr>
<td>LTM2881HY-3</td>
<td></td>
<td>SAC305 (RoHS)</td>
<td>LTM2881Y-3</td>
<td>e1</td>
<td>BGA</td>
<td>3</td>
<td>−40°C to 105°C</td>
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<tr>
<td>LTM2881HY-3</td>
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<td>SnPb (63/37)</td>
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<tr>
<td>LTM2881HY-5#PBF</td>
<td>4.5V to 5.5V</td>
<td>SAC305 (RoHS)</td>
<td>LTM2881Y-5</td>
<td>e1</td>
<td>BGA</td>
<td>3</td>
<td>−40°C to 105°C</td>
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<tr>
<td>LTM2881HY-5</td>
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<td>SnPb (63/37)</td>
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<td>e0</td>
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<td>LTM2881MPY-3#PBF</td>
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<td>3</td>
<td>−55°C to 105°C</td>
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<tr>
<td>LTM2881MPY-3</td>
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<td>SnPb (63/37)</td>
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<td>e0</td>
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<tr>
<td>LTM2881CV-3#PBF</td>
<td>3V to 3.6V</td>
<td>Au (RoHS)</td>
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<tr>
<td>LTM2881IV-3#PBF</td>
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<td>SAC305 (RoHS)</td>
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<td>−40°C to 105°C</td>
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<tr>
<td>LTM2881HV-3#PBF</td>
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<td>SnPb (63/37)</td>
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<tr>
<td>LTM2881IV-5#PBF</td>
<td>4.5V to 5.5V</td>
<td>SAC305 (RoHS)</td>
<td>LTM2881V-5</td>
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<td>LGA</td>
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<td>−55°C to 105°C</td>
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<tr>
<td>LTM2881HV-5#PBF</td>
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<td>SnPb (63/37)</td>
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<td>e0</td>
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</table>

• Device temperature grade is indicated by a label on the shipping container.
• Pad or ball finish code is per IPC/JEDEC J-STD-609.
• Terminal Finish Part Marking: www.linear.com/leadfree
• This product is not recommended for second side reflow. For more information, go to: www.linear.com/BGA-assy
• Recommended BGA and LGA PCB Assembly and Manufacturing Procedures: www.linear.com/umodule/pcbassembly
• LGA and BGA Package and Tray Drawings: www.linear.com/packaging
• This product is moisture sensitive. For more information, go to: www.linear.com/umodule/pcbassembly
## ELECTRICAL CHARACTERISTICS

The ⚫ denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25°C$. LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5.0V$, $V_L = 3.3V$, $GND = GND2 = 0V$, $ON = V_L$ unless otherwise noted.

### SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS
--- | --- | --- | --- | --- | --- | ---
$V_{CC}$ | $V_{CC}$ Supply Voltage | LTM2881-3 | ⚫ | 3.0 | 3.3 | 3.6 | V
| | | LTM2881-5 | ⚫ | 4.5 | 5.0 | 5.5 | V
$V_L$ | $V_L$ Supply Voltage | | ⚫ | 1.62 | 5.5 | V
$I_{CCPOFF}$ | $V_{CC}$ Supply Current in Off Mode | $ON = 0V$ | | 0 | 10 | µA
$I_{CCS}$ | $V_{CC}$ Supply Current in On Mode | $LTM2881-3$ $DE = 0V$, $RE = V_L$, No Load | ⚫ | 20 | 30 | mA
| | | $LTM2881-5$ $DE = 0V$, $RE = V_L$, No Load | ⚫ | 15 | 25 | mA
$V_{CC2}$ | Regulated $V_{CC2}$ Output Voltage, Loaded | $LTM2881-3$ $DE = 0V$, $RE = V_L$, $ILOAD = 100mA$ | ⚫ | 4.75 | 5.0 | V
| | | $LTM2881-5$ $DE = 0V$, $RE = V_L$, $ILOAD = 150mA$ | ⚫ | 4.75 | 5.0 | V
| | | $LTM2881-3$, H/MP-Grade, $ILOAD = 90mA$ | ⚫ | 4.75 | | V
$V_{CC2NOLOAD}$ | Regulated $V_{CC2}$ Output Voltage, No Load | $DE = 0V$, $RE = V_L$, No Load | | 4.8 | 5.0 | 5.35 | V
Efficiency | | | | | | |
$I_{CC2S}$ | $V_{CC2}$ Short-Circuit Current | $DE = 0V$, $RE = V_L$, $V_{CC2} = 0V$ | | 62 | | %

### Driver

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | --- |
$|V_{OD}|$ | Differential Driver Output Voltage | $R = \infty$ (Figure 1) | ⚫ | 2.1 | $V_{CC2}$ | V
| | | $R = 27Ω$ (RS485) (Figure 1) | ⚫ | 2.1 | $V_{CC2}$ | V
| | | $R = 50Ω$ (RS422) (Figure 1) | ⚫ | | $V_{CC2}$ | V
$\Delta|V_{OD}|$ | Difference in Magnitude of Driver Differential Output Voltage for Complementary Output States | $R = 27Ω$ or $R = 50Ω$ (Figure 1) | ⚫ | 0.2 | | V
$V_{OC}$ | Driver Common Mode Output Voltage | $R = 27Ω$ or $R = 50Ω$ (Figure 1) | ⚫ | 3 | | V
$\Delta|V_{OC}|$ | Difference in Magnitude of Driver Common Mode Output Voltage for Complementary Output States | $R = 27Ω$ or $R = 50Ω$ (Figure 1) | ⚫ | 0.2 | | V
$I_{OZD}$ | Driver Three-State (High Impedance) Output Current on $Y$ and $Z$ | $DE = 0V$, $(Y \text{ or } Z) = –7V, +12V$ | ⚫ | ±10 | | µA
| | | $DE = 0V$, $(Y \text{ or } Z) = –7V, +12V$, H/MP-Grade | ⚫ | ±50 | | µA
$I_{OSD}$ | Maximum Driver Short-Circuit Current | $–7V \leq (Y \text{ or } Z) \leq 12V$ (Figure 2) | ⚫ | –250 | 250 | mA

### Receiver

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | --- |
$R_{IN}$ | Receiver Input Resistance | $RE = 0V$ or $V_L$, $V_{IN} = –7V, –3V, 3V, 7V, 12V$ (Figure 3) | ⚫ | 96 | 125 | kΩ
| | | $RE = 0V$ or $V_L$, $V_{IN} = –7V, –3V, 3V, 7V, 12V$ (Figure 3), H/MP-Grade | ⚫ | 48 | 125 | kΩ
$R_{TE}$ | Receiver Termination Resistance Enabled | $TE = V_L$, $V_{AB} = 2V$, $V_B = –7V, 0V, 10V$ (Figure 8) | ⚫ | 108 | 120 | 156 | Ω
$I_{IN}$ | Receiver Input Current (A, B) | $ON = 0V$ $V_{CC2} = 0V$ or $5V$, $V_{IN} = 12V$ (Figure 3) | ⚫ | 125 | | µA
| | | $ON = 0V$ $V_{CC2} = 0V$ or $5V$, $V_{IN} = 12V$ (Figure 3), H/MP-Grade | ⚫ | 250 | | µA
| | | $ON = 0V$ $V_{CC2} = 0V$ or $5V$, $V_{IN} = –7V$ (Figure 3) | ⚫ | –100 | | µA
| | | $ON = 0V$ $V_{CC2} = 0V$ or $5V$, $V_{IN} = –7V$ (Figure 3), H/MP-Grade | ⚫ | –145 | | µA
$V_{TH}$ | Receiver Differential Input Threshold Voltage (A-B) | $–7V \leq B \leq 12V$ | ⚫ | –0.2 | 0.2 | V
$\Delta V_{TH}$ | Receiver Input Failsafe Hysteresis | $B = 0V$ | | | 25 | mV
| | | $B = 0V$ | | | | |
$Receiver Input Failsafe Threshold | | | | | | |
**ELECTRICAL CHARACTERISTICS**  The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25°C$. LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5.0V$, $V_L = 3.3V$, $GND = GND2 = 0V$, $ON = V_L$ unless otherwise noted.

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<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<tr>
<td>$V_{IL}$</td>
<td>Logic Input Low Voltage</td>
<td>$1.62V \leq V_L \leq 5.5V$</td>
<td>●</td>
<td>0.4</td>
<td>V</td>
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<tr>
<td>$V_{IH}$</td>
<td>Logic Input High Voltage</td>
<td>$D_{IN}$, $S_{LO}$</td>
<td>$V_L \geq 2.35V$</td>
<td>●</td>
<td>0.67$V_{CC2}$</td>
<td>V</td>
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<td>$V_L = 1.62V \leq V_L &lt; 2.35V$</td>
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<td>2</td>
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<td>V</td>
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<tr>
<td>$I_{INL}$</td>
<td>Logic Input Current</td>
<td>●</td>
<td>0 ±1</td>
<td>μA</td>
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<tr>
<td>$V_{HYS}$</td>
<td>Logic Input Hysteresis</td>
<td>(Note 2)</td>
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<td>150</td>
<td>mV</td>
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<tr>
<td>$V_{OH}$</td>
<td>Output High Voltage</td>
<td>Output High, $I_{LOAD} = –4mA$ (Sourcing), $5.5V \geq V_L \geq 3V$</td>
<td>●</td>
<td>$V_L – 0.4$</td>
<td>V</td>
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<tr>
<td></td>
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<td>Output High, $I_{LOAD} = –1mA$ (Sourcing), $1.62V \leq V_L &lt; 3V$</td>
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<td>$V_L – 0.4$</td>
<td>V</td>
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<td>$V_{OL}$</td>
<td>Output Low Voltage</td>
<td>Output Low, $I_{LOAD} = 4mA$ (Sourcing), $5.5V \geq V_L \geq 3V$</td>
<td>●</td>
<td>0.4</td>
<td>V</td>
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<tr>
<td></td>
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<td>Output Low, $I_{LOAD} = 1mA$ (Sourcing), $1.62V \leq V_L &lt; 3V$</td>
<td></td>
<td>0.4</td>
<td>V</td>
<td></td>
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<tr>
<td>$I_{OZR}$</td>
<td>Three-State (High Impedance) Output Current on RO</td>
<td>$RE = V_L$, $0V \leq RO \leq V_L$</td>
<td>●</td>
<td>±1</td>
<td>μA</td>
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<tr>
<td>$I_{OSR}$</td>
<td>Short-Circuit Current</td>
<td>$0V \leq (RO or DOUT) \leq V_L$</td>
<td>●</td>
<td>±85</td>
<td>mA</td>
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</table>

**SWITCHING CHARACTERISTICS**  The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25°C$. LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5.0V$, $V_L = 3.3V$, $GND = GND2 = 0V$, $ON = V_L$ unless otherwise noted.

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<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<tr>
<td><strong>Driver $SLO = V_{CC2}$</strong></td>
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<tr>
<td>$I_{MAX}$</td>
<td>Maximum Data Rate</td>
<td>(Note 3)</td>
<td>20</td>
<td>Mbps</td>
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<tr>
<td>$I_{PLHD}$</td>
<td>Driver Input to Output</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>●</td>
<td>60</td>
<td>85</td>
<td>ns</td>
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<tr>
<td>$I_{PHLD}$</td>
<td></td>
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<tr>
<td>$\Delta I_P$</td>
<td>Driver Input to Output Difference</td>
<td>$I_{PLHD} – I_{PHLD}$</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>●</td>
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<td>8</td>
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<tr>
<td>$I_{SKEWD}$</td>
<td>Driver Output Y to Output Z</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>●</td>
<td>1</td>
<td>±8</td>
<td>ns</td>
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<tr>
<td>$I_{RD}$</td>
<td>Driver Rise or Fall Time</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>●</td>
<td>4</td>
<td>12.5</td>
<td>ns</td>
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<tr>
<td>$I_{ZLD}$, $I_{ZHD}$, $I_{LZD}$, $I_{HZD}$</td>
<td>Driver Output Enable or Disable Time</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$ (Figure 5)</td>
<td>●</td>
<td>170</td>
<td></td>
<td>ns</td>
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</table>

| **Driver $SLO = GND2$** |           |            |     |     |     |       |
| $I_{MAX}$ | Maximum Data Rate | (Note 3) | 250 | kbps |     |       |
| $I_{PLHD}$ | Driver Input to Output | $R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4) | 1   | 1.55 | μs |       |
| $I_{PHLD}$ |           |            |     |     |     |       |
| $\Delta I_P$ | Driver Input to Output Difference | $I_{PLHD} – I_{PHLD}$ | $R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4) | 50  | 500  | ns   |
| $I_{SKEWD}$ | Driver Output Y to Output Z | $R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4) | ±200 | ±500 | ns   |
| $I_{RD}$ | Driver Rise or Fall Time | $R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4) | ●   | 0.9 | 1.5  | μs   |
## SWITCHING CHARACTERISTICS

The * denotes the specifications which apply over the full operating temperature range; otherwise specifications are at $T_A = 25^\circ C$, LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5.0V$, $V_L = 3.3V$, $GND = GND2 = 0V$, $ON = V_L$ unless otherwise noted.

### SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS
--- | --- | --- | --- | --- | --- | ---
$t_{ZLD}$, $t_{ZHD}$, $t_{LZD}$, $t_{HZD}$ | Driver Output Enable or Disable Time | $R_L = 500\Omega$, $C_L = 50pF$ (Figure 5) | $\bullet$ | 400 | ns |

### Receiver

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | ---
$t_{PLHR}$ | Receiver Input to Output Time | $C_L = 15\mu F$, $V_{CM} = 2.5V$, $|V_{AB}| = 1.4V$, $t_R$ and $t_F < 4ns$ (Figure 6) | $\bullet$ | 100 | 140 | ns |
$t_{SKWWR}$ | Differential Receiver Skew $|t_{PLHR} - t_{PHLR}|$ | $C_L = 15\mu F$ (Figure 6) | $\bullet$ | 1 | 8 | ns |
$t_{RR}$ | Receiver Output Rise or Fall Time | $C_L = 15\mu F$ (Figure 6) | $\bullet$ | 3 | 12.5 | ns |
$t_{IZLR}$, $t_{IZHR}$, $t_{LZR}$, $t_{HZR}$ | Receiver Output Enable Time | $R_L = 1k\Omega$, $C_L = 15pF$ (Figure 7) | $\bullet$ | 50 | | ns |
$t_{RTEN}$, $t_{RTZ}$ | Termination Enable or Disable Time | $RE = 0V$, $DE = 0V$, $V_{AB} = 2V$, $V_B = 0V$ (Figure 8) | $\bullet$ | 100 | | µs |

### Generic Logic Input

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | ---
$t_{PHL1}$, $t_{PIL1}$ | $D_{IN}$ to $D_{OUT}$ Input to Output Time | $C_L = 15pF$, $t_R$ and $t_F < 4ns$ | $\bullet$ | 60 | 100 | ns |

### Power Supply Generator

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | ---
$V_{CC2}$–GND2 Supply Start-Up Time | ON _† $V_L$, No Load | $\bullet$ | 325 | 800 | µs |

## ISOLATION CHARACTERISTICS

$T_A = 25^\circ C$, LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5.0V$, $V_L = 3.3V$ unless otherwise noted.

### SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
--- | --- | --- | --- | --- | --- | ---
$V_{ISO}$ | Rated Dielectric Insulation Voltage | 1 Minute (Derived from 1 Second Test) | 2500 | | $V_{RMS}$ | |
| | | 1 Second (Notes 5, 6) | $\pm 4400$ | | $V_{DC}$ | |
| | Common Mode Transient Immunity | LTM2881-3 $V_{CC} = 3.3V$, LTM2881-5 $V_{CC} = 5V$, $V_L = ON = 3.3V$, $V_{CM} = 1kV$, $\Delta t = 33ns$ (Note 2) | | $\pm 30$ | | kV/µs |
$V_{VORM}$ | Maximum Working Insulation Voltage | (Notes 2, 5) | 560 | 400 | $V_{PEAK}$ | $V_{RMS}$ |
| | Partial Discharge | $V_{PR} = 1050$ $V_{PEAK}$ (Note 2) | | | 5 | pC |
| | Comparative Tracking Index | IEC 60112 (Note 2) | 600 | | $V_{RMS}$ | |
| | Depth of Erosion | IEC 60112 (Note 2) | 0.017 | | | mm |
| | Distance Through Insulation | (Note 2) | | | 0.06 | | mm |
| | Input to Output Resistance | (Notes 2, 5) | $10^9$ | | | Ω |
| | Input to Output Capacitance | (Notes 2, 5) | 6 | | | pF |
| | Creepage Distance | (Notes 2, 5) | 9.48 | | | mm |

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** Guaranteed by design and not subject to production test.

**Note 3:** Maximum Data rate is guaranteed by other measured parameters and is not tested directly.

**Note 4:** This µModule transceiver includes overtemperature protection that is intended to protect the device during momentary overload conditions. Junction temperature will exceed 125°C when overtemperature protection is active. Continuous operation above specified maximum operating junction temperature may result in device degradation or failure.

**Note 5:** Device considered a 2-terminal device. Pin group A1 through B8 shorted together and pin group K1 through L8 shorted together.

**Note 6:** The rated dielectric insulation voltage should not be interpreted as a continuous voltage rating.
TYPICAL PERFORMANCE CHARACTERISTICS

$V_C = 5.0V, V_L = 3.3V$ unless otherwise noted.

$T_A = 25^\circ C, \text{LTM2881}-3 \ V_{CC} = 3.3V, \text{LTM2881}-5$

- Receiver Skew vs Temperature
- Driver Skew vs Temperature
- Driver Propagation Delay vs Temperature
- Receiver Output Voltage vs Output Current (Source and Sink)
- Receiver Propagation Delay vs Temperature
- Driver Output Low/High Voltage vs Output Current
- Driver Differential Output Voltage vs Temperature
- Supply Current vs Data Rate

For more information www.linear.com/LTM2881
TYPICAL PERFORMANCE CHARACTERISTICS

VCC = 5.0V, VL = 3.3V unless otherwise noted.

Ta = 25°C, LTM2881-3 VCC = 3.3V, LTM2881-5

VCC Supply Current vs Temperature at ILOAD = 100mA on VCC2

VCC Surplus Current vs Temperature

VCC2 vs Load Current

VCC Power Efficiency

VCC Load Step (100mA)

VCC Noise
PIN FUNCTIONS

LOGIC SIDE (VCC, VL, GND)

DOUT (Pin A1): General Purpose Logic Output. Logic output connected through isolation path to DIN. Under the condition of an isolation communication failure DOUT is in a high impedance state.

TE (Pin A2): Terminator Enable. A logic high enables a termination resistor (typically 120Ω) between pins A and B.

DI (Pin A3): Driver Input. If the driver outputs are enabled (DE high), then a low on DI forces the driver noninverting output (Y) low and the inverting output (Z) high. A high on DI, with the driver outputs enabled, forces the driver noninverting output (Y) high and inverting output (Z) low.

DE (Pin A4): Driver Enable. A logic low disables the driver leaving the outputs Y and Z in a high impedance state. A logic high enables the driver.

RE (Pin A5): Receiver Enable. A logic low enables the receiver output. A logic high disables RO to a high impedance state.

RO (Pin A6): Receiver Output. If the receiver output is enabled (RE low) and if A – B is > 200mV, RO is a logic high, if A – B is < –200mV RO is a logic low. If the receiver inputs are open, shorted, or terminated without a valid signal, RO will be high. Under the condition of an isolation communication failure RO is in a high impedance state.

VL (Pin A7): Logic Supply. Interface supply voltage for pins RO, RE, TE, DI, DE, DOUT, and ON. Recommended operating voltage is 1.62V to 5.5V. Internally bypassed to GND with 2.2µF.

ON (Pin A8): Enable. Enables power and data communication through the isolation barrier. If ON is high the part is enabled and power and communications are functional to the isolated side. If ON is low the logic side is held in reset and the isolated side is unpowered.


VCC (Pins B6-B8): Supply Voltage. Recommended operating voltage is 3V to 3.6V for LTM2881-3 and 4.5V to 5.5V for LTM2881-5. Internally bypassed to GND with 2.2µF.

ISOLATED SIDE (VCC2, GND2)

DIN (Pin L1): General Purpose Isolated Logic Input. Logic input on the isolated side relative to VCC2 and GND2. A logic high on DIN will generate a logic high on DOUT. A logic low on DIN will generate a logic low on DOUT.

SLR (Pin L2): Driver Slew Rate Control. A low input, relative to GND2, will force the driver into a reduced slew rate mode for reduced EMI. A high input, relative to GND2, puts the driver into full speed mode to support maximum data rates.

Y (Pin L3): Non Inverting Driver Output. High impedance when the driver is disabled.

Z (Pin L4): Inverting Driver Output. High impedance when the driver is disabled.

B (Pin L5): Inverting Receiver Input. Impedance is > 96kΩ in receive mode with TE low or unpowered.

A (Pin L6): Non Inverting Receiver Input. Impedance is > 96kΩ in receive mode with TE low or unpowered.

VCC2 (Pins L7-L8): Isolated Supply Voltage. Internally generated from VCC by an isolated DC/DC converter and regulated to 5V. Internally bypassed to GND2 with 2.2µF.

GND2 (Pins K1-K8): Isolated Side Circuit Ground. The pads should be connected to the isolated ground and/or cable shield.
**Block Diagram**

![Block Diagram](image)

**Test Circuits**

**Figure 1. Driver DC Characteristics**

**Figure 2. Driver Output Short-Circuit Current**

**Figure 3. Receiver Input Current and Input Resistance**
## TEST CIRCUITS

**Figure 4. Driver Timing Measurement**

**Figure 5. Driver Enable and Disable Timing Measurements**

**Figure 6. Receiver Propagation Delay Measurements**
TEST CIRCUITS

Figure 7. Receiver Enable/Disable Time Measurements

Figure 8. Termination Resistance and Timing Measurements

FUNCTIONAL TABLE

<table>
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<tr>
<th>LOGIC INPUTS</th>
<th>MODE</th>
<th>A, B</th>
<th>Y, Z</th>
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<th>DC/DC CONVERTER</th>
<th>TERMINATOR</th>
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<td>1</td>
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<td>X</td>
<td>X</td>
<td>X</td>
<td>Off</td>
<td>R_{IN}</td>
<td>Hi-Z</td>
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Overview
The LTM2881 µModule transceiver provides a galvanically-isolated robust RS485/RS422 interface, powered by an integrated, regulated DC/DC converter, complete with decoupling capacitors. A switchable termination resistor is integrated at the receiver input to provide proper termination to the RS485 bus. The LTM2881 is ideal for use in networks where grounds can take on different voltages. Isolation in the LTM2881 blocks high voltage differences and eliminates ground loops and is extremely tolerant of common mode transients between ground potentials. Error free operation is maintained through common mode events greater than 30kV/µs providing excellent noise isolation.

µModule Technology
The LTM2881 utilizes isolator µModule technology to translate signals and power across an isolation barrier. Signals on either side of the barrier are encoded into pulses and translated across the isolation boundary using coreless transformers formed in the µModule substrate. This system, complete with data refresh, error checking, safe shutdown on fail, and extremely high common mode immunity, provides a robust solution for bidirectional signal isolation. The µModule technology provides the means to combine the isolated signaling with our RS485 transceiver and powerful isolated DC/DC converter in one small package.

DC/DC Converter
The LTM2881 contains a fully integrated isolated DC/DC converter, including the transformer, so that no external components are necessary. The logic side contains a full-bridge driver, running about 2MHz, and is AC-coupled to a single transformer primary. A series DC blocking capacitor prevents transformer saturation due to driver duty cycle imbalance. The transformer scales the primary voltage, and is rectified by a full-wave voltage doubler. This topology eliminates transformer saturation caused by secondary imbalances.

The DC/DC converter is connected to a low dropout regulator (LDO) to provide a regulated low noise 5V output. The internal power solution is sufficient to support the transceiver interface at its maximum specified load and data rate, and external pins are supplied for extra decoupling (optional) and heat dissipation. The logic supplies, VCC and VL have a 2.2µF decoupling capacitance to GND and the isolated supply VCC2 has a 2.2µF decoupling capacitance to GND2 within the µModule package.

VCC2 Output
The on-board DC/DC converter provides isolated 5V power to output VCC2. VCC2 is capable of supplying up to 1W of power at 5V in the LTM2881-5 option and up to 600mW of power in the LTM2881-3 option. This surplus current is available to external applications. The amount of surplus current is dependent upon the implementation and current delivered to the RS485 driver and line load. An example of available surplus current is shown in the Typical Performance Characteristics graph, VCC2 Surplus Current vs Temperature. Figure 19 demonstrates a method of using the VCC2 output directly and with a switched power path that is controlled with the isolated RS485 data channel.

Driver
The driver provides full RS485 and RS422 compatibility. When enabled, if DI is high, Y–Z is positive. When the driver is disabled, both outputs are high impedance with less than 10µA of leakage current over the entire common mode range of –7V to 12V, with respect to GND2.

Driver Overvoltage and Overcurrent Protection
The driver outputs are protected from short circuits to any voltage within the absolute maximum range of (VCC2 –15V) to (GND2 +15V) levels. The maximum VCC2 current in this condition is 250mA. If the pin voltage exceeds about ±10V, current limit folds back to about half of the peak value to reduce overall power dissipation and avoid damaging the part.

The device also features thermal shutdown protection that disables the driver and receiver output in case of excessive power dissipation (See Note 4 in the Electrical Characteristics section).

SLO Mode
The LTM2881 features a logic-selectable reduced slew rate mode (SLO mode) that softens the driver output edges to
reduce EMI emissions from equipment and data cables. The reduced slew rate mode is entered by taking the SLO pin low to GND2, where the data rate is limited to about 250kbps. Slew limiting also mitigates the adverse effects of imperfect transmission line termination caused by stubs or mismatched cables.

Figures 9a and 9b show the frequency spectrums of the LTM2881 driver outputs in normal and SLO mode operating at 250kbps. SLO mode significantly reduces the high frequency harmonics.

Receiver and Failsafe
With the receiver enabled, when the absolute value of the differential voltage between the A and B pins is greater than 200mV, the state of RO will reflect the polarity of (A-B). During data communication the receiver detects the state of the input with symmetric thresholds around 0V. The symmetric thresholds preserve duty cycle for attenuated signals with slow transition rates on high capacitive busses, or long cable lengths. The receiver incorporates a failsafe feature that guarantees the receiver output to be a logic-high during an idle bus, when the inputs are shorted, left open or terminated, but not driven. The failsafe feature eliminates the need for system level integration of network pre-biasing by guaranteeing a logic-high on RO under the conditions of an idle bus. Further network biasing constructed to condition transient noise during an idle state is unnecessary due to the common mode transient rejection of the LTM2881. The failsafe detector monitors A and B in parallel with the receiver and detects the state of the bus when A-B is above the input failsafe threshold for longer than about 3µs with a hysteresis of 25mV. This failsafe feature is guaranteed to work for inputs spanning the entire common mode range of –7V to 12V.

The receiver output is internally driven high (to V_L) or low (to GND) with no external pull-up needed. When the receiver is disabled the RO pin becomes Hi-Z with leakage of less than ±1µA for voltages within the supply range.

**Receiver Input Resistance**
The receiver input resistance from A or B to GND2 is greater than 96k permitting up to a total of 256 receivers per system without exceeding the RS485 receiver loading specification. High temperature H-/MP-Grade operation reduces the input resistance to 48k permitting 128 receivers on the bus. The input resistance of the receiver is unaffected by enabling/disabling the receiver or by powering/unpowering the part. The equivalent input resistance looking into A and B is shown in Figure 10.
APPLICATIONS INFORMATION

Switchable Termination

Proper cable termination is very important for signal fidelity. If the cable is not terminated with its characteristic impedance, reflections will distort the signal waveforms.

The integrated switchable termination resistor provides logic control of the line termination for optimal performance when configuring transceiver networks.

When the TE pin is high, the termination resistor is enabled and the differential resistance from A to B is 120Ω. Figure 11 shows the I/V characteristics between pins A and B with the termination resistor enabled and disabled. The resistance is maintained over the entire RS485 common mode range of –7V to 12V as shown in Figure 12. The integrated termination resistor has a high frequency response which does not limit performance at the maximum specified data rate. Figure 13 shows the magnitude and phase of the termination impedance versus frequency. The termination resistor cannot be enabled by TE if the device is unpowered, ON is low or the LTM2881 is in thermal shutdown.

Supply Current

The static supply current is dominated by power delivered to the termination resistance. Power supply current increases with data rate due to capacitive loading. Figure 14 shows supply current versus data rate for three different loads for the circuit configuration of Figure 4. Supply current increases with additional external applications drawing current from VCC2.
APPLICATIONS INFORMATION

PROFIBUS Applications

The LTM2881 can be used in PROFIBUS-DP networks where isolation is required. The standard PROFIBUS termination differs from RS485 termination and is shown in Figure 15. If used in this way, the internal termination should remain disabled (TE low). The 390Ω resistors in Figure 15 pre-bias the bus so that when the line is not driven, the receiver delivers a high output. Since the LTM2881 uses a fail-safe receiver, the pre-biasing resistors are not necessary and standard RS485 termination can be used with control from TE.

VCC2 provides an isolated source for the external termination resistor as shown in the Figure 15. When using the LTM2881 in PROFIBUS applications, it is recommended that no additional loads are connected to VCC2 in order to maintain the specified driver output swing.

Figure 15. PROFIBUS-DP Connections with Termination

PCB Layout Considerations

The high integration of the LTM2881 makes PCB layout very simple. However, to optimize its electrical isolation characteristics, EMI, and thermal performance, some layout considerations are necessary.

- Under heavily loaded conditions VCC and GND current can exceed 300mA. Sufficient copper must be used on the PCB to insure resistive losses do not cause the supply voltage to drop below the minimum allowed level. Similarly, the VCC2 and GND2 conductors must be sized to support any external load current. These heavy copper traces will also help to reduce thermal stress and improve the thermal conductivity.

- Input and Output decoupling is not required, since these components are integrated within the package. An additional bulk capacitor with a value of 6.8μF to 22μF is recommended. The high ESR of this capacitor reduces board resonances and minimizes voltage spikes caused by hot plugging of the supply voltage. For EMI sensitive applications, an additional low ESL ceramic capacitor of 1μF to 4.7μF, placed as close to the power and ground terminals as possible, is recommended. Alternatively, a number of smaller value parallel capacitors may be used to reduce ESL and achieve the same net capacitance.

- Do not place copper on the PCB between the inner columns of pads. This area must remain open to withstand the rated isolation voltage.

- The use of solid ground planes for GND and GND2 is recommended for non-EMI critical applications to optimize signal fidelity, thermal performance, and to minimize RF emissions due to uncoupled PCB trace conduction. The drawback of using ground planes, where EMI is of concern, is the creation of a dipole antenna structure which can radiate differential voltages formed between GND and GND2. If ground planes are used it is recommended to minimize their area, and use contiguous planes as any openings or splits can exacerbate RF emissions.

- For large ground planes a small capacitance (≤330pF) from GND to GND2, either discrete or embedded within the substrate, provides a low impedance current return path for the module parasitic capacitance, minimizing any high frequency differential voltages and substantially reducing radiated emissions. Discrete capacitance will not be as effective due to parasitic ESL. In addition, voltage rating, leakage, and clearance must be considered for component selection. Embedding the capacitance within the PCB substrate provides a near ideal capacitor and eliminates component selection issues; however, the PCB must be 4 layers. Care must be exercised in applying either technique to insure the voltage rating of the barrier is not compromised.

For more information www.linear.com/LTM2881
Figure 16a. Low EMI Demo Board Layout

Figure 16b. Low EMI Demo Board Layout (DC1746A), Top Layer

Figure 16c. Low EMI Demo Board Layout (DC1746A), Inner Layer 1
APPLICATIONS INFORMATION

Figure 16d. Low EMI Demo Board Layout (DC1746A), Inner Layer 2

Figure 16e. Low EMI Demo Board Layout (DC1746A), Bottom Layer

Figure 17. Low EMI Demo Board Emissions
APPLICATIONS INFORMATION

The PCB layout in Figures 16a to 16e show the low EMI demo board for the LTM2881. The demo board uses a combination of EMI mitigation techniques, including both embedded PCB bridge capacitance and discrete GND to GND2 capacitors. Two safety rated type Y2 capacitors are used in series, manufactured by Murata, part number GA342QR7GF471KW01L. The embedded capacitor effectively suppresses emissions above 400MHz, whereas the discrete capacitors are more effective below 400MHz.

EMI performance is shown in Figure 17, measured using a Gigahertz Transverse Electromagnetic (GTEM) cell and method detailed in IEC 61000-4-20, “Testing and Measurement Techniques – Emission and Immunity Testing in Transverse Electromagnetic Waveguides.”

Cable Length versus Data Rate

For a given data rate, the maximum transmission distance is bounded by the cable properties. A typical curve of cable length versus data rate compliant with the RS485 standard is shown in Figure 18. Three regions of this curve reflect different performance limiting factors in data transmission. In the flat region of the curve, maximum distance is determined by resistive loss in the cable. The downward sloping region represents limits in distance and rate due to the AC losses in the cable. The solid vertical line represents the specified maximum data rate in the RS485 standard. The dashed line at 250kbps shows the maximum data rate when SLO is low. The dashed line at 20Mbps shows the maximum data rate when SLO is high.

RF, Magnetic Field Immunity

The LTM2881 has been independently evaluated and has successfully passed the RF and magnetic field immunity testing requirements per European Standard EN 55024, in accordance with the following test standards:

- EN 61000-4-3 Radiated, Radio-Frequency, Electromagnetic Field Immunity
- EN 61000-4-8 Power Frequency Magnetic Field Immunity
- EN 61000-4-9 Pulsed Magnetic Field Immunity

Tests were performed using an unshielded test card designed per the data sheet PCB layout recommendations. Specific limits per test are detailed in Table 1.

Table 1

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<th>TEST</th>
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<td>1.4MHz to 2GHz</td>
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<td>2GHz to 2.7GHz</td>
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<td>EN 61000-4-9, Level 5</td>
<td>Pulse</td>
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*Non IEC Method

Figure 18. Cable Length vs Data Rate
Figure 19. Isolated System Fault Detection

Figure 20. Full-Duplex RS485 Connection

Figure 21. Switched 5V Power with Isolated CMOS Logic Connection with Low Voltage Interface

Figure 22. 4-Wire Full Duplex Self Biasing for Unshielded CAT5 Connection
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/product/LTM2881#packaging for the most recent package drawings.

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. ALL DIMENSIONS ARE IN MILLIMETERS
3. BALL DESIGNATION PER JESD MS-028 AND JEP95
4. DETAIL OF PIN #1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE PIN #1 IDENTIFIER MAY BE EITHER A MOLD OR MARKED FEATURE
5. PRIMARY DATUM Z IS SEATING PLANE
6. SOLDER BALL COMPOSITION IS 96.5% Sn/3.0% Ag/0.5% Cu
7. PACKAGE ROW AND COLUMN LABELING MAY VARY AMONG µModule PRODUCTS. REVIEW EACH PACKAGE LAYOUT CAREFULLY

BGA Package
32-Lead (15mm x 11.25mm x 3.42mm) (Reference LTC DWG # 05-08-1851 Rev D)

PACKAGE BOTTOM VIEW
PACKAGE TOP VIEW
PACKAGE SIDE VIEW
PACKAGE IN TRAY LOADING ORIENTATION

SUGGESTED PCB LAYOUT

For more information www.linear.com/LTM2881
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/product/LTM2881#packaging for the most recent package drawings.
# Revision History

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<td>Changes to Features, Description and Typical Application</td>
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<td>Add BGA Package to Pin Configuration, Order Information and Package Description Sections</td>
<td>2, 19</td>
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<td>Added lead finish part numbers</td>
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</tr>
<tr>
<td></td>
<td></td>
<td>Added CTI and DTI parameters</td>
<td>6</td>
</tr>
<tr>
<td>H</td>
<td>8/14</td>
<td>$I_{CCS}$, $V_{CC2}$ Short-Circuit Current: Deleted max spec. Added typical spec. Removed temp dot.</td>
<td>4</td>
</tr>
<tr>
<td>I</td>
<td>4/16</td>
<td>Added CSA information</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Changed $I_{CCS}$ limits</td>
<td>4</td>
</tr>
<tr>
<td>J</td>
<td>11/16</td>
<td>Corrected LGA Part Marking</td>
<td>3</td>
</tr>
</tbody>
</table>
TYPICAL APPLICATION

Figure 23. Multi-Node Network with End Termination and Single Ground Connection on Isolation Bus

RELATED PARTS

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTM2882</td>
<td>Dual Isolated RS232 µModule Transceiver + Power</td>
<td>1Mbps, ±10kV HBM ESD, 2500V_RMS</td>
</tr>
<tr>
<td>LTC1535</td>
<td>Isolated RS485 Transceiver</td>
<td>2500V_RMS Isolation in Surface Mount Package</td>
</tr>
<tr>
<td>LT1785</td>
<td>±60V Fault-Protected Transceiver</td>
<td>Half Duplex</td>
</tr>
<tr>
<td>LT1791</td>
<td>±60V Fault-Protected Transceiver</td>
<td>Full Duplex</td>
</tr>
<tr>
<td>LTC2861</td>
<td>20Mbps RS485 Transceivers with Integrated Switchable Termination</td>
<td>Full Duplex 15kV ESD</td>
</tr>
<tr>
<td>LTC2870/LTC2871</td>
<td>RS232/RS485 Multiprotocol Transceivers with Integrated Termination</td>
<td>20Mbps RS485 and 500kbps RS232, ±26kV ESD, 3V to 5V Operation</td>
</tr>
<tr>
<td>LTC2862/LTC2863/LTC2864/LTC2865</td>
<td>±60V Fault Protected 3V to 5.5V RS485/RS422 Transceivers</td>
<td>20Mbps or 250kbps, ±15kV HBM ESD, ±25V Common Mode Range</td>
</tr>
<tr>
<td>LTM2883</td>
<td>SPI/Digital or I²C Isolated µModule with Adjustable 5V and ±12V Rails</td>
<td>2500V_RMS Isolation with Power in BGA Package</td>
</tr>
<tr>
<td>LTM2892</td>
<td>SPI/Digital or I²C Isolated µModule</td>
<td>3500V_RMS Isolation, 6 Channels</td>
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</tbody>
</table>