FEATURES

- 20Mbps or Low EMI 250kbps Maximum Data Rate
- No Damage or Latchup to ESD: ±15kV HBM
- High Input Impedance Supports 256 Nodes (C, I-Grade)
- Operation Up to 125°C (H-Grade)
- Guaranteed Failsafe Receiver Operation Over the Entire Common Mode Range
- Current Limited Drivers and Thermal Shutdown
- Delayed Micropower Shutdown: 5μA Maximum (C, I-Grade)
- Power Up/Down Glitch-Free Driver Outputs
- Low Operating Current: 900μA Maximum in Receive Mode
- Compatible with TIA/EIA-485-A Specifications
- Available in 8-Lead and 10-Lead 3mm × 3mm DFN and 8-Lead and 10-Lead MSOP Packages

APPLICATIONS

- Low Power RS485/RS422 Transceiver
- Level Translator
- Backplane Transceiver

DESCRIPTION

The LTC®2856-1, LTC2856-2, LTC2857-1, LTC2857-2, LTC2858-1 and LTC2858-2 are low power, RS485/RS422 transceivers operating on 5V supplies at maximum data rates of 20Mbps or 250kbps for low EMI. The receiver has a one-eighth unit load, supporting up to 256 nodes per bus (C, I-Grade), and a failsafe feature that guarantees a high output state under conditions of floating or shorted inputs.

The driver maintains a high output impedance over the entire common mode range when disabled or when the supply is removed. Excessive power dissipation caused by bus contention or a fault is prevented by current limiting all outputs and by thermal shutdown.

Enhanced ESD protection allows these parts to withstand ±15kV (human body model) on the transceiver interface pins without latchup or damage.

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>MAX DATA RATE (Mbps)</th>
<th>DUPLEX</th>
<th>PACKAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC2856-1</td>
<td>20</td>
<td>Half</td>
<td>MSOP-8, DFN-8</td>
</tr>
<tr>
<td>LTC2856-2</td>
<td>0.25</td>
<td>Half</td>
<td>MSOP-8, DFN-8</td>
</tr>
<tr>
<td>LTC2857-1</td>
<td>20</td>
<td>Full</td>
<td>MSOP-8, DFN-8</td>
</tr>
<tr>
<td>LTC2857-2</td>
<td>0.25</td>
<td>Full</td>
<td>MSOP-8, DFN-8</td>
</tr>
<tr>
<td>LTC2858-1</td>
<td>20</td>
<td>Full</td>
<td>MSOP-10, DFN-10</td>
</tr>
<tr>
<td>LTC2858-2</td>
<td>0.25</td>
<td>Full</td>
<td>MSOP-10, DFN-10</td>
</tr>
</tbody>
</table>

For more information www.linear.com/LTC2856-1
ABSOLUTE MAXIMUM RATINGS

(Note 1)
Supply Voltage (V_{CC}) ................................... –0.3 V to 7 V
Logic Input Voltages (RE, DE, DI) ..................... –0.3 V to 7 V
Interface I/O (A, B, Y, Z) ........................ (V_{CC} – 15 V) to 15 V
Receiver Output Voltage (RO) ......................... –0.3 V to (V_{CC} + 0.3 V)

Operating Temperature Range (Note 4)
LTC285xC ................................................ 0 °C to 70°C
LTC285xI ............................................. –40 °C to 85°C
LTC285xH .......................................... –40 °C to 125°C

Storage Temperature Range
MSOP ................................................... –65°C to 150°C
DFN ..................................................... –65°C to 150°C

Lead Temperature (Soldering, 10 sec)
MSOP ................................................... 300°C

PACKAGE INFORMATION

LTC2856

TOP VIEW

RO 9
DE 8
DI 7
GND 6
VCC 5
B 4
A 3

DD PACKAGE
8-LEAD (3mm × 3mm) PLASTIC DFN
EXPOSED PAD (PIN 9) IS GND, MUST BE SOLDERED TO PCB
T_{J\text{MAX}} = 150°C, \theta_{JA} = 43°C/W, \theta_{JC} = 5.5°C/W

LTC2857

TOP VIEW

VCC 1
RO 10
DE 9
DI 8
GND 7
A 6
B 5
Y 4
Z 3

DD PACKAGE
8-LEAD (3mm × 3mm) PLASTIC DFN
EXPOSED PAD (PIN 9) IS GND, MUST BE SOLDERED TO PCB
T_{J\text{MAX}} = 150°C, \theta_{JA} = 43°C/W, \theta_{JC} = 5.5°C/W

LTC2858

TOP VIEW

RO 11
RE 10
DE 9
DI 8
GND 7
VCC 6
A 5
B 4
Y 3
Z 2

DD PACKAGE
10-LEAD (3mm × 3mm) PLASTIC DFN
EXPOSED PAD (PIN 11) IS GND, MUST BE SOLDERED TO PCB
T_{J\text{MAX}} = 150°C, \theta_{JA} = 43°C/W, \theta_{JC} = 5.5°C/W

For more information www.linear.com/LTC2856-1
ORDER INFORMATION

http://www.linear.com/product/LTC2856-1#orderinfo

LEAD FREE DESIGNATOR

TAPE AND REEL
TR = Tape and Reel

SPEED
–1 = 20Mbps Max. Data Rate
–2 = 250kbps Max. Data Rate

PACKAGE TYPE
DD = 8-Lead Plastic DFN (LTC2856, LTC2858)
MS8 = 8-Lead Plastic MSOP (LTC2856, LTC2857)
MS = 10-Lead Plastic MSOP (LTC2858)

TEMPERATURE GRADE
C = Commercial Temperature Range (0°C to 70°C)
I = Industrial Temperature Range (–40°C to 85°C)
H = Automotive Temperature Range (–40°C to 125°C)

PRODUCT PART NUMBER
LTC2856 = Half Duplex, with Enables
LTC2857 = Full Duplex, no Enables
LTC2858 = Full Duplex, with Enables

Consult LTC Marketing for parts specified with wider operating temperature ranges.
For more information on lead free part marking, go to: http://www.linear.com/leadfree/
For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/. Some packages are available in 500 unit reels through designated sales channels with #TRMPBF suffix.

PRODUCT SELECTION GUIDE

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PART MARKING</th>
<th>MAX DATA RATE (Mbps)</th>
<th>DUPLEX</th>
<th>LOW POWER SHUTDOWN MODE</th>
<th>PACKAGE</th>
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</thead>
<tbody>
<tr>
<td>LTC2856-1</td>
<td>LTCMF, LCMG</td>
<td>20</td>
<td>Half</td>
<td>Yes</td>
<td>MSOP-8, DFN-8</td>
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<tr>
<td>LTC2856-2</td>
<td>LTCMH, LCMJ</td>
<td>0.25</td>
<td>Half</td>
<td>Yes</td>
<td>MSOP-8, DFN-8</td>
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<tr>
<td>LTC2857-1</td>
<td>LTCMC, LCMD</td>
<td>20</td>
<td>Full</td>
<td>No</td>
<td>MSOP-8, DFN-8</td>
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<tr>
<td>LTC2857-2</td>
<td>LTCMK, LCMM</td>
<td>0.25</td>
<td>Full</td>
<td>No</td>
<td>MSOP-8, DFN-8</td>
</tr>
<tr>
<td>LTC2858-1</td>
<td>LTCGQ, LCGR</td>
<td>20</td>
<td>Full</td>
<td>Yes</td>
<td>MSOP-10, DFN-10</td>
</tr>
<tr>
<td>LTC2858-2</td>
<td>LTCMQ, LCMR</td>
<td>0.25</td>
<td>Full</td>
<td>Yes</td>
<td>MSOP-10, DFN-10</td>
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</tbody>
</table>
# Electrical Characteristics

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$. $V_{CC} = 5V$ unless otherwise noted. (Note 2)

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\Delta V_{OD}$</td>
<td>Change in Magnitude of Driver Common Mode Output Voltage for Complementary Output States</td>
<td>$R = 27\Omega$ or $50\Omega$ (Figure 1)</td>
<td>●</td>
<td>0.2</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$V_{OC}$</td>
<td>Driver Common Mode Output Voltage</td>
<td>$R = 27\Omega$ or $50\Omega$ (Figure 1)</td>
<td>●</td>
<td>3</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$\Delta V_{OC}$</td>
<td>Change in Magnitude of Driver Common Mode Output Voltage for Complementary Output States</td>
<td>$R = 27\Omega$ or $50\Omega$ (Figure 1)</td>
<td>●</td>
<td>0.2</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_{OZD}$</td>
<td>Driver Three-State (High Impedance) Output Current on Y and Z</td>
<td>$DE = 0V$, $(Y$ or $Z) = –7V$, 12V, LTC2858-1, LTC2858-2 H-Grade</td>
<td>●</td>
<td>±10</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>$I_{OSD}$</td>
<td>Maximum Driver Short-Circuit Current</td>
<td>–7V ≤ $(Y$ or $Z)$ ≤ 12V (Figure 2)</td>
<td>●</td>
<td>±120</td>
<td>±250</td>
<td>mA</td>
</tr>
<tr>
<td>$R_{IN}$</td>
<td>Receiver Input Resistance</td>
<td>$RE = V_{CC}$ or 0V, $DE = TE = 0V$, $V_{IN} = –7V$, 3V, 7V, 12V (Figure 3) (H-Grade)</td>
<td>●</td>
<td>96</td>
<td>125</td>
<td>kΩ</td>
</tr>
<tr>
<td>$V_{TH}$</td>
<td>Receiver Differential Input Threshold Voltage</td>
<td>–7V ≤ $B$ ≤ 12V</td>
<td>●</td>
<td>±0.2</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$\Delta V_{TH}$</td>
<td>Receiver Input Hysteresis</td>
<td>$B = 0V$</td>
<td>25</td>
<td>mV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{OZR}$</td>
<td>Receiver Three-State (High Impedance) Output Current on RO</td>
<td>$RE = 5V$, $0V ≤ RO ≤ V_{CC}$, LTC2856-1, LTC2856-2, LTC2858-1, LTC2858-2</td>
<td>●</td>
<td>±1</td>
<td>μA</td>
<td></td>
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<tr>
<td>$V_{IH}$</td>
<td>Logic Input High Voltage</td>
<td>$DE, DI, RE, V_{CC} = 5.5V$</td>
<td>●</td>
<td>2</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_{INL}$</td>
<td>Logic Input Low Voltage</td>
<td>$DE, DI, RE, V_{CC} = 4.5V$</td>
<td>●</td>
<td>0</td>
<td>±10</td>
<td>μA</td>
</tr>
<tr>
<td>$I_{CCS}$</td>
<td>Supply Current in Shutdown Mode</td>
<td>$DE = 0V$, $RE = V_{CC}$, LTC2856, LTC2858 (C and I-Grade)</td>
<td>●</td>
<td>0</td>
<td>5</td>
<td>μA</td>
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<tr>
<td>$I_{CCR}$</td>
<td>Supply Current in Receive Mode</td>
<td>No Load, $DE = 0V$, $RE = 0V$, LTC2856-1, LTC2856-2, LTC2858-1, LTC2858-2</td>
<td>●</td>
<td>540</td>
<td>900</td>
<td>μA</td>
</tr>
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</table>
SWITCHING CHARACTERISTICS

The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ$C. $V_{CC} = 5V$ unless otherwise noted. (Note 2)

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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</thead>
<tbody>
<tr>
<td>$I_{CCT}$</td>
<td>Supply Current in Transmit Mode</td>
<td>No Load, $DE = V_{CC}$, $RE = V_{CC}$</td>
<td>LTC2856-1</td>
<td>•</td>
<td>630</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2856-2</td>
<td>•</td>
<td>670</td>
<td>1100</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2858-1</td>
<td>•</td>
<td>660</td>
<td>1100</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2858-2</td>
<td>•</td>
<td>700</td>
<td>1200</td>
</tr>
<tr>
<td>$I_{CCTR}$</td>
<td>Supply Current with Both Driver and Receiver Enabled</td>
<td>No Load, $DE = V_{CC}$, $RE = 0V$</td>
<td>LTC2856-1</td>
<td>•</td>
<td>630</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2856-2</td>
<td>•</td>
<td>670</td>
<td>1100</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2858-1</td>
<td>•</td>
<td>660</td>
<td>1100</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>LTC2858-2</td>
<td>•</td>
<td>700</td>
<td>1200</td>
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</table>

Driver in LTC2856-1, LTC2857-1, LTC2858-1

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$f_{MAX}$</td>
<td>Maximum Data Rate</td>
<td>(Note 3)</td>
<td>•</td>
<td>20</td>
<td></td>
<td>Mbps</td>
</tr>
<tr>
<td>$t_{PLHD}, t_{PHLD}$</td>
<td>Driver Input to Output</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>10</td>
<td>50</td>
<td>ns</td>
</tr>
<tr>
<td>$\Delta t_{PD}$</td>
<td>Driver Input to Output Difference $</td>
<td>t_{PLHD} - t_{PHLD}</td>
<td>$</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>1</td>
</tr>
<tr>
<td>$t_{SKEWD}$</td>
<td>Driver Output Y to Output Z</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>1</td>
<td>±6</td>
<td>ns</td>
</tr>
<tr>
<td>$t_{RD}, t_{FD}$</td>
<td>Driver Rise or Fall Time</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>4</td>
<td>12.5</td>
<td>ns</td>
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<tr>
<td>$t_{ZLD}, t_{ZHD}, t_{LZD}, t_{HZD}$</td>
<td>Driver Enable or Disable Time</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$, $RE = 0V$, LTC2856-1, LTC2858-1 (Figure 5)</td>
<td>•</td>
<td>70</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>$t_{ZHSD}, t_{ZLSD}$</td>
<td>Driver Enable from Shutdown</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$, $RE = V_{CC}$, LTC2856-1, LTC2858-1 (Figure 5)</td>
<td>•</td>
<td>8</td>
<td></td>
<td>µs</td>
</tr>
<tr>
<td>$t_{SHDN}$</td>
<td>Time to Shutdown</td>
<td>(DE = ↓, $RE = V_{CC}$) or (DE = 0V, $RE = ↑$), LTC2856-1, LTC2858-1 (Figure 5)</td>
<td>•</td>
<td>100</td>
<td></td>
<td>ns</td>
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</table>

Driver in LTC2856-2, LTC2857-2, LTC2858-2

<table>
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<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$f_{MAXS}$</td>
<td>Maximum Data Rate</td>
<td>(Note 3)</td>
<td>•</td>
<td>250</td>
<td></td>
<td>kbps</td>
</tr>
<tr>
<td>$t_{PLHDS}, t_{PHLDS}$</td>
<td>Driver Input to Output</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>0.95</td>
<td>1.5</td>
<td>µs</td>
</tr>
<tr>
<td>$\Delta t_{PDS}$</td>
<td>Driver Input to Output Difference $</td>
<td>t_{PLHD} - t_{PHLD}</td>
<td>$</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>50</td>
</tr>
<tr>
<td>$t_{SKEWDS}$</td>
<td>Driver Output A to Output B</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>200</td>
<td>±500</td>
<td>ns</td>
</tr>
<tr>
<td>$t_{RDS}, t_{FDS}$</td>
<td>Driver Rise or Fall Time</td>
<td>$R_{DIFF} = 54\Omega$, $C_L = 100pF$ (Figure 4)</td>
<td>•</td>
<td>0.90</td>
<td>1.5</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{ZHDS}, t_{ZLDS}$</td>
<td>Driver Enable Time</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$, $RE = 0V$, LTC2856-2, LTC2858-2 (Figure 5)</td>
<td>•</td>
<td>300</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>$t_{LZDS}, t_{HZDS}$</td>
<td>Driver Disable Time</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$, $RE = 0V$, LTC2856-2, LTC2858-2 (Figure 5)</td>
<td>•</td>
<td>70</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>$t_{ZHSDS}, t_{ZLSDS}$</td>
<td>Driver Enable from Shutdown</td>
<td>$R_L = 500\Omega$, $C_L = 50pF$, $RE = V_{CC}$, LTC2856-2, LTC2858-2 (Figure 5)</td>
<td>•</td>
<td>8</td>
<td></td>
<td>µs</td>
</tr>
<tr>
<td>$t_{SHDNS}$</td>
<td>Time to Shutdown</td>
<td>(DE = 0V, $RE = ↑$) or (DE = 0V, $RE = \downarrow$), LTC2856-2, LTC2858-2 (Figure 5)</td>
<td>•</td>
<td>500</td>
<td></td>
<td>ns</td>
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</table>

Receiver

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$t_{PLHR}, t_{PHLR}$</td>
<td>Receiver Input to Output</td>
<td>$C_L = 15pF$, $V_{CM} = 1.5V$, $</td>
<td>V_{AB}</td>
<td>= 1.5V$, $t_q$ and $t_f &lt; 4ns$ (Figure 6)</td>
<td>•</td>
<td>50</td>
</tr>
<tr>
<td>$t_{SKEWR}$</td>
<td>Differential Receiver Skew, $</td>
<td>t_{PLHR} - t_{PHLR}</td>
<td>$</td>
<td>$C_L = 15pF$ (Figure 6)</td>
<td>•</td>
<td>1</td>
</tr>
<tr>
<td>$t_{RR}, t_{FR}$</td>
<td>Receiver Output Rise or Fall Time</td>
<td>$C_L = 15pF$ (Figure 6)</td>
<td>•</td>
<td>3</td>
<td>12.5</td>
<td>ns</td>
</tr>
<tr>
<td>$t_{ZLHR}, t_{ZHR}, t_{LZHR}, t_{HZHR}$</td>
<td>Receiver Enable/Disable</td>
<td>$R_L = 1k$, $C_L = 15pF$, $DE = V_{CC}$, LTC2856-1, LTC2856-2, LTC2858-1, LTC2858-2 (Figure 7)</td>
<td>•</td>
<td>50</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>$t_{ZHRS}, t_{ZLRS}$</td>
<td>Receiver Enable from Shutdown</td>
<td>$R_L = 1k$, $C_L = 15pF$, $DE = 0V$, LTC2856-1, LTC2856-2, LTC2858-1, LTC2858-2 (Figure 7)</td>
<td>•</td>
<td>8</td>
<td></td>
<td>µs</td>
</tr>
</tbody>
</table>
ELECTRICAL CHARACTERISTICS

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime. High temperatures degrade operating lifetimes. Operating lifetime is derated at temperatures greater than 105°C.

Note 2: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.

Note 3: Maximum data rate is guaranteed by other measured parameters and is not tested directly.

Note 4: This IC includes overtemperature protection that is intended to protect the device during momentary overload conditions. Overtemperature protection actives at a junction temperature exceeding 150°C. Continuous operation above the specified maximum operating junction temperature may result in device degradation or failure.

TEST CIRCUITS

Figure 1. Driver DC Characteristics

Figure 2. Driver Output Short-Circuit Current

Figure 3. Receiver Input Current and Input Resistance
TEST CIRCUITS

Figure 4. Driver Timing Measurement

Figure 5. Driver Enable and Disable Timing Measurement
**TEST CIRCUITS**

![Circuit Diagram](image)

**Figure 6. Receiver Propagation Delay Measurements**

![Circuit Diagram](image)

**Figure 7. Receiver Enable/Disable Time Measurements**

**FUNCTION TABLES**

**LTC2856-1, LTC2856-2**

<table>
<thead>
<tr>
<th>LOGIC INPUTS</th>
<th>MODE</th>
<th>A, B</th>
<th>RO</th>
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<tbody>
<tr>
<td>DE</td>
<td>RE</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>Receive</td>
<td>$R_{IN}$</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>Shutdown</td>
<td>$R_{IN}$</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Transceive</td>
<td>Driven</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>Transmit</td>
<td>Driven</td>
</tr>
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</table>

**LTC2858-1, LTC2858-2**

<table>
<thead>
<tr>
<th>LOGIC INPUTS</th>
<th>MODE</th>
<th>A, B</th>
<th>Y, Z</th>
<th>RO</th>
</tr>
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<tbody>
<tr>
<td>DE</td>
<td>RE</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>Receive</td>
<td>$R_{IN}$</td>
<td>High-Z</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>Shutdown</td>
<td>$R_{IN}$</td>
<td>High-Z</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Transceive</td>
<td>$R_{IN}$</td>
<td>Driven</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>Transmit</td>
<td>$R_{IN}$</td>
<td>Driven</td>
</tr>
</tbody>
</table>
### TYPICAL PERFORMANCE CHARACTERISTICS

For more information [www.linear.com/LTC2856-1](http://www.linear.com/LTC2856-1)

**Tₐ = 25°C, Vₐcc = 5V unless otherwise noted.**

#### Receiver Skew vs Temperature

- **V_{AB} = 1.5V**
- **C_L = 15pF**

#### Driver Skew vs Temperature

- **R_{DIFF} = 54Ω**
- **C_L = 100pF**

#### Driver Propagation Delay vs Temperature

- **R_{DIFF} = 54Ω**
- **C_L = 100pF**

#### Driver Output Short-Circuit Current vs Temperature

- **SOURCE (V_{OUT} = 0V)**
- **SINK (V_{OUT} = 5V)**

#### Driver Output Low/High Voltage vs Output Current

- **V_{OH}**
- **V_{OL}**

#### Driver Differential Output Voltage vs Temperature

- **R_{DIFF} = ∞**
- **R_{DIFF} = 100Ω**
- **R_{DIFF} = 54Ω**

#### Receiver Output Voltage vs Output Current (Source and Sink)

- **SOURCE**
- **SINK**

#### Receiver Propagation Delay vs Temperature

- **V_{AB} = 1.5V**
- **C_L = 15pF**

#### Supply Current vs Data Rate

- **C_L = 100pF**
- **R_{DIFF} = 54Ω**
- **R_{DIFF} = 100Ω**
- **R_{DIFF} = ∞**

**TEMPERATURE (°C)**
- –40
- 0
- 20
- 40
- 60
- 80
- 100
- 120

**V_{AB} = 1.5V**

**C_L = 15pF**

**C_L = 100pF**

**R_{DIFF} = 54Ω**

**R_{DIFF} = ∞**
# PIN FUNCTIONS

<table>
<thead>
<tr>
<th>PIN NAME</th>
<th>PIN NUMBER</th>
<th>DESCRIPTION</th>
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<tbody>
<tr>
<td>RO</td>
<td>1/2/1</td>
<td>Receiver Output. If the receiver output is enabled (RE low) and A &gt; B by 200mV, then RO will be high. If A &lt; B by 200mV, then RO will be low. If the receiver inputs are open, shorted or terminated without a valid signal, RO will be high.</td>
</tr>
<tr>
<td>RE</td>
<td>2/2/2</td>
<td>Receiver Enable. A low enables the receiver. A high input forces the receiver output into a high impedance state.</td>
</tr>
<tr>
<td>DE</td>
<td>3/3/3</td>
<td>Driver Enable. A high on DE enables the driver. A low input will force the driver outputs into a high impedance. If RE is high with DE low, the part will enter a low power shutdown state.</td>
</tr>
<tr>
<td>DI</td>
<td>4/4/4</td>
<td>Driver Input. If the driver outputs are enabled (DE high), then a low on DI forces the driver positive output low and negative output high. A high on DI, with the driver outputs enabled, forces the driver positive output high and negative output low.</td>
</tr>
<tr>
<td>GND</td>
<td>5/9*/4/9* 5/11*</td>
<td>Ground. *The Exposed Pad on the DFN packages should be connected to ground.</td>
</tr>
<tr>
<td>Y</td>
<td>5/6/5</td>
<td>Noninverting Driver Output for the LTC2857-1, LTC2857-2, LTC2858-1 and LTC2858-2. High impedance when the driver is disabled (LTC2858-1) or unpowered.</td>
</tr>
<tr>
<td>Z</td>
<td>6/7/6</td>
<td>Inverting Driver Output for the LTC2857-1, LTC2857-2, LTC2858-1 and LTC2858-2. High impedance when the driver is disabled (LTC2858-1) or unpowered.</td>
</tr>
<tr>
<td>B</td>
<td>7/7/7</td>
<td>Inverting Receiver Input (and Inverting Driver Output for the LTC2856-1 and LTC2856-2). Impedance is &gt; 96kΩ in receive mode or unpowered.</td>
</tr>
<tr>
<td>A</td>
<td>6/8/6</td>
<td>Noninverting Receiver Input (and Noninverting Driver Output for the LTC2856-1 and LTC2856-2). Impedance is &gt; 96kΩ in receive mode or unpowered.</td>
</tr>
<tr>
<td>VCC</td>
<td>8/1/10</td>
<td>Positive Supply. 4.5V &lt; VCC &lt; 5.5V. Bypass with a 0.1µF ceramic capacitor.</td>
</tr>
</tbody>
</table>
**BLOCK DIAGRAMS**

**LTC2856-1, LTC2856-2**

```
RECEIVER

SLEEP/SHUTDOWN
LOGIC AND DELAY

DRIVER
```

**LTC2857-1, LTC2857-2**

```
RECEIVER

DRIVER
```

**LTC2858-1, LTC2858-2**

```
RECEIVER

SLEEP/SHUTDOWN
LOGIC AND DELAY

DRIVER
```
APPLICATIONS INFORMATION

Driver

The driver provides full RS485 and RS422 compatibility. When enabled, if DI is high, Y-Z is positive for the full-duplex devices and A-B is positive for the half-duplex device.

When the driver is disabled, both outputs are high impedance. For the full-duplex devices, the leakage on the driver output pins is guaranteed to be less than 10μA over the entire common mode range of –7V to 12V. On the half-duplex device, the impedance is dominated by the receiver input resistance, \( R_{IN} \).

Driver Overvoltage and Overcurrent Protection

The driver outputs are protected from short circuits to any voltage within the Absolute Maximum range of \( (V_{CC} - 15V) \) to 15V. The maximum current in this condition is 250mA. If the pin voltage exceeds about ±10V, current limit folds back to about half of the peak value to reduce overall power dissipation and avoid damaging the part.

All devices also feature thermal shutdown protection that disables the driver and receiver output in case of excessive power dissipation (see Note 4).

Slew Limiting for EMI Emissions Control

The LTC2856-2, LTC2857-2 and the LTC2858-2 feature reduced slew rate driver outputs to control the high frequency EMI emissions from equipment and data cables. These devices are limited to data rates of 250kbaud or less. Slew limiting also mitigates the adverse affects of imperfect transmission line termination caused by stubs or mismatched cable.

Figures 10 and 11 show the output waveforms from the LTC2858-1 and its slew rate limited counterpart, the LTC2858-2, operating at 250kbps. The corresponding frequency spectrums show significant reduction in the high frequency harmonics for the slew rate limited device.

Receiver and Failsafe

With the receiver enabled, when the absolute value of the differential voltage between the A and B pins is greater than 200mV, the state of RO will reflect the polarity of (A-B).

These parts have a failsafe feature that guarantees the receiver output to be in a logic-high state when the inputs are either shorted, left open or terminated, but not driven for more than about 3μs. The delay prevents signal zero crossings from being interpreted as shorted inputs and causing RO to go high inadvertently. This failsafe feature is guaranteed to work for inputs spanning the entire common mode range of –7V to 12V.

The receiver output is internally driven high (to \( V_{CC} \)) or low (to ground) with no external pull-up needed. When the receiver is disabled the RO pin becomes high-Z with leakage of less than ±1μA for voltages within the supply range.

Receiver Input Resistance

The receiver input resistance from A or B to ground is guaranteed to be greater than 96k (C, I-Grade). This is 8× higher than the requirements for the RS485 standard and thus this receiver represents a one-eighth unit load. This, in turn, means that 8× the standard number of receivers, or 256 total, can be connected to a line without loading it beyond what is called out in the RS485 standard. The receiver input resistance from A or B to ground on high temperature H-Grade parts is greater than 48k providing a one-quarter unit load. The input resistance of the receivers is unaffected by enabling/disabling the receiver and by powering/unpowering the part.

Supply Current

The unloaded static supply currents in these devices are very low—typically under 700μA for all modes of operation. In applications with resistively terminated cables, the supply current is dominated by the driver load. For example, when using two 120Ω terminators with a differential driver output voltage of 2V, the DC load current is 33mA, which is sourced by the positive voltage supply. Power supply current increases with toggling data due to capacitive loading and this term can increase significantly at high data rates. Figure 8 shows supply current vs data rate for two different capacitive loads for the circuit configuration of Figure 4.
APPLICATIONS INFORMATION

High Speed Considerations

A ground plane layout is recommended. A 0.1µF bypass capacitor less than one-quarter inch away from the VCC pin is also recommended. The PC board traces connected to signals A/B and Z/Y should be symmetrical and as short as possible to maintain good differential signal integrity. To minimize capacitive effects, the differential signals should be separated by more than the width of a trace and should not be routed on top of each other if they are on different signal planes.

Care should be taken to route outputs away from any sensitive inputs to reduce feedback effects that might cause noise, jitter or even oscillations. For example, in the full-duplex LTC2857-1, DI and A/B should not be routed near the driver or receiver outputs.

The logic inputs have 100mV of hysteresis to provide noise immunity. Fast edges on the outputs can cause glitches in the ground and power supplies which are exacerbated by capacitive loading. If a logic input is held near its threshold (typically 1.5V), a noise glitch from a driver transition may exceed the hysteresis levels on the logic and data input pins causing an unintended state change. This can be avoided by maintaining normal logic levels on the pins and by slewing inputs through their thresholds by faster than 1V/µs when transitioning. Good supply decoupling and proper line termination also reduce glitches caused by driver transitions.

Cable Length vs Data Rate

For a given data rate, the maximum transmission distance is bounded by the cable properties. A typical curve of cable length vs data rate compliant with the RS485 standard is shown in Figure 9. Three regions of this curve reflect different performance limiting factors in data transmission. In the flat region of the curve, maximum distance is determined by resistive losses in the cable. The downward sloping region represents limits in distance and data rate due to AC losses in the cable. The solid vertical line represents the specified maximum data rate in the RS485 standard. The dashed line at 250kbps shows the maximum data rate of the low-EMI LTC2856-2, LTC2857-2, and LTC2858-2. The dashed line at 20Mbps shows the maximum data rates of the LTC2856-1, LTC2857-1 and LTC2858-1.

Cable Termination

Proper cable termination is very important for good signal fidelity. If the cable is not terminated with its characteristic impedance, reflections will result in distorted waveforms.

RS485 transceivers typically communicate over twisted-pair cables with characteristic impedance ranging from 100Ω to 120Ω. Each end of the network should be terminated with a discrete resistor matching the characteristic impedance or with an LTC2859/LTC2861 transceiver with integrated termination capability.
APPLICATIONS INFORMATION

TYPICAL APPLICATIONS

Failsafe “0” Application (Idle State = Logic “0”)

Figure 10. LTC2858-1 Driver Output at 100kHz Into 100Ω Resistor

Figure 11. LTC2858-2 Driver Output at 100kHz Into 100Ω Resistor
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/product/LTC2856-1#packaging for the most recent package drawings.

DD Package
8-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1698 Rev C)

NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-1)
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON TOP AND BOTTOM OF PACKAGE

For more information www.linear.com/LTC2856-1
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/product/LTC2856-1#packaging for the most recent package drawings.

DD Package
10-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1699 Rev C)

NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-2).
   CHECK THE LTC WEBSITE DATA SHEET FOR CURRENT STATUS OF VARIATION ASSIGNMENT
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE TOP AND BOTTOM OF PACKAGE
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/product/LTC2856-1#packaging for the most recent package drawings.

MS8 Package
8-Lead Plastic MSOP
(Reference LTC DWG # 05-08-1660 Rev G)

NOTE:
1. DIMENSIONS IN MILLIMETER/(INCH)
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
   MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
   INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX

For more information www.linear.com/LTC2856-1
MS Package
10-Lead Plastic MSOP

(REference LTC DWG # 05-08-1661 Rev F)

NOTE:
1. DIMENSIONS IN MILLIMETER/INCH
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
   MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
   INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX
## REVISION HISTORY  (Revision history begins at Rev F)

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<th>REV</th>
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<tr>
<td>F</td>
<td>11/13</td>
<td>Corrected $\theta_{JA}$ and $\theta_{JC}$ values.</td>
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<tr>
<td>G</td>
<td>09/17</td>
<td>Added H-Grade test condition for $I_{QZD}$</td>
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Multi-Node Network and End Termination Using the LTC2856-1

RELATED PARTS

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<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
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<tbody>
<tr>
<td>LTC485</td>
<td>Low Power RS485 Interface Transceiver</td>
<td>( I_{CC} = 300\mu A ) (Typ)</td>
</tr>
<tr>
<td>LTC491</td>
<td>Differential RS485 Driver and Receiver Pair</td>
<td>( I_{CC} = 300\mu A )</td>
</tr>
<tr>
<td>LTC1480</td>
<td>3.3V Ultralow Power RS485 Transceiver</td>
<td>3.3V Operation</td>
</tr>
<tr>
<td>LTC1483</td>
<td>Ultralow Power RS485 Low EMI Transceiver</td>
<td>Controlled Driver Slew Rate</td>
</tr>
<tr>
<td>LTC1485</td>
<td>Differential Bus Transceiver</td>
<td>10Mbaud Operation</td>
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<tr>
<td>LTC1487</td>
<td>Ultralow Power RS485 with Low EMI, Shutdown and High Input Impedance</td>
<td>Up to 256 Transceiver on the Bus</td>
</tr>
<tr>
<td>LTC1520</td>
<td>50Mbps Precision Quad Line Receiver</td>
<td>Channel-to-Channel Skew 400ps (Typ)</td>
</tr>
<tr>
<td>LTC1535</td>
<td>Isolated RS485 Full-Duplex Transceiver</td>
<td>2500V_{RMS} Isolation in Surface Mount Package</td>
</tr>
<tr>
<td>LTC1685</td>
<td>52Mbps RS485 Transceiver with Precision Delay</td>
<td>Propagation Delay Skew 500ps (Typ)</td>
</tr>
<tr>
<td>LT1785</td>
<td>60V Fault Protected RS485 Transceiver</td>
<td>60V Tolerant, ( \pm 15)kV ESD</td>
</tr>
<tr>
<td>LTC2859/LTC2861</td>
<td>20Mbps RS485 Transceivers with Integrated Switchable Termination</td>
<td>Integrated, Switchable, 120\Omega Termination Resistor, ( \pm 15)kV ESD</td>
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