FEATURES

- Fully Enhances N-Channel Power MOSFETs
- 8µA Iq Standby Current
- 85µA Iq ON Current
- No External Charge Pump Capacitors
- 4.5V to 18V Supply Range
- Short-Circuit Protection
- Thermal Shutdown via PTC Thermistor
- Status Output Indicates Shutdown
- Available in 8-Pin SOIC and PDIP Packages

APPLICATIONS

- Laptop Computer Power Switching
- SCSI Termination Power Switching
- Cellular Telephone Power Management
- Battery Charging and Management
- High Side Industrial and Automotive Switching
- Stepper Motor and DC Motor Control

DESCRIPTION

The LTC®1154 single high side gate driver allows using low cost N-channel FETs for high side switching applications. An internal charge pump boosts the gate drive voltage above the positive rail, fully enhancing an N-channel MOS switch with no external components. Micropower operation, with 8µA standby current and 85µA operating current, allows use in virtually all systems with maximum efficiency.

Included on chip is programmable overcurrent sensing. A time delay can be added to prevent false triggering on high inrush current loads. An active high shutdown input is also provided and interfaces directly to a standard PTC thermistor for thermal shutdown. An open-drain output is provided to report switch status to the µP. An active low enable input is provided to control multiple switches in banks.

The LTC1154 is available in both 8-pin DIP and 8-pin SOIC packages.
ABSOLUTE MAXIMUM RATINGS
(Note 1)

Supply Voltage .......................................................... 22V
Input Voltage .......................................................... (V_S + 0.3V) to (GND – 0.3V)
Enable Input Voltage .................................................. (V_S + 0.3V) to (GND – 0.3V)
Gate Voltage ........................................................... (V_S + 24V) to (GND – 0.3V)
Status Output Voltage ............................................... 15V
Current (Any Pin) ........................................................ 50mA

Operating Temperature
LTC1154C ............................................................. 0°C to 70°C
LTC1154H ............................................................. −40°C to 150°C
Storage Temperature Range ................................−65°C to 150°C
Lead Temperature (Soldering, 10 sec.) .............. 300°C

PIN CONFIGURATION

ORDER INFORMATION (http://www.linear.com/product/LTC1154#orderinfo)

LEAD FREE FINISH

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>TAPE AND REEL</th>
<th>PART MARKING</th>
<th>PACKAGE DESCRIPTION</th>
<th>TEMPERATURE RANGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC1154CN8#PBF</td>
<td>LTC1154CN8#TRPBF</td>
<td>LTC1154</td>
<td>8-Lead Plastic DIP</td>
<td>0°C to 70°C</td>
</tr>
<tr>
<td>LTC1154CS8#PBF</td>
<td>LTC1154CS8#TRPBF</td>
<td>1154</td>
<td>8-Lead Plastic SOIC</td>
<td>0°C to 70°C</td>
</tr>
<tr>
<td>LTC1154HS8#PBF</td>
<td>LTC1154HS8#TRPBF</td>
<td>1154H</td>
<td>8-Lead Plastic SOIC</td>
<td>−40°C to 150°C</td>
</tr>
</tbody>
</table>

LEAD BASED FINISH

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>TAPE AND REEL</th>
<th>PART MARKING</th>
<th>PACKAGE DESCRIPTION</th>
<th>TEMPERATURE RANGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC1154CN8</td>
<td>LTC1154CN8#TR</td>
<td>LTC1154</td>
<td>8-Lead Plastic DIP</td>
<td>0°C to 70°C</td>
</tr>
<tr>
<td>LTC1154CS8</td>
<td>LTC1154CS8#TR</td>
<td>1154</td>
<td>8-Lead Plastic SOIC</td>
<td>0°C to 70°C</td>
</tr>
<tr>
<td>LTC1154HS8</td>
<td>LTC1154HS8#TR</td>
<td>1154H</td>
<td>8-Lead Plastic SOIC</td>
<td>−40°C to 150°C</td>
</tr>
</tbody>
</table>

Consult LTC Marketing for parts specified with wider operating temperature ranges.
For more information on lead free part marking, go to: http://www.linear.com/leadfree/
For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/. Some packages are available in 500 unit reels through designated sales channels with #TRMPBF suffix.

ELECTRICAL CHARACTERISTICS

The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_S = 4.5V to 18V, T_A = 25°C, V_EN = 0V, V_SD = 0V unless otherwise noted.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_S</td>
<td>Supply Voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>I_Q</td>
<td>Quiescent Current OFF</td>
<td>V_S = 5V, V_IN = 0V</td>
<td>4.5</td>
<td>18</td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td>Quiescent Current On</td>
<td>V_S = 5V, V_IN = 5V</td>
<td></td>
<td>85</td>
<td>120</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td>Quiescent Current ON</td>
<td>V_S = 12V, V_IN = 5V</td>
<td></td>
<td>180</td>
<td>400</td>
<td>µA</td>
</tr>
<tr>
<td>V_INH</td>
<td>Input High Voltage</td>
<td>•</td>
<td>2</td>
<td></td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$, $V_S = 4.5\text{V}$ to $18\text{V}$, $T_A = 25^\circ\text{C}$, $V_{EN} = 0\text{V}$, $V_{SD} = 0\text{V}$ unless otherwise noted.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{INL}$</td>
<td>Input Low Voltage</td>
<td>●</td>
<td>0.8</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{IN}$</td>
<td>Input Current</td>
<td>$0V &lt; V_{IN} &lt; V_S$ ●</td>
<td>±1</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$C_{IN}$</td>
<td>Input Capacitance</td>
<td></td>
<td>5</td>
<td>pF</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{ENH}$</td>
<td>ENABLE Input High Voltage</td>
<td>●</td>
<td>3.5</td>
<td>2.6</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$V_{ENL}$</td>
<td>ENABLE Input Low Voltage</td>
<td>●</td>
<td>1</td>
<td>0.6</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_{EN}$</td>
<td>ENABLE Input Current</td>
<td>$0V &lt; V_{EN} &lt; V_S$ ●</td>
<td>±1</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{SDH}$</td>
<td>Shutdown Input High Voltage</td>
<td>●</td>
<td>2</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{SDL}$</td>
<td>Shutdown Input Low Voltage</td>
<td>●</td>
<td>0.8</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{SD}$</td>
<td>Shutdown Input Current</td>
<td>$0V &lt; V_{SD} &lt; V_S$ ●</td>
<td>±1</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{SEN}$</td>
<td>Drain Sense Threshold Voltage</td>
<td></td>
<td>80</td>
<td>100</td>
<td>120</td>
<td>mV</td>
</tr>
<tr>
<td>$I_{SEN}$</td>
<td>Drain Sense Input Current</td>
<td>$0V &lt; V_{SEN} &lt; V_S$ ●</td>
<td>±0.1</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{GATE} - V_S$</td>
<td>Gate Voltage Above Supply</td>
<td>$V_S = 5\text{V}$ ●</td>
<td>6</td>
<td>7</td>
<td>9</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_S = 6\text{V}$ ●</td>
<td>7.5</td>
<td>8.3</td>
<td>15</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_S = 12\text{V}$ ●</td>
<td>15</td>
<td>18</td>
<td>25</td>
<td>V</td>
</tr>
<tr>
<td>$V_{STATUS}$</td>
<td>Status Output Low Voltage</td>
<td>$I_{STATUS} = 400\mu\text{A}$ ●</td>
<td>0.05</td>
<td>0.4</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_{STATUS}$</td>
<td>Status Output Leakage Current</td>
<td>$V_{STATUS} = 12\text{V}$ ●</td>
<td>1</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$t_{ON}$</td>
<td>Turn-ON Time</td>
<td>$V_S = 5\text{V}, C_{GATE} = 1000\text{pF}$</td>
<td>30</td>
<td>110</td>
<td>300</td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Time for $V_{GATE} &gt; V_S + 2V$ 100</td>
<td>450</td>
<td>1000</td>
<td>μs</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_S = 12\text{V}, C_{GATE} = 1000\text{pF}$</td>
<td>20</td>
<td>80</td>
<td>200</td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Time for $V_{GATE} &gt; V_S + 5V$</td>
<td>50</td>
<td>160</td>
<td>500</td>
<td>μs</td>
</tr>
<tr>
<td>$t_{OFF}$</td>
<td>Turn-OFF Time</td>
<td>$V_S = 5\text{V}, C_{GATE} = 1000\text{pF}$</td>
<td>10</td>
<td>36</td>
<td>60</td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Time for $V_{GATE} &lt; 1V$</td>
<td>10</td>
<td>28</td>
<td>60</td>
<td>μs</td>
</tr>
<tr>
<td>$t_{SC}$</td>
<td>Short-Circuit Turn-OFF Time</td>
<td>$V_S = 5\text{V}, C_{GATE} = 1000\text{pF}$</td>
<td>5</td>
<td>25</td>
<td>40</td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Time for $V_{GATE} &lt; 1V$</td>
<td>5</td>
<td>23</td>
<td>40</td>
<td>μs</td>
</tr>
<tr>
<td>$t_{SD}$</td>
<td>Shutdown Turn-OFF Time</td>
<td>$V_S = 5\text{V}, C_{GATE} = 1000\text{pF}$</td>
<td>17</td>
<td>40</td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Time for $V_{GATE} &lt; 1V$</td>
<td>13</td>
<td>35</td>
<td></td>
<td>μs</td>
</tr>
</tbody>
</table>

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.
TYPICAL PERFORMANCE CHARACTERISTICS

**Standby Supply Current**

- $V_{IN} = 0V$
- $T_A = 25^\circ C$

**Supply Current ON**

- $T_A = 25^\circ C$

**High Side Gate Voltage**

**Input Threshold Voltage**

**Drain Sense Threshold Voltage**

**Low Side Gate Voltage**

**Turn-ON Time**

- $C_{GATE} = 1000pF$

**Turn-OFF Time**

- $C_{GATE} = 1000pF$

- TIME FOR $V_{GATE} < 1V$

- $V_{SEN} = V_{GS} - 1V$

- NO EXTERNAL DELAY

**Short-Circuit Turn-OFF Delay Time**
**TYPICAL PERFORMANCE CHARACTERISTICS**

**Standby Supply Current**

$V_{IN} = 0V$, $V_{EN} = 0V$

- $V_S = 18V$
- $V_S = 5V$

**Supply Current ON**

$V_{IN} = 5V$, $V_{EN} = 0V$

- $V_S = 12V$
- $V_S = 5V$

**Input ON Threshold Voltage**

- $V_S = 5V$
- $V_S = 18V$

**Shutdown Threshold Voltage**

- $V_S = 5V$
- $V_S = 18V$

**ENABLE Threshold Voltage**

- $V_S = 12V$
- DISABLE
- ENABLE

**Gate Drive Current**

$T_A = 25^\circ C$

- $V_S = 18V$
- $V_S = 12V$
- $V_S = 5V$

**PIN FUNCTIONS**

**IN and SD (Pins 1, 5):** Input and Shutdown Pins. The LTC1154 input pin is active high and activates all of the protection and charge pump circuitry when switched ON. The shutdown pin is designed to immediately disable the switch if a secondary fault condition (over temperature, etc.) is detected. The LTC1154 logic and shutdown inputs are high impedance CMOS gates with ESD protection diodes to ground and supply and therefore should not be forced beyond the power supply rails. The shutdown pin should be connected to ground when not in use.

**EN (Pin 2):** ENABLE Input Pin. The ENABLE input can be used to enable a number of LTC1154 high side switches in banks or to provide a secondary means of control. It can also act as an inverting input. The ENABLE input is a high impedance CMOS gate with ESD clamp diodes to ground and supply and therefore should not be forced beyond the power supply rails. This pin should be grounded when not in use.
PIN FUNCTIONS

G (Pin 6): Gate Drive Pin. The gate drive pin is either driven to ground when the switch is turned OFF or driven above the supply rail when the switch is turned ON. This pin is a relatively high impedance when driven above the rail (the equivalent of a few hundred kΩ). Care should be taken to minimize any loading of this pin by parasitic resistance to ground or supply.

V_S (Pin 8): Supply Pin. The supply pin of the LTC1154 serves two vital purposes. The first is obvious: it powers the input, gate drive, regulation and protection circuitry. The second purpose is less obvious: it provides a Kelvin connection to the top of the drain sense resistor for the internal 100mV reference.

The LTC1154 is designed to be continuously powered so that the gate of the MOSFET is actively driven at all times. If it is necessary to remove power from the supply pin and then re-apply it, the input pin (or enable pin) should be cycled a few milliseconds after the power is re-applied to reset the input latch and protection circuitry. Also, the input and enable pins should be isolated with 10k resistors to limit the current flowing through the ESD protection diodes to the supply pin.

The supply pin of the LTC1154 should never be forced below ground as this may result in permanent damage to the device. A 300Ω resistor should be inserted in series with the ground pin if negative supply voltage transients are anticipated.

DS (Pin 7): Drain Sense Pin. The drain sense pin is compared against the supply pin voltage. If the voltage at this pin is more than 100mV below the supply pin, the input latch will be reset and the MOSFET gate will be quickly discharged. Cycle the input, or ENABLE input, to reset the short-circuit latch and turn the MOSFET back on.

This pin is also a high impedance CMOS gate with ESD protection and therefore should not be forced beyond the power supply rails. To defeat the overcurrent protection, short the drain sense to supply.

Some loads, such as large supply capacitors, lamps, or motors require high inrush currents. An RC time delay can be added between the sense resistor and the drain sense pin to ensure that the drain sense circuitry does not false-trigger during start-up. This time constant can be set from a few microseconds to many seconds. However, very long delays may put the MOSFET in risk of being destroyed by a short-circuit condition. (see Applications Information Section).

STATUS (Pin 3): STATUS Pin. The STATUS pin is an open-drain output which is driven low whenever a fault condition is detected. A 51k pull-up resistor should be connected between this output and a logic supply. The STATUS pins of multiple LTC1154s can be OR’d together if independent fault sensing is not required. No connection is required to this pin when not in use.

BLOCK DIAGRAM

For more information www.linear.com/LTC1154
The LTC1154 is a single micropower MOSFET driver with built-in protection, status feedback and gate charge pump. The LTC1154 consists of the following functional blocks:

**TTL and CMOS Compatible Inputs**

The LTC1154 input and shutdown input have been designed to accommodate a wide range of logic families. Both input thresholds are set at about 1.3V with approximately 100mV of hysteresis.

A low standby current voltage regulator provides continuous bias for the TTL-to-CMOS converter. The TTL-to-CMOS converter output enables the rest of the circuitry. In this way the power consumption is kept to a minimum in the standby mode.

**ENABLE Input**

The ENABLE input is CMOS compatible and inhibits the input signal whenever it is held logic high. This input should be grounded when not in use.

**Internal Voltage Regulation**

The output of the TTL-to-CMOS converter drives two regulated supplies which power the low voltage CMOS logic and analog blocks. The regulator outputs are isolated from each other so that the noise generated by the charge pump logic is not coupled into the 100mV reference or the analog comparator.

**Gate Charge Pump**

Gate drive for the MOSFET switch is produced by an adaptive charge pump circuit which generates a gate voltage substantially higher than the power supply voltage. The charge pump capacitors are included on chip and therefore no external components are required to generate the gate drive.

**Drain Current Sense**

The LTC1154 is configured to sense the current flowing into the drain of the power MOSFET in a high side application. An internal 100mV reference is compared to the drop across a sense resistor (typically 0.002Ω to 0.10Ω) in series with the drain lead. If the drop across this resistor exceeds the internal 100mV threshold, the input latch is reset and the gate is quickly discharged via a large N-channel transistor.

**Controlled Gate Rise and Fall Times**

When the input is switched ON and OFF, the gate is charged by the internal charge pump and discharged in a controlled manner. The charge and discharge rates have been set to

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**TRUTH TABLE**

<table>
<thead>
<tr>
<th>INPUTS</th>
<th>OUTPUTS</th>
<th>SWITCH CONDITION</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN</td>
<td>EN</td>
<td>SD</td>
</tr>
<tr>
<td>X</td>
<td>H</td>
<td>X</td>
</tr>
<tr>
<td>L</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>L</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>L</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>L</td>
</tr>
</tbody>
</table>

L = LOGIC LOW  
H = LOGIC HIGH  
X = IRRELEVANT  
\( \downarrow \) L = EDGE TRIGGERED

The Truth Table demonstrates how the LTC1154 receives inputs and returns status information to the µP. The ENABLE and input signal from the µP controls the switch in its normal operating mode, where the rise and fall time of the gate drive are controlled to limit EMI and RFI emissions. The shutdown and overcurrent detection circuitry however, switch the gate off at a much higher rate to limit the exposure of the MOSFET switch and the load to dangerous conditions. The STATUS pin remains high as long as the switch is operating normally, and is driven low only when a fault condition is detected. Note that the shutdown pin is edge-sensitive and latches the output off even if the shutdown pin returns to a low state.
minimize RFI and EMI emissions in normal operation. If a short-circuit or current overload condition is encountered, the gate is discharged very quickly (typically a few microseconds) by a large N-channel transistor.

**Status Output Driver**

The status circuitry continuously monitors the fault detection logic. This open-drain output is driven low when the gate of the MOSFET is driven low by the protection circuitry. The status circuitry is reset along with the input latch when the input, or ENABLE input, is cycled.

**APPLICATIONS INFORMATION**

**MOSFET and Load Protection**

The LTC1154 protects the power MOSFET switch by removing drive from the gate as soon as an overcurrent condition is detected. Resistive and inductive loads can be protected with no external time delay in series with the drain sense pin. Lamp loads, however, require that the overcurrent protection be delayed long enough to start the lamp but short enough to ensure the safety of the MOSFET.

**Resistive Loads**

Loads that are primarily resistive should be protected with as short a delay as possible to minimize the amount of time that the MOSFET is subjected to an overload condition. The built-in delay of approximately 10µs to eliminate false triggering by power supply or load transient conditions. This delay is sufficient to “mask” short load current transients and the starting of a small capacitor (<1µF) in parallel with the load. The drain sense pin can therefore be connected directly to the drain current sense resistor as shown in Figure 1.

**Inductive Loads**

Loads that are primarily inductive, such as relays, solenoids and stepper motor windings should be protected with as short a delay as possible to minimize the amount of time that the MOSFET is subjected to an overload condition. The built-in 10µs delay will ensure that the overcurrent protection is not false-triggered by a supply or load transient. No external delay components are required as shown in Figure 2.

Large inductive loads (>0.1mH) may require diodes connected directly across the inductor to safely divert the stored energy to ground. Many inductive loads have these diodes included. If not, a diode of the proper current rating should be connected across the load, as shown in Figure 2, to safely divert the stored energy.
APPLICATIONS INFORMATION

Capacitive Loads

Large capacitive loads, such as complex electrical systems with large bypass capacitors, should be powered using the circuit shown in Figure 3. The gate drive to the power MOSFET is passed through an RC delay network, R1 and C1, which greatly reduces the turn-on ramp rate of the switch. And since the MOSFET source voltage follows the gate voltage, the load is powered smoothly and slowly from ground. This dramatically reduces the start-up current flowing into the supply capacitor(s) which, in turn, reduces supply transients and allows for slower activation of sensitive electrical loads. (Diode, D1, provides a direct path for the LTC1154 protection circuitry to quickly discharge the gate in the event of an overcurrent condition).

Lamp Loads

The inrush current created by a lamp during turn-on can be 10 to 20 times greater than the rated operating current. The circuit shown in Figure 4 shifts the current limit threshold up by a factor of 11:1 (to 30A) for 100ms when the bulb is first turned on. The current limit then drops down to 2.7A after the inrush current has subsided.

The RC network, R_D and C_D, in series with the drain sense input should be set to trip based on the expected characteristics of the load after start-up. With this circuit, it is possible to power a large capacitive load and still react quickly to an overcurrent condition. The ramp rate at the output of the switch as it lifts off ground is approximately:

\[ \frac{dV}{dt} = \frac{(V_G - V_{TH})}{(R1 \cdot C1)} \]

And therefore the current flowing into the capacitor during start-up is approximately:

\[ I_{START-UP} = C_{LOAD} \cdot \frac{dV}{dt} \]

Using the values shown in Figure 3, the start-up current is less than 100mA and does not false-trigger the drain sense circuitry which is set at 2.7A with a 1ms delay.
The Y axis of the graph is normalized to one RC time constant. The X axis is normalized to the current. (The set current is defined as the current required to develop 100mV across the drain sense resistor).

Note that the shutdown time is shorter for increasing levels of MOSFET current. This ensures that the total energy dissipated by the MOSFET is always within the bounds established by the manufacturer for safe operation. (See MOSFET data sheet for further information).

Using a Speed-Up Diode

To reduce the amount of time that the power MOSFET is in a short-circuit condition, “bypass” the delay resistor with a small signal diode as shown in Figure 6. The diode will engage when the drop across the drain sense resistor exceeds about 0.7V, providing a direct path to the sense pin and dramatically reducing the amount of time the MOSFET is in an overload condition. The drain sense resistor value is selected to limit the maximum DC current to 2.8A. The diode conducts when the drain current exceeds 20A and reduces the turn-off time to 15µs.

Reverse Battery Protection

The LTC1154 can be protected against reverse battery conditions by connecting a resistor in series with the ground lead as shown in Figure 7. The resistor limits the supply current to less than 50mA with −12V applied.

Current Limited Power Supplies

The LTC1154 requires at least 3.5V at the supply pin to ensure proper operation. It is therefore necessary that the supply to the LTC1154 be held higher than 3.5V at all times, even when the output of the switch is short circuited to ground. The output voltage of a current limited regulator may drop very quickly during short-circuit and pull the supply pin of the LTC1154 below 3.5V before the shutdown circuitry has had time to respond and remove drive from the gate of the power MOSFET. A supply filter should be...
added as shown in Figure 8 which holds the supply pin of the LTC1154 high long enough for the overcurrent shutdown circuitry to respond and fully discharge the gate.

Five volt linear regulators with small output capacitors are the most difficult to protect as they can “switch” from a voltage mode to a current limited mode very quickly. The large output capacitors on many switching regulators may be able to hold the supply pin of the LTC1154 above 3.5V sufficiently long that this extra filtering is not required.

Because the LTC1154 is micropower in both the standby and ON state, the voltage drop across the supply filter is less than 2mV, and does not significantly alter the accuracy of the 100mV drain sense threshold voltage.

**TYPICAL APPLICATIONS**

High Side Driver with Thermal Shutdown

![Diagram of High Side Driver with Thermal Shutdown](image)

High Side Driver with Overvoltage Shutdown

![Diagram of High Side Driver with Overvoltage Shutdown](image)

High Side Driver with Undervoltage Shutdown

![Diagram of High Side Driver with Undervoltage Shutdown](image)

24V to 28V High Side Switch with Thermal Shutdown

![Diagram of 24V to 28V High Side Switch with Thermal Shutdown](image)

*A 51k pull-up resistor should be connected between Status Output and 5V Logic Supply.

*OPTIONAL IF SUPPLY VOLTAGE LESS THAN 6V.

**CAPACITOR CHARGED TO SUPPLY VOLTAGE. SHUTDOWN OCCURS WHEN SUPPLY VOLTAGE DROPS BY 0.6V.
**TYPICAL APPLICATIONS**

**24V to 28V Switch with Bootstrapped Supply**

**High Side Relay Driver with Overcurrent Protection and Status Feedback**

*KEYSTONE RL2006-100-100-30-PT. MOUNT ON MOSFET OR LOAD HEAT SINK. I_{Q(ON)} = 1mA, I_{Q(OFF)} = 600μA.*

†A 51k pull-up resistor should be connected between Status Output and 5V Logic Supply.

**“4-Cell-to-5V” Extremely Low Voltage Drop Regulator with Overcurrent Shutdown, Status Feedback, Ramped Turn-ON and 8μA Standby Current**

470μF ESR < 0.5Ω
TYPICAL APPLICATIONS

Bank Controlled High Side Switches with “Global” Thermal and Overvoltage Shutdown

*KEYSTONE RL2006-100-100-30-PT. MOUNT ON COMMON HEAT SINK.
TYPICAL APPLICATIONS

12V Step-Up Regulator with Ultralow Standby Current, Overcurrent Protection and Status Feedback

12V Step-Up Regulator with 1A Overcurrent Protection, Switch Status Feedback and Ramped Output

For more information www.linear.com/LTC1154
**PACKAGE DESCRIPTION**

Please refer to [http://www.linear.com/product/LTC1154#packaging](http://www.linear.com/product/LTC1154#packaging) for the most recent package drawings.

**N Package**
8-Lead PDIP (Narrow .300 Inch)
(Reference LTC DWG # 05-08-1510 Rev I)

**NOTE:**
1. DIMENSIONS ARE INCHES
2. MILLIMETERS

*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)
S8 Package
8-Lead Plastic Small Outline (Narrow .150 Inch)
(Reference LTC DWG # 05-08-1610 Rev G)

NOTE:
1. DIMENSIONS IN INCHES
2. DRAWING NOT TO SCALE
3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
   MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)
4. PIN 1 CAN BE BEVEL EDGE OR A DIMPLE

For more information www.linear.com/LTC1154
## REVISION HISTORY

(Revision history begins at Rev B)

<table>
<thead>
<tr>
<th>REV</th>
<th>DATE</th>
<th>DESCRIPTION</th>
<th>PAGE NUMBER</th>
</tr>
</thead>
</table>
| B   | 4/11 | Updated Graph TPC05  
Updated SCSI Termination Typical Application  
Updated Related Parts | 4  
18  
18 |
| C   | 2/16 | Corrected Pin Names | 2, 3, 5, 6 |

Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.
# LTC1154

## Typical Applications

**Auto-Reset High Side Switch with Overcurrent and Overcurrent Temperature Shutdown**

![Diagram of Auto-Reset High Side Switch](image)

**SCSI Termination Power Switch with 1A Overcurrent Shutdown, Auto-Reset and Load Soft-Start**

![Diagram of SCSI Termination Power Switch](image)

## Related Parts

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC4440/LTC4440-5</td>
<td>High Speed, High Voltage High Side Gate Driver</td>
<td>Up to 80V Supply Voltage, 8V ≤ V&lt;sub&gt;CC&lt;/sub&gt; ≤ 15V, 2.4A Peak Pull-Up/1.5Ω Peak Pull-Down</td>
</tr>
<tr>
<td>LTC4441/LTC4441-1</td>
<td>N-Channel MOSFET Gate Driver</td>
<td>Up to 25V Supply Voltage, 5V ≤ V&lt;sub&gt;CC&lt;/sub&gt; ≤ 25V, 6A Peak Output Current</td>
</tr>
<tr>
<td>LT1910</td>
<td>Protected High Side Gate Driver</td>
<td>Up to 48V Supply Voltage, Short Circuit Protected</td>
</tr>
<tr>
<td>LTC4444</td>
<td>High Voltage Synchronous N-Channel MOSFET Driver</td>
<td>Up to 100V Supply Voltage, 7.2V ≤ V&lt;sub&gt;CC&lt;/sub&gt; ≤ 13.5V, 3A Peak Pull-Up/0.55Ω Peak Pull-Down</td>
</tr>
<tr>
<td>LTC4444/LTC4444-5</td>
<td>High Voltage Synchronous N-Channel MOSFET Driver with Shoot Thru Protection</td>
<td>Up to 100V Supply Voltage, 4.5V/7.2V ≤ V&lt;sub&gt;CC&lt;/sub&gt; ≤ 13.5V, 3A Peak Pull-Up/0.55Ω Peak Pull-Down</td>
</tr>
<tr>
<td>LTC4442/LTC4449</td>
<td>High Speed Synchronous N-Channel MOSFET Driver</td>
<td>Up to 38V Supply Voltage, 4.5V/6V ≤ V&lt;sub&gt;CC&lt;/sub&gt; ≤ 9.5V, 3.2A Peak Pull-Up/4.5A Peak Pull-Down</td>
</tr>
</tbody>
</table>