

# SELECTED ITEM DRAWING

			
Unless otherwise specified <b>DIMENSIONS ARE IN INCHES (MM)</b>	<b>TOLERANCES:</b> .XX +/- 0.010 .XXX +/- 0.005 .XXXX +/- 0.002 ANGLES +/- .5 DEG	Drawing practices per <b>ASME Y14.100</b>	<b>DC to 6 GHz, Gain Block Amplifier</b>
			SIZE <b>A</b>

**SID000038** **Rev. B**  
 Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective companies.

One Analog Way, Wilmington, MA 01887-2356, U.S.A.  
 Tel: 781.935.5565 [www.analog.com](http://www.analog.com)  
 Tel: 800.262.5643 © 2024 Analog Devices, Inc. All rights reserved.

## 1. SCOPE

### 1.1. Scope

This drawing establishes the requirements for the DC to 6 GHz, GaAs, InGaP, HBT, MMIC, Gain Block Amplifier, to be screened in accordance with MIL-PRF-38534, Class K, to the requirements specified in 4.1, and 4.2 herein.

### 1.2. Analog Devices Part Number

<u>Generic Part Number</u>	<u>Screened Part Number</u>
ADH311S	HMC8430

## 2. APPLICABLE DOCUMENTS

### 2.1. Government Documents

Unless otherwise specified, the following drawings and standards, of the issue in effect on the date of the accepted purchase order, in the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, shall form a part of this drawing to the extent specified herein.

#### **DEPARTMENT OF DEFENSE TEST METHOD STANDARD**

MIL-STD-883            Microcircuits

#### **DEPARTMENT OF DEFENSE PERFORMANCE SPECIFICATIONS**

MIL-PRF-38534        Hybrid Microcircuits, General Specifications For

### 2.2. Non-Government Documents

The following documents, of the issue in effect on the date of the purchase order, form a part of this drawing to the extent specified herein:

#### **Analog Devices Inc.**

HMC311LP3 / 311LP3E Data Sheet    Commercial Product Datasheet            v07.0420 (Reference Only)

### **3. REQUIREMENTS**

#### **3.1. General Requirements**

The devices delivered shall comply to this specification.

#### **3.2. Design Construction and Physical Dimensions**

The design construction and physical dimensions shall be as defined in Figure 1 herein.

#### **3.3. Traceability**

Each delivered device shall be traceable to the wafer number and the wafer lot number of each device. Inspection lot records shall be maintained to provide traceability to the specific wafer and wafer lot from which the chips originated.

#### **3.4. Burn-In and Life Test Circuit**

The burn-in and life test circuit and conditions shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test methods 1005 and 1015 per MIL-STD-883.

### **4. QUALITY ASSURANCE PROVISIONS**

#### **4.1. Wafer Acceptance Testing**

The wafer lot acceptance testing shall consist of Class Level S inspections per MIL-STD-883 TM 5007. All deliverable Die shall have passed Element evaluation per 4.2.2 and passed 100% visual inspection, per MIL-STD-883 TM2010 Condition A, after wafer dicing.

#### **4.2. Dice Qualification Requirements**

Dice qualification requirements shall be in accordance with MIL-PRF-38534, Appendix C, Table C-II, per Class K.

##### **4.2.1. Element Electrical Test (RF-On Wafer)**

Die are not able to be electrically tested on-wafer. Delivered Die will be picked from the center of the wafer only and inspected per MIL-STD-883, Method 2010A.

##### **4.2.2. Element Evaluation (Lot Acceptance Testing)**

Sample Die shall be randomly selected from inspected Die per 4.2.1 and shall be mounted to suitable fixturing for screening.

###### **4.2.2.1. Initial Electrical Test (Pre Burn-In)**

Pre Burn-In electrical tests shall consist of the tests specified on Table I, tested at room temperature only.

###### **4.2.2.2. Interim Electrical Test (Post Burn-In)**

Post Burn-In electrical tests shall consist of the tests specified on Table I, tested at room temperature only and shall meet performance requirements in accordance with Table I and Table II.

###### **4.2.2.3. Final Electrical Test (Post Life Test)**

Final electrical tests shall consist of the tests specified in Table II, tested at -40 °C and +85 °C, and shall meet performance requirements in accordance with Table I and Table II.

## 5. MIL-PRF-38534 EXCEPTIONS

### 5.1. Wafer Fabrication

Foundry information is available upon request.

### 5.2. Microcircuit Dice Evaluation Requirements (TABLE C-II)

- Pre-screen test post assembly required to Die qualification to remove all assembly related rejects.
- Subgroup 4: mechanical shock or constant acceleration not performed.
- Subgroup 4: Interim and post burn-in electrical tests will include tests screened at +25 °C only.
- Subgroup 4: Qty/Acc = 22/0.

## 6. PREPARATION FOR DELIVERY

The preparation for delivery, packaging, preservation, ESD protection and handling shall be in accordance with MIL-PRF-38534.

### 6.1. Die Packaging Information

The Die shall be delivered in accordance with Table III herein.

### 6.2. Inspection Data Requirements

The following data shall accompany each shipment.

- a. A Certificate of Conformance (C of C) certifies that the lot(s) meets all requirements of this specification.
- b. Die Photograph
- c. Attribute data for all tests.
- d. Wafer Lot Acceptance data including photos from SEM inspection in 4.1.
- e. Element Evaluation test results per 4.2.2.
  - a. Variables data for electrical end-point measurements.
- f. Failure Analysis with photos (If applicable)
- g. A cover sheet indicating the following purchasing information:
  1. Customer purchase order number.
  2. Analog Devices part number.
  3. Part lot identification codes.
  4. Date & quantity shipped.

**TABLE I: ELECTRICAL CHARACTERISTICS FOR QUALIFICATION SAMPLES**

Parameter	Conditions <u>1/2/3/</u> Unless otherwise specified	Sub-Group	Limits		Units
			Min	Max	
Gain	RFIN = 0.5 GHz, 1.5 GHz, 2 GHz, 4.5 GHz, and 6 GHz	4, 5, 6	10.5		dB
Gain Variation Over Temperature	RFIN = 0.5 GHz, 1.5 GHz, and 2.0 GHz	5, 6		0.008	dB/°C
	RFIN = 4.5 GHz, and 6 GHz	5, 6		0.016	dB/°C
Output Power for 1 dB Compression (OP1dB)	RFIN = 0.5 GHz, 1.5 GHz, 2 GHz, 4.5 GHz, and 6 GHz	4, 5, 6	8.5		dBm
Supply Current (Icq)	No Signal at RF IN	4, 5, 6		74	mA

TABLE I Notes:

1/ Limits apply at +25 °C only with Vs = +5 V, and Rbias = 22 Ohm.

2/ Data taken with broadband bis tee on device output.

3/ See MIL-PRF-38534 Table C-Xa for subgroup parameter definitions.

**TABLE II: BURN-IN/LIFE TEST DELTA LIMITS 1/2/3/**

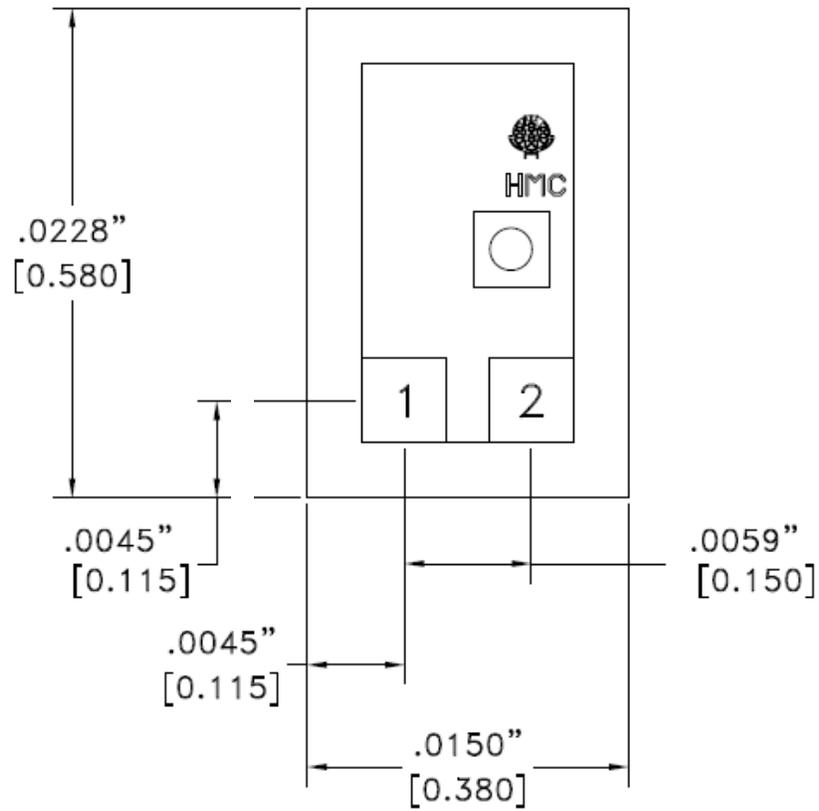
Parameter	Test Conditions	Delta Limits	Units
Gain	Per Table I	± 1	dB
Supply Current (Icq)		± 10	%

TABLE II Notes:

1/ Delta test is performed at TA = +25 °C only.

2/ Table I limits will not be exceeded.

3/ Delta calculated pre/post 240 hours and post 240 hours / post 1000 hours.



PAD	DESCRIPTION	PAD SIZE
1	RFIN	.0039[.100] X .0039[.100]
2	RFOUT	.0039[.100] X .0039[.100]

NOTES:

1. ALL DIMENSIONS ARE IN INCHES [MM]
2. DIE THICKNESS IS .004"
3. BACKSIDE METALIZATION: GOLD
4. BACKSIDE METAL IS GROUND
5. BOND PAD METALIZATION: GOLD

Figure 1 – Device Outline for the HMC8430

**TABLE III: DIE PACKAGING INFORMATION**

<b>Standard Package</b>	<b>Alternate</b>
GP-3 (Gel Pack)	<u>1/</u>

TABLE III Note:

1/ For alternate packaging information, contact Analog Devices Inc.

## ORDERING GUIDE

Model	Temperature Range
HMC8430	-40 °C to +85 °C

## Revision History

Revision History		
Rev	Description of Change	Date
A	Initial release.	10/08/2024
B	Update Section 2.1, 4.2.2.3 and Table II	01/03/2025