Interfacing the ADL5370 I/Q Modulator to the AD9779A Dual-Channel, 1 GSPS, High Speed DAC

CIRCUIT FUNCTION AND BENEFITS
The circuit described in this application note provides a simple, elegant interface between the ADL5370 I/Q modulator and the AD9779A high speed digital-to-analog converter (DAC). The ADL5370 and the AD9779A are well matched devices because they have the same bias levels and similarly high signal-to-noise ratios (SNR). The matched bias levels of 500 mV allow a glueless interface; there is no requirement to add a level shifting network that adds noise and insertion loss along with extra components. The addition of the swing limiting resistors (RSLI and RSLQ) allows the DAC swing to scale appropriately without loss of resolution or the 0.5 V bias level. The high SNR of each device preserves a high SNR through the circuit.

CIRCUIT DESCRIPTION
The ADL5370 interfaces with minimal components to members of the Analog Devices, Inc., family of TxDAC® converters. The baseband inputs of the ADL5370 require a dc common-mode bias voltage of 500 mV. With each of the AD9779A outputs swinging from 0 mA to 20 mA, a single 50 Ω resistor to ground from each of the DAC outputs provides the desired 500 mV dc bias. With just the four 50 Ω resistors in place, the voltage swing on each pin is 1 V p-p, resulting in a differential voltage swing of 2 V p-p on each input pair.

By adding the RSLI and RSLQ resistors to the interface, the output swing of the DAC reduces without any loss of DAC resolution. Place the resistor as a shunt between each side of the differential pair, as shown in Figure 1. It has the effect of reducing the ac swing without changing the dc bias already established by the 50 Ω resistors and the DAC output current.

It is generally necessary to low-pass filter the DAC outputs to remove image frequencies when driving a modulator. The interface shown in Figure 1 lends itself well to the introduction of such a filter. Insert the filter between the dc bias setting resistors and the ac swing limiting resistor because doing so establishes the input and output impedances for the filter.
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A simulated filter example is shown in Figure 3 with a third-order, elliptical low-pass filter with a 3 dB frequency of 3 MHz. Matching input and output impedances makes the filter design easier; therefore, the shunt resistor chosen is 100 Ω, producing an ac swing of 1 V p-p differential for a 0 mA to 20 mA DAC full-scale output current. In a practical application, the use of standard value components, along with the input impedance of the I/Q modulator (2900 kΩ in parallel with a few picofarads of input capacitance), slightly changes the frequency response of this circuit.

![Figure 3. DAC Modulator Interface with 3 MHz Third-Order, Elliptical Low-Pass Filter (Calculated Component Values)](image)

Connect the power supply pins of the ADL5370 to the same 5 V source. Tie adjacent pins of the same name together and decouple them to a large area ground plane with a 0.1 µF capacitor. Locate these capacitors as close as possible to the device. The power supply can range between 4.75 V and 5.25 V.

Tie the COM1, COM2, COM3, and COM4 pins to the same ground plane through low impedance paths. Solder the exposed paddle on the underside of the package to a low thermal and electrical impedance ground plane. If the ground plane spans multiple layers on the circuit board, stitch them together with nine vias under the exposed paddle. The AN-772 Application Note discusses the thermal and electrical grounding of the LFCSP in greater detail.

**COMMON VARIATIONS**

The interface described in this application note can interface any TxDAC converter with ground referenced 0 mA to 20 mA output currents to any I/Q modulator with a 0.5 V input bias level. For zero IF applications, the AD9783 dual DAC provides a low voltage differential signaling (LVDS) interface, whereas the complementary metal-oxide semiconductor (CMOS) driven AD9788 dual DAC generates a fine resolution complex IF input to the I/Q modulator.

The ADL5370/ADL5371/ADL5372/ADL5373 family of I/Q modulators provides narrow-band operation with high output 1 dB compression point and output third-order intercept (OIP3), whereas the ADL5375 provides broadband high performance operation from 400 MHz to 6 GHz. The ADL5385 I/Q modulator uses a 2x local oscillator (LO) and operates from 30 MHz to 2.2 GHz.

**REFERENCES**


Kester, Walt, Bryant, James, and Byrne, Mike. MT-031 Tutorial. *Grounding Data Converters and Solving the Mystery of ‘AGND’ and ‘DGND’*. Analog Devices, Inc., 2009.

