

# Triple Bucks Plus a Boost Controller to Meet Tough Requirements of Wide Range $V_{IN}$ Automotive Applications

Zhongming Ye  
Analog Devices, Inc.

Automobiles present space-constrained, harsh environments that demand reliable and compact power supplies for increasingly complex electronics systems. The **LT8603** compact regulator is a robust solution that combines two high voltage 2.5 A and 1.5 A buck regulators, a low voltage 1.8 A buck regulator, and a boost controller in a compact 6 mm × 6 mm QFN package. The boost controller simplifies the design of wide input, multiple output supplies when used, for example, in the following solutions:

- ▶ Cold crank tolerant automotive supplies with three regulated outputs
- ▶ Four regulated outputs with a fourth rail as SEPIC
- ▶ Boost powering from one buck

## Cold Crank Tolerant Automotive Supply with Three Regulated Outputs

In automobile applications, regulated 5 V, 3.3 V, and sub-2 V rails are required to power various analog and digital ICs that may require different rails for content, the processor I/O, and the core. These rails are generated from the nominal 12 V automotive battery voltage  $V_{BAT}$ , which typically ranges from 8 V to 16 V. High efficiency step-down buck regulators cover most situations, but  $V_{BAT}$  can drop to 2 V for 10s of ms during a cold crank situation, where pure buck regulators would lose regulation if powered directly from  $V_{BAT}$ .

The LT8603 boost controller operates down to 2 V, making it ideal as a preregulator to power the buck regulators. When  $V_{BAT}$  drops below 8.5 V, the boost controller output (OUT4) is regulated to 8 V. The two high voltage bucks can ride through the cold crank condition while providing constant 5 V and 3.3 V outputs, as shown in Figure 1. Once  $V_{BAT}$  recovers to above 8 V from the cold crank, the boost controller simply works as diode through. The high voltage bucks can handle  $V_{BAT}$  up to 42 V. In Figure 1, the low voltage buck is powered from OUT2, providing 1.2 V through the cold crank event.

## Four Regulated Outputs with the Fourth Rail as SEPIC

$V_{BAT}$  can remain high for an extended period of time, such as during a double battery jump-start or in a 24 V system. This has no effect on the boost regulator in Figure 1— $V_{BAT}$  passes through when  $V_{BAT}$  is higher than 8 V—but the current output capacities of the two high voltage buck regulators are typically thermally limited at higher  $V_{BAT}$  due to increased switching losses, especially at 2 MHz switching frequency that is often used in automotive applications.

The temperature rise can be controlled by either reducing the switching frequency or reducing the operating voltage of the buck regulators. In Figure 2, the fourth channel is set up as a SEPIC to power the high voltage bucks, with its output regulated at 12 V, which is optimal for the buck regulator efficiency. By running the bucks at optimal efficiency, the temperature rise is well controlled. Figure 2 shows an easy way to generate four accurately regulated outputs. At light load, this circuit maintains regulations with input down to 2 V.

## Boost Powered from One of the Bucks

Some automotive applications require a regulated high voltage, such as 54 V. One way to produce this regulated high voltage rail is to drive the boost regulator from the output of one of the high voltage buck regulators, as shown in Figure 3. All four outputs are regulated as long as  $V_{BAT}$  is higher than the minimum input voltage of the high voltage bucks. The buck regulator limits the maximum current of the boost converter, protecting the boost against short circuits and limiting cycle-by-cycle current.

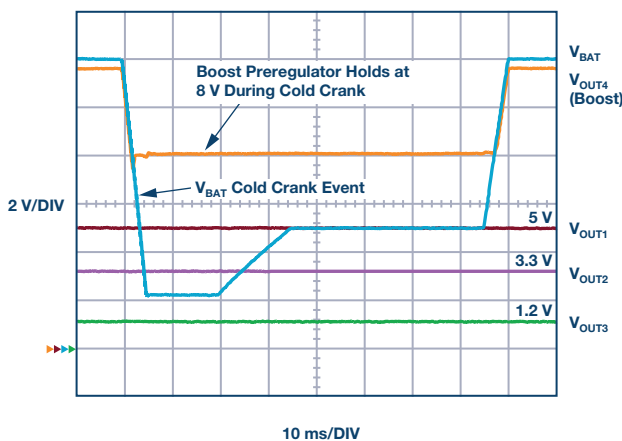
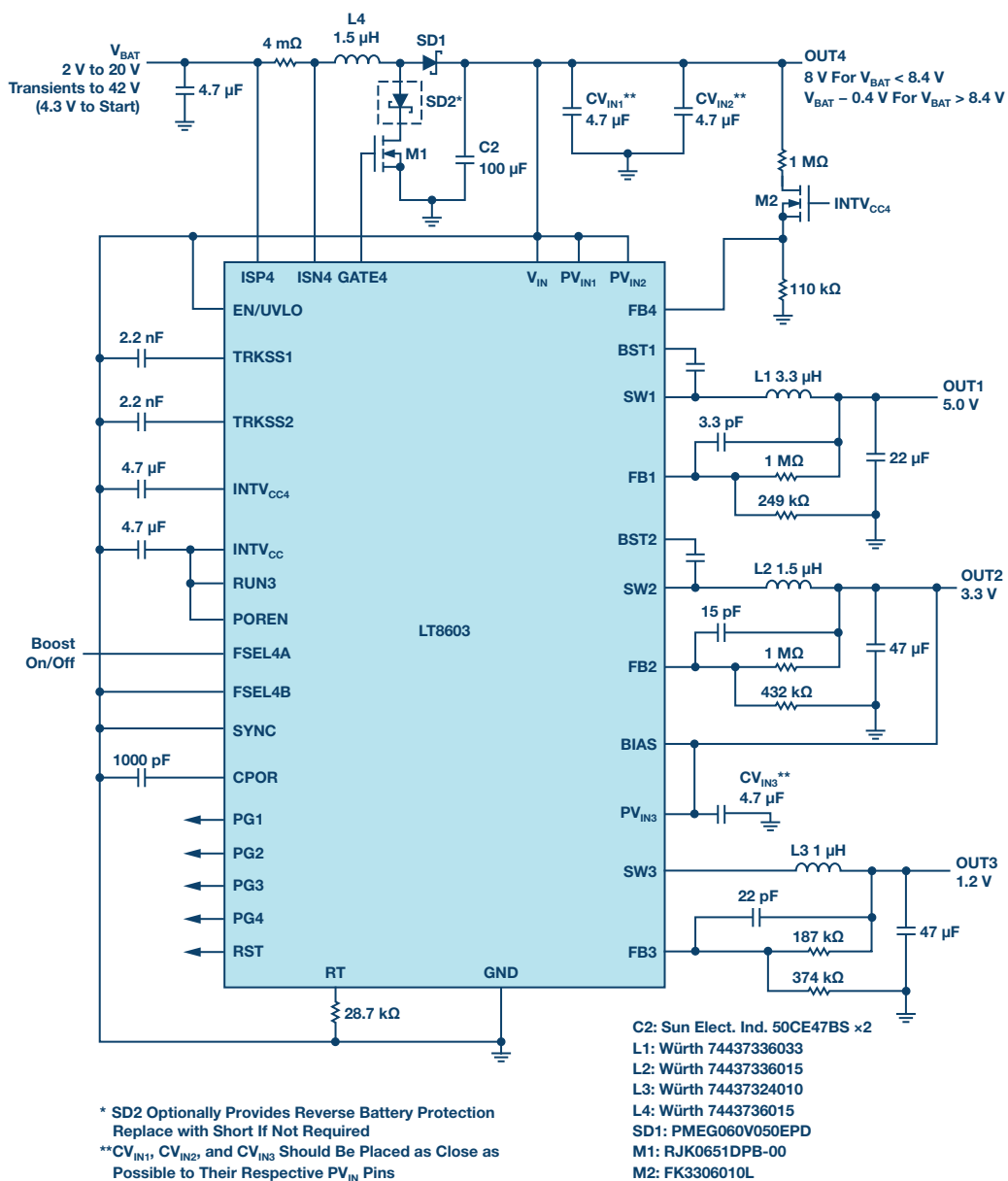


Figure 1. Cold crank tolerant automotive supply with three regulated outputs. Three bucks are powered with a boost preregulator ( $V_{OUT4}$ ), yielding precise regulation for all three outputs through a  $V_{BAT}$  cold crank event (also shown).

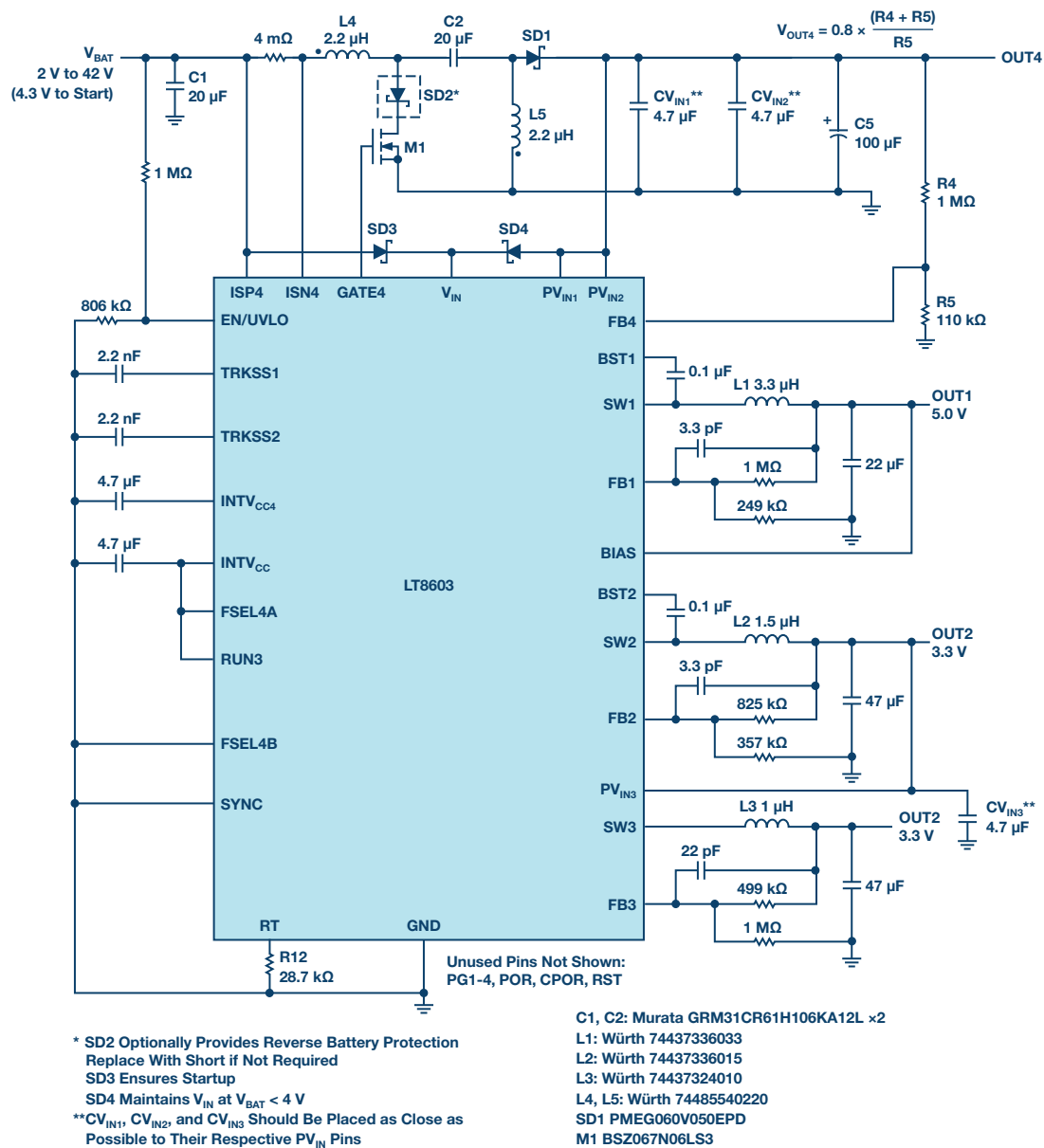


Figure 2. High voltage bucks optimized for efficiency and powered by a SEPIC.

## Additional Regulated Voltage with Charge Pump

A charge pump circuit can be added to a SEPIC circuit, as shown in Figure 4, to provide another regulated output. The regulation curves are shown in Figure 4 for different input voltages. Similarly, a negative output charge pump can be implemented to generate a negative rail.

## EMI Performance

The LT8603 uses a 2-phase clock. Channel 1 operates 180° from Channel 2, reducing the peak input current of the bucks and helping reduce EMI. The high density of electronic components requires carefully balancing of thermal and EMI performance. The LT8603 demo circuit DC2114A exemplifies a layout optimized for low EMI, passing CISPR 25 Class 5 peak limits. Figure 5 shows the radiated EMI results with vertical polarization in the range of 30 MHz to 1000 MHz. Input is 14 V with a 1 A load in each of the outputs.

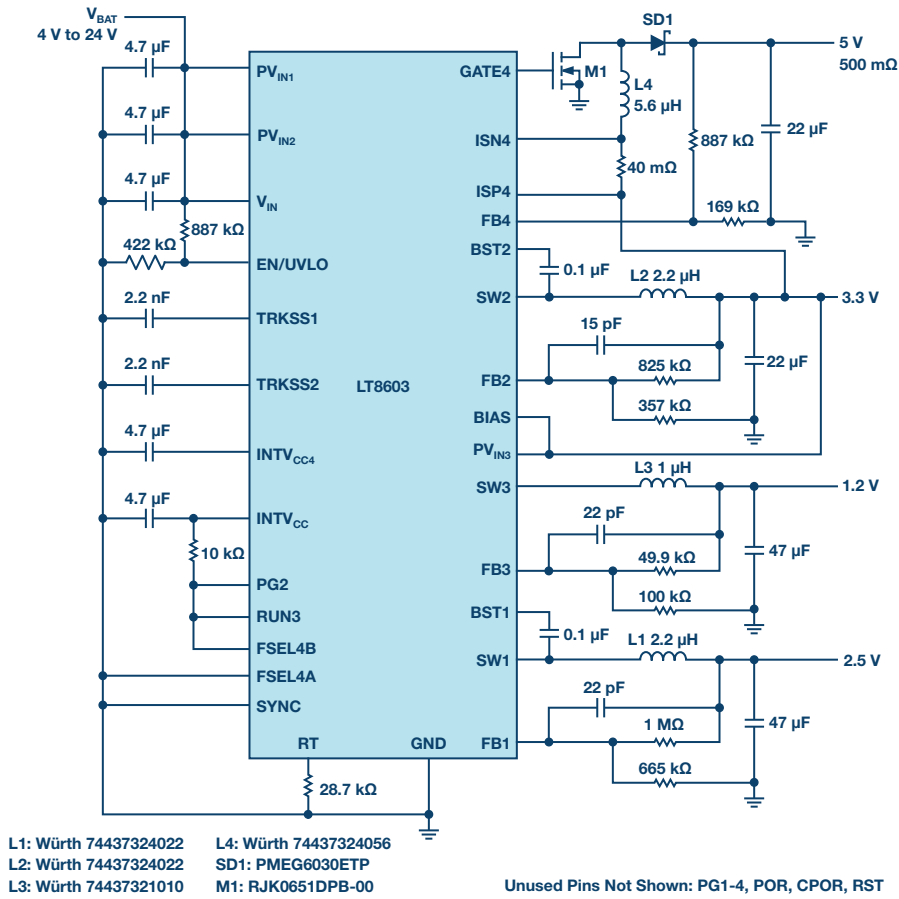


Figure 3. Four regulated outputs with the boost converter are powered from the Channel 3 buck regulator.

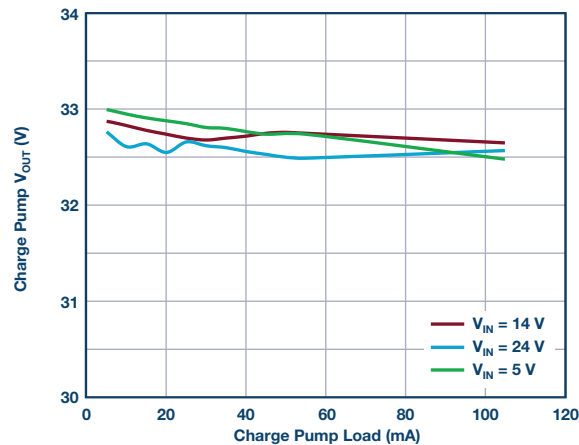
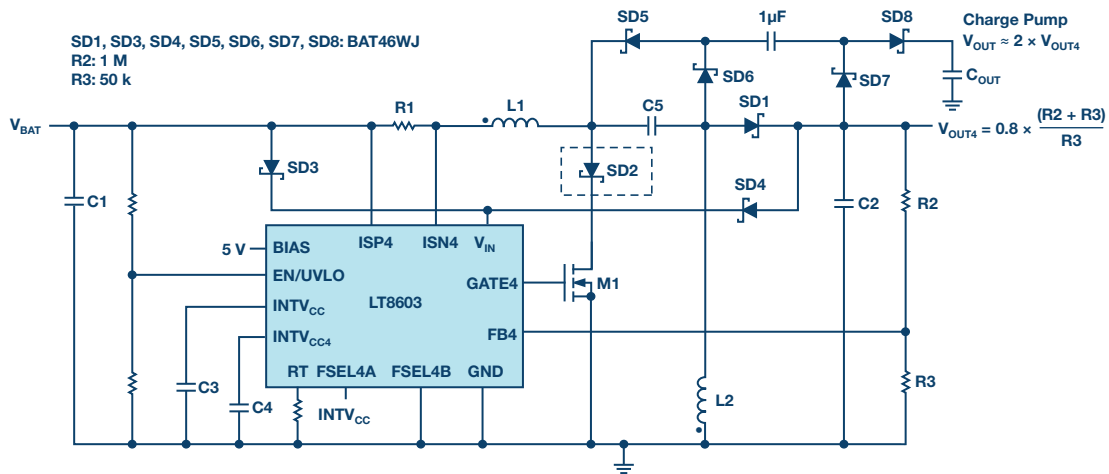


Figure 4. A charge pump circuit provides an additional high voltage output.

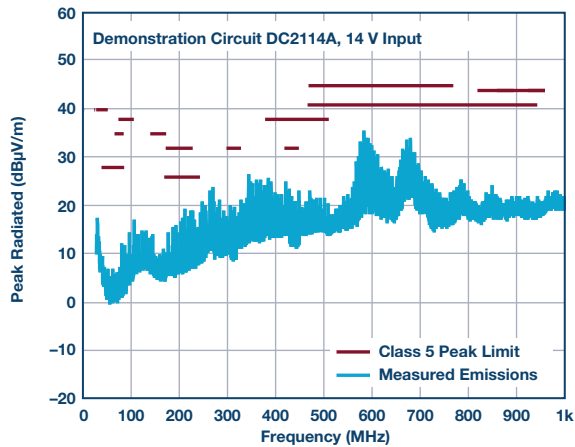


Figure 5. LT8603 DC2114A CISPR 25 Class 5 radiated EMI, 30 MHz to 1 GHz.

## Conclusion

The LT8603 offers versatile and compact power supply solutions by combining three buck regulators and a boost controller into a tiny 6 mm × 6 mm QFN package. Each of the buck regulators has internal power switches, cycle-by-cycle current limiting, and track/soft start control. Its synchronous rectification topology delivers up to 94% efficiency. Burst Mode® operation keeps quiescent current under 30 μA (all channels on), ideal for always-on systems. The wide input range, from 2 V to 42 V, and versatile functions make the LT8603 a good fit for automotive and other demanding applications.

## About the Author

Zhongming Ye is a senior applications engineer for power products at Analog Devices, Inc., in Milpitas, California. He has been working at Linear Technology (now ADI) since 2009 to provide application support on various products including buck, boost, flyback, and forward converters. His interests in power management include high performance power converters and regulators of high efficiency, high power density, and low EMI for automotive, medical, and industrial applications. Prior to this, he worked at Intersil for three years on PWM controllers for isolated power products. He obtained a Ph.D. in electrical engineering from Queen's University, Kingston, Canada. Zhongming was a senior member of IEEE Power Electronics Society. He can be reached at [zhongming.ye@analog.com](mailto:zhongming.ye@analog.com).

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### Analog Devices, Inc. Worldwide Headquarters

Analog Devices, Inc.  
One Technology Way  
P.O. Box 9106  
Norwood, MA 02062-9106  
U.S.A.  
Tel: 781.329.4700  
(800.262.5643, U.S.A. only)  
Fax: 781.461.3113

### Analog Devices, Inc. Europe Headquarters

Analog Devices GmbH  
Otto-Aicher-Str. 60-64  
80807 München  
Germany  
Tel: 49.89.76903.0  
Fax: 49.89.76903.157

### Analog Devices, Inc. Japan Headquarters

Analog Devices, KK  
New Pier Takeshiba  
South Tower Building  
1-16-1 Kaigan, Minato-ku,  
Tokyo, 105-6891  
Japan  
Tel: 813.5402.8200  
Fax: 813.5402.1064

### Analog Devices, Inc. Asia Pacific Headquarters

Analog Devices  
5F, Sandhill Plaza  
2290 Zuchongzhi Road  
Zhangjiang Hi-Tech Park  
Pudong New District  
Shanghai, China 201203  
Tel: 86.21.2320.8000  
Fax: 86.21.2320.8222

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