

# Simple Power Backup Supply for a 3.3 V Rail

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## Introduction

Data loss is a concern in telecom, industrial, and automotive applications where embedded systems require a dependable supply of power. Sudden power interruptions can corrupt data during read and write operations performed by hard drives and flash memory. Designers often use batteries, capacitors, and supercapacitors to store enough energy to support critical loads for a short time during a power interruption.

The LTC3643 power backup supply allows designers to use a relatively inexpensive storage component: low cost electrolytic capacitors. In the backup or holdup supply presented here, the LTC3643 charges a storage capacitor to 40 V when power is present and discharges it to the critical load when power is interrupted. The load (output) voltage can be programmed to any voltage between 3 V and 17 V.

The LTC3643 easily fits backup solutions for 5 V and 12 V rails, but a 3.3 V rail solution requires extra care. The minimum operating voltage of the LTC3643 is 3 V, which is relatively close to the nominal 3.3 V input voltage level. This is too tight when a blocking diode is used to decouple the backup voltage source from noncritical circuitry, as shown in Figure 1a. If D1 is a Schottky diode, its forward voltage drop—as a function of load current and temperature—can reach 0.4 V to 0.5 V, enough to place the voltage at the LTC3643  $V_{IN}$  pin below the 3 V minimum. As a result, the backup supply circuit may not start up.

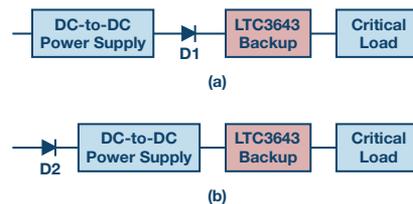


Figure 1(a) and (b). Location of the blocking diode in the backup system schematic.

One possible solution is to move the diode to the input of the supplying dc-to-dc converter, D2, as shown in Figure 1b. Unfortunately, in this scenario, noncritical loads connected to the upstream dc-to-dc supply can draw power from the backup supply and leave less energy for critical loads.

## 3.3 V Backup Supply Operation

Figure 2 shows a solution to producing a 3.3 V backup supply that reserves energy for critical loads using a blocking MOSFET. The blocking diode shown in Figure 1 is replaced by Q1, a low gate threshold voltage power P-channel MOSFET.

The key to operating the backup supply in a 3.3 V environment is the addition of the series RA-CA circuit. At startup, as the input voltage rises, the current through the capacitor CA is governed by the equation  $I_{CA} = C \times (dV/dt)$ . This current generates a potential across RA, which is enough to enhance Q2, a low gate threshold voltage small signal N-channel

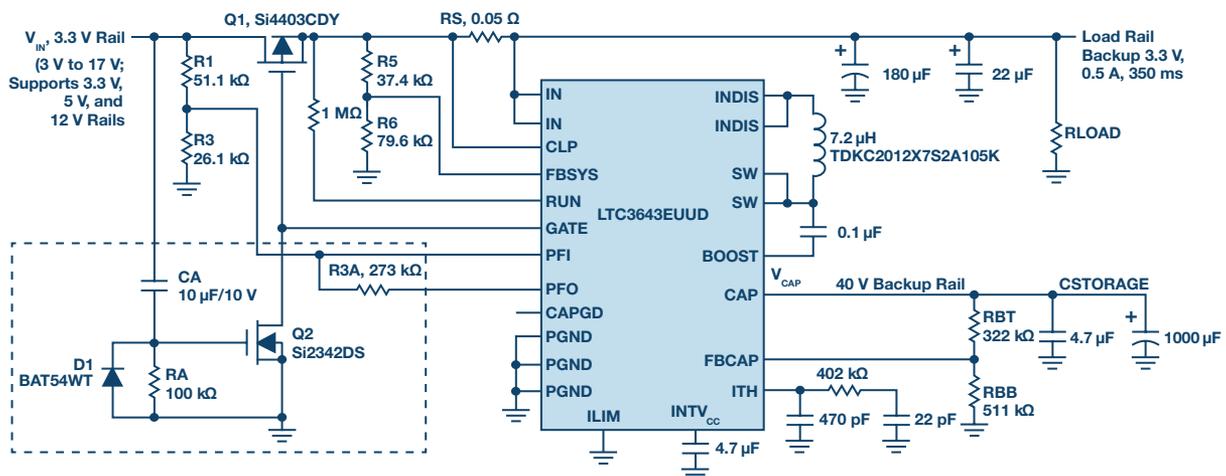


Figure 2. Enhanced schematic of the LTC3643 solution for a 3.3 V rail.

MOSFET. As Q2 turns on, it pulls the gate of the Q1 to ground, providing an extremely low resistance path from the input voltage to the supply pins  $V_{IN}$  of LTC3643. Once 3.3 V is applied to the converter, it starts up, pulling down both the gate of Q1 and the PFO pin, and it starts charging the storage capacitor.

As the 3.3 V rail reaches steady state, the  $I_{CA}$  current reduces to the point where the voltage across RA falls below the Q2 gate threshold level and Q2 turns off, no longer affecting the functionality of the backup converter. Also, the PFO pin grounds R3A, which resets the PFI pin power fail voltage level to the minimum 3 V to ensure that the converter remains operational when the input voltage source is disconnected.

## Circuit Functionality

The waveforms in Figure 3 show results as the 3.3 V rail starts up. As the input voltage rises, so does the gate of Q2, pulling the gate of Q1 low. Q1 remains enhanced, allowing the full 3.3 V to reach the LTC3643, bypassing the Q1 body diode. Eventually the gate of Q2 voltage drops below the threshold level and Q2 turns off—by this time the LTC3643 is fully operational and takes control over the gate of Q1.

The versatility of the LTC3643 is on display here: specifically, its ability to limit the charging current of the boost converter used to charge the storage capacitor. In cases where total current must be minimized, such as when there are long lines or high impedance voltage sources, the boost current can be set relatively low in order to minimize the effect of charging current on the input voltage drop. This is particularly important for 3.3 V rails. In Figure 2, the 0.05  $\Omega$  resistor RS sets a limit of 0.5 A (10.5 A load) for the boost converter charging the current (the maximum possible set limit is 2 A); the rest is delivered to the load.

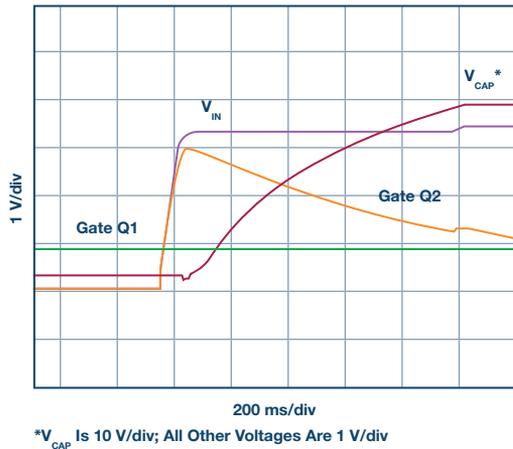


Figure 3. Waveforms of 3.3 V rail for power-up.

Figure 4 shows the waveforms when the 3.3 V rail is lost. As the input voltage falls, the voltage at the gate of Q2 remains unchanged, close to ground, and Q2 remains off. In contrast, the voltage at the gate of Q1 rises sharply to 3.3 V. This turns Q1 off, with the body diode of Q1 acting as a blocking diode, which decouples the load from the input. At this point, the backup supply takes over, with the LTC3643 supplying 3.3 V to the critical load by discharging a storage capacitor.

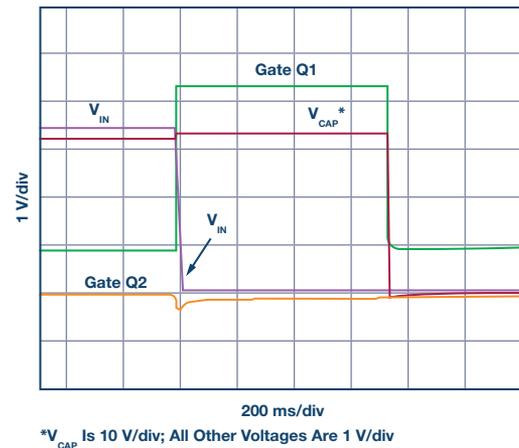


Figure 4. Waveforms of 3.3 V rail for power-down.

## Conclusion

The circuit presented here enables the LTC3643 to be used as a backup supply solution for 3.3 V rails. The LTC3643 simplifies backup supplies by employing low cost electrolytic capacitors as the energy storage component.

## About the Author

Victor Khasiev is a senior applications engineer at ADI. Victor has extensive experience in power electronics both in ac-to-dc and dc-to-dc conversion. He holds two patents and wrote multiple articles. These article related to use ADI semiconductors in automotive and industrial applications. They cover step-up, step-down, SEPIC, positive-to-negative, negative-to-negative, flyback, and forward converters, as well as bidirectional backup supplies. His patents are about efficient power factor correction solutions and advanced gate drivers. Victor enjoys supporting ADI customers: answering questions about ADI products, designing and verifying power supplies schematics, laying out print circuit boards, troubleshooting, and participating in testing final systems. He can be reached at [victor.khasiev@analog.com](mailto:victor.khasiev@analog.com).

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