
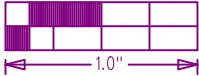
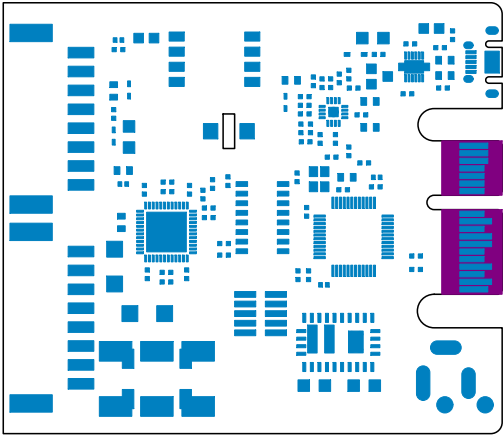

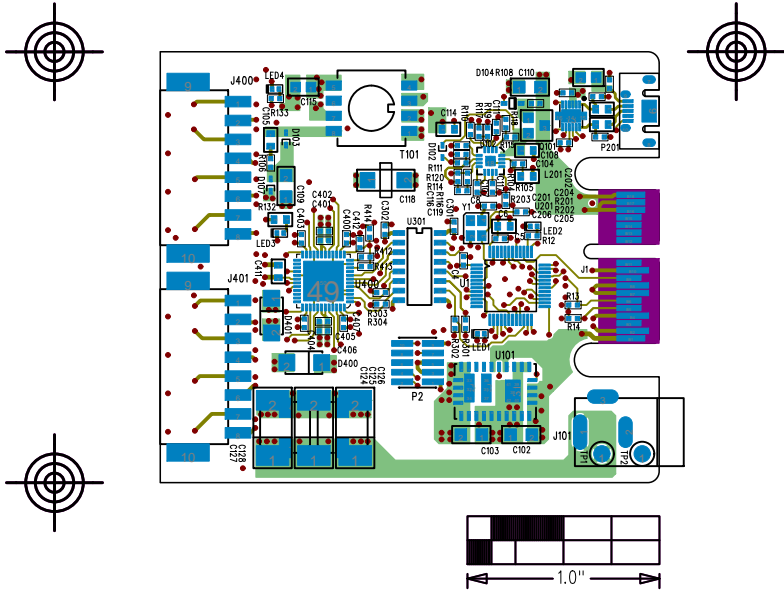



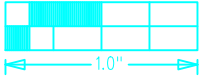
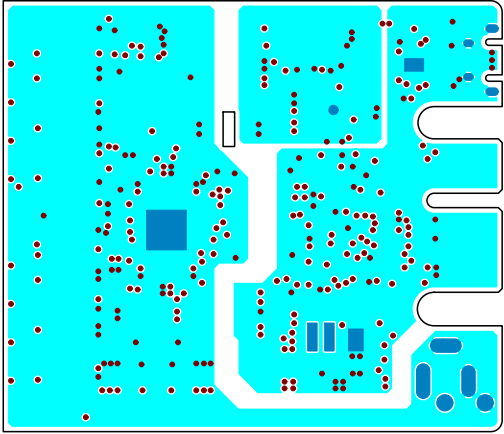
MAXREFDES63# uPLC IO Card - D0 Board	
P/N: EPCBREFDES63	REV A
 maxim integrated™	
LAYER TOP SOLDERMASK	
DATE:	ALL UNITS ARE IN 0.001"




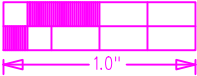
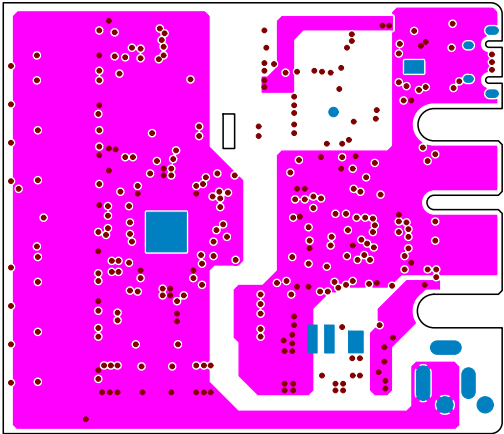
MAXREFDES63# uPLC IO Card - D0 Board	
P/N: EPCBREFDES63	REV A
 maxim integrated™	
LAYER COMPONENT SIDE	
DATE:	ALL UNITS ARE IN 0.001"



MAXREFDES63# uPLC IO Card - DO Board	
P/N: EPCBREFDES63	REV A
	
LAYER LAYER 2 GND_F, GND_S, ISO_GND	
DATE:	ALL UNITS ARE IN 0.001"




MAXREFDES63# uPLC IO Card - D0 Board	
P/N: EPCBREFDES63	REV A
 maxim integrated™	
LAYER LAYER 3 ISO_24V, V24V, V5V, VDD_S	
DATE:	ALL UNITS ARE IN 0.001"



MAXREFDES63#
uPLC IO Card - D0 Board

P/N: EPCBREFDES63

REV
A

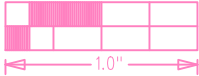
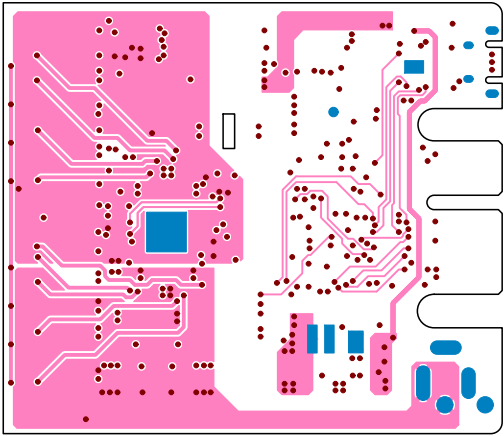
 maxim
integrated™


LAYER

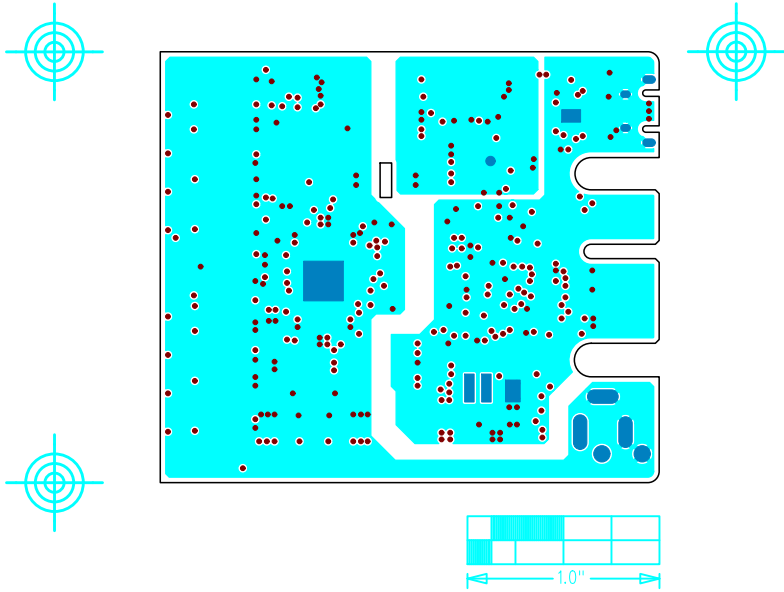
LAYER 4 ISO_24V, ISO_5V, V24V, V5V, VDD_S, SIGNALS

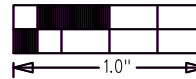
DATE:


ALL UNITS ARE IN 0.001"



MAXREFDES63# uPLC IO Card - D0 Board	
P/N: EPCBREFDES63	REV A
	
LAYER LAYER 5 ISO_GND, GND_F, GND_S	
DATE:	ALL UNITS ARE IN 0.001"





MAXREFDES63# uPLC IO Card - D0 Board	
P/N: EPCBREFDES63	REV A
 maxim integrated™	
LAYER BOTTOM SOLDERMASK	
DATE:	ALL UNITS ARE IN 0.001"

