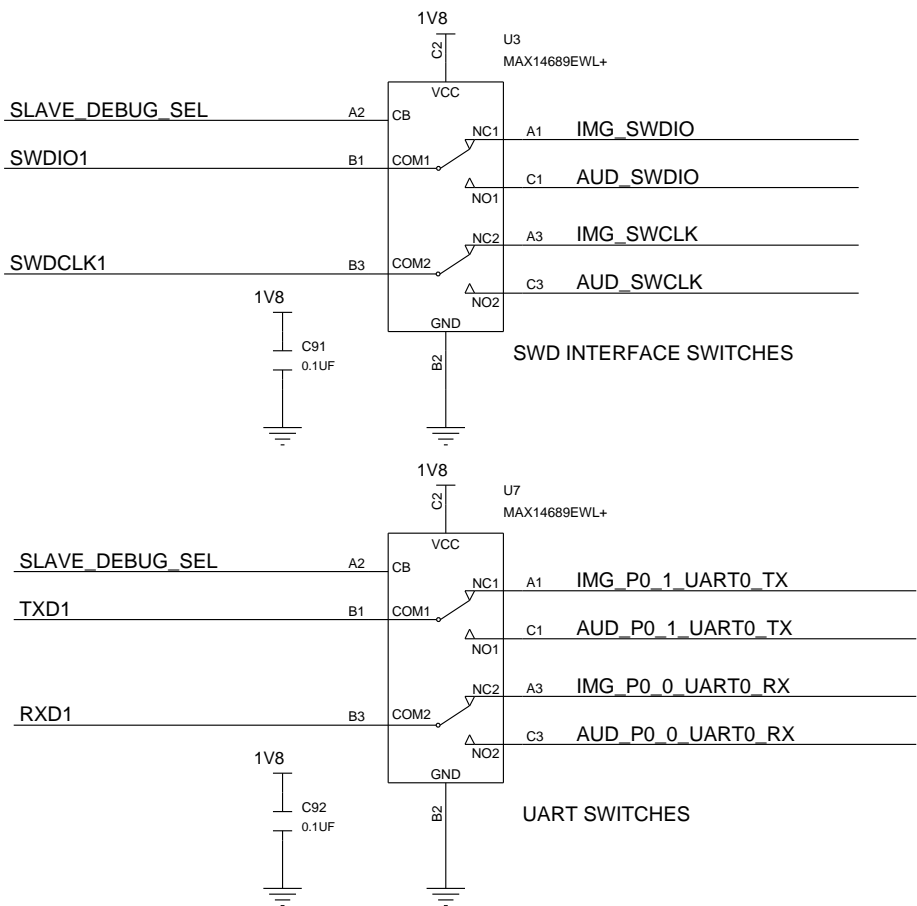


IMG PREFIX IS FOR MAX78000 DEDICATED FOR IMAGE APPLICATIONS
AUD PREFIX IS FOR MAX78000 DEDICATED FOR AUDIO APPLICATIONS

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

ARM SWD INTERFACE AND DEBUG UART SWITCHES TO HOST BOARD



SLAVE_DEBUG_SEL SIGNAL DRIVEN BY TYE HOST BOARD :

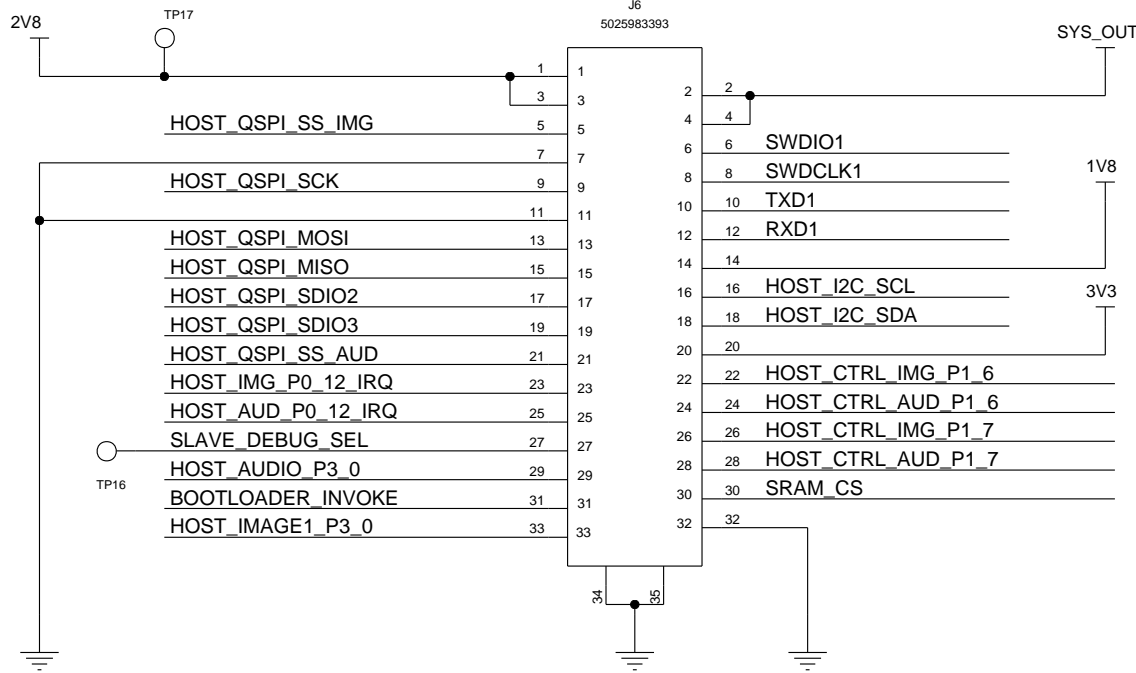
- 0 : MAX78000-IMAGE IS CONNECTED TO HOST / USB TYPE-C
1 : MAX78000-AUDIO IS CONNECTED TO HOST / USB TYPE-C

HOST BOARD FLAT FLEX CABLE CONNECTOR

POWER SOURCES ON HOST BOARD :

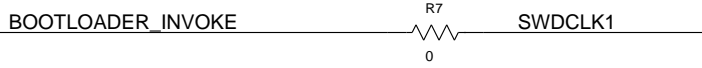
- 2V8 : 2.8V RAIL. BUCK CONVERTER OF MAX20303 PMIC
SYS_OUT : VBATT OR VUSB
1V8 : 1.8V RAIL. BUCK CONVERTER OF MAX20303 PMIC
3V3 : 3.3V RAIL FOR POWER MEASUREMENT. LDO OUTPUT

MOLEX FPC CONNECTOR
DUAL CONTACT (TOP+BOTTOM)



HOST BOARD IS QSPI MASTER. MAX78000 CHIPS
ARE QSPI SLAVE DEVICES

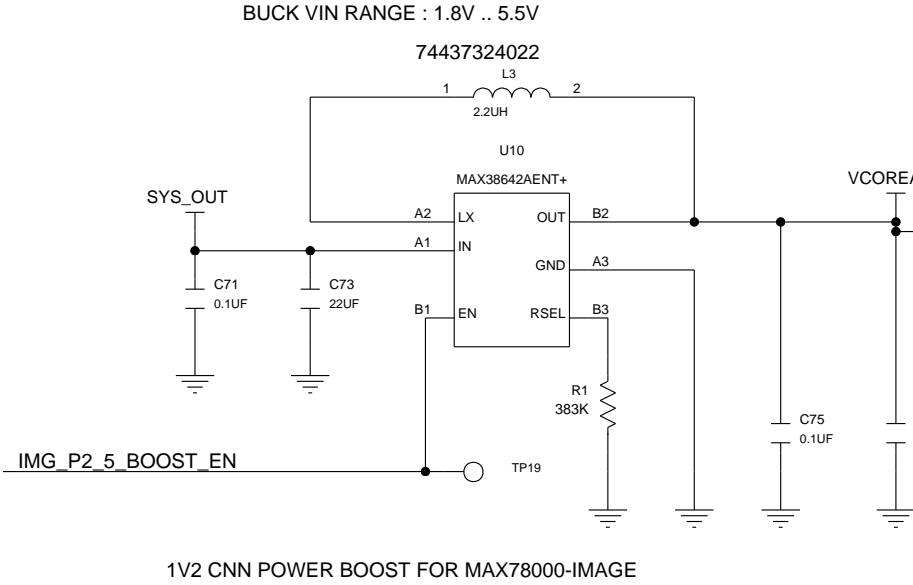
HOST BOARD IS I2C MASTER. MAX78000 CHIPS AND
POWER ACCUMULATOR ARE I2C SLAVE DEVICES



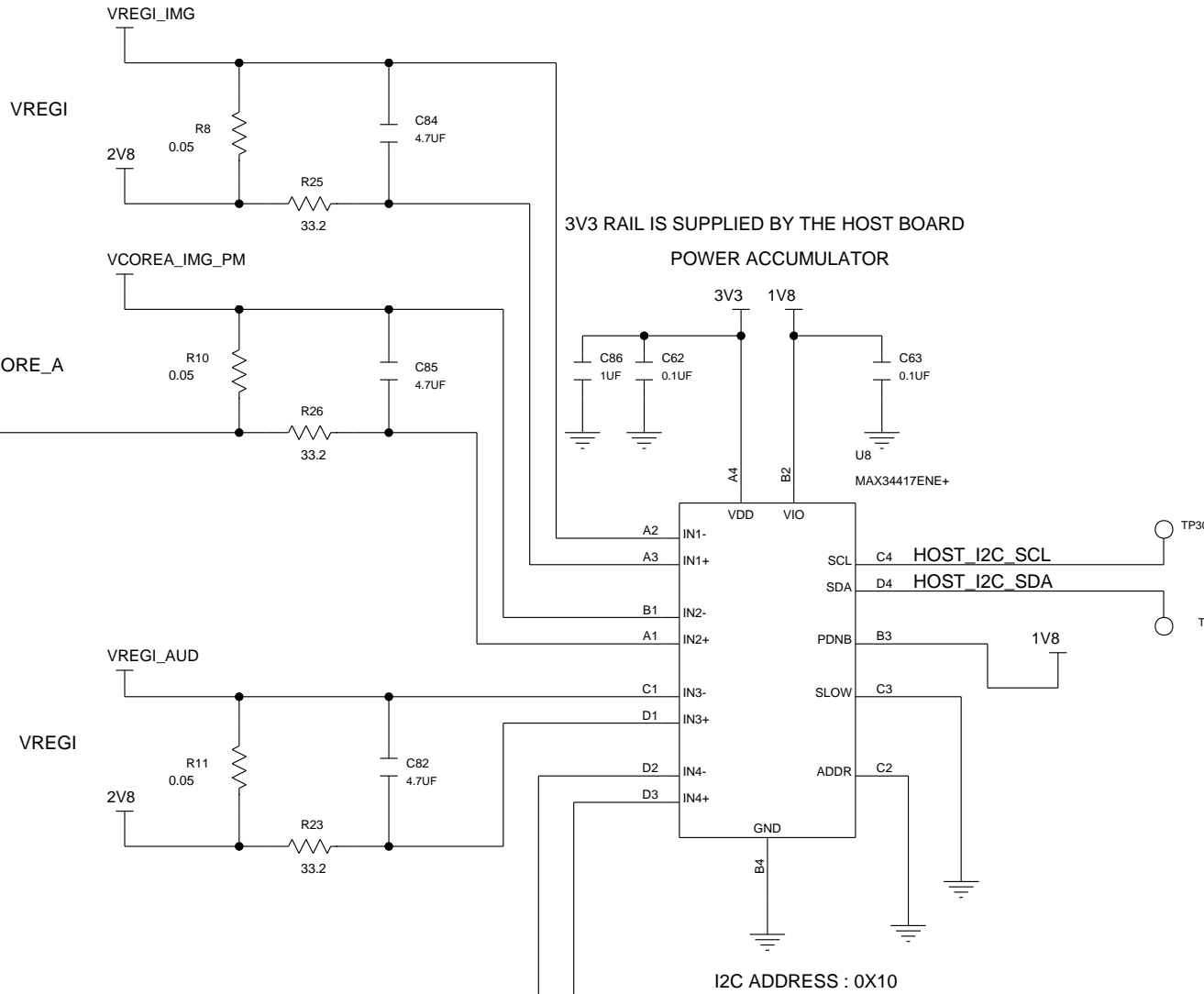
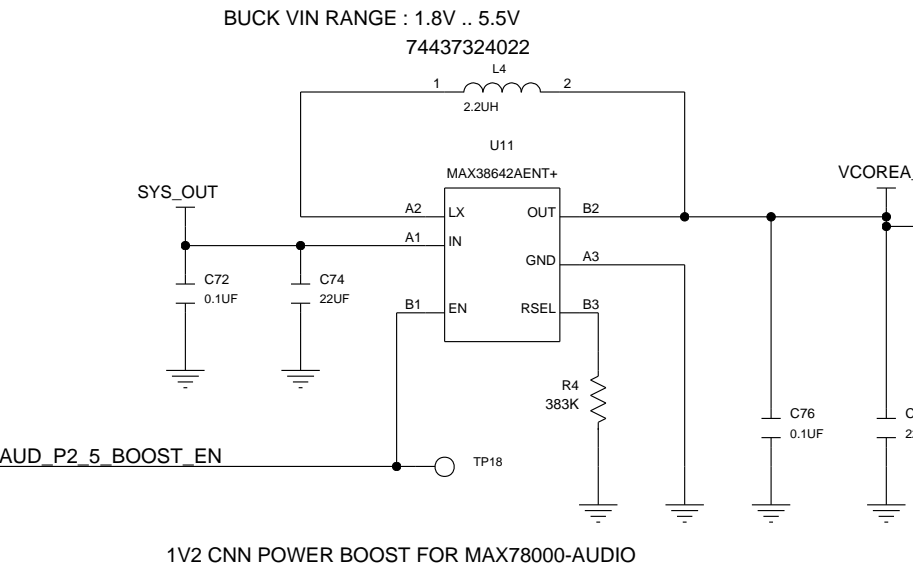
SELECTED MAX78000 (VIA SLAVE_DEBUG_SEL)
CAN BE PUT INTO BOOTLOADER MODE BY
BOOTLOADER_INVOKE SIGNAL DRIVEN BY THE
HOST BOARD

2X BUCK CONVERTERS FOR CNN POWER AND POWER ACCUMULATOR FOR POWER BENCHMARKING

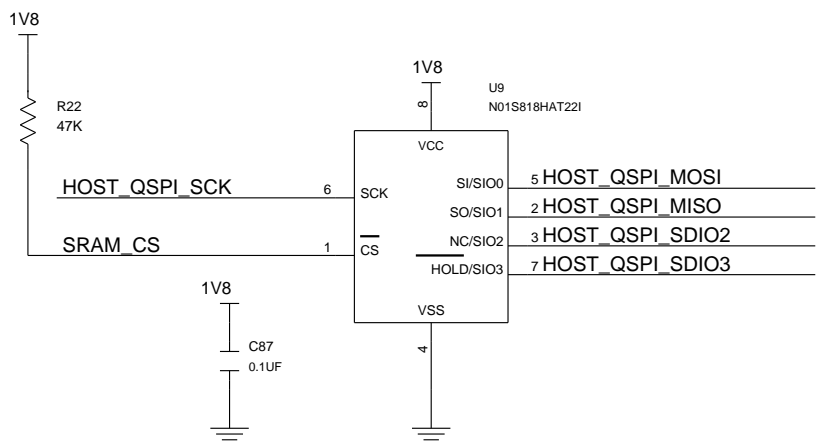
MAX78000 IMAGE



MAX78000 AUDIO



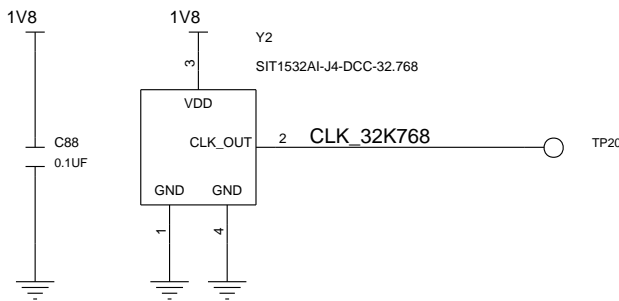
QSPI SRAM



THIS IS AN OPTIONAL PART. THE HOST BOARD
QSPI MASTER CONTROLS THE SRAM.

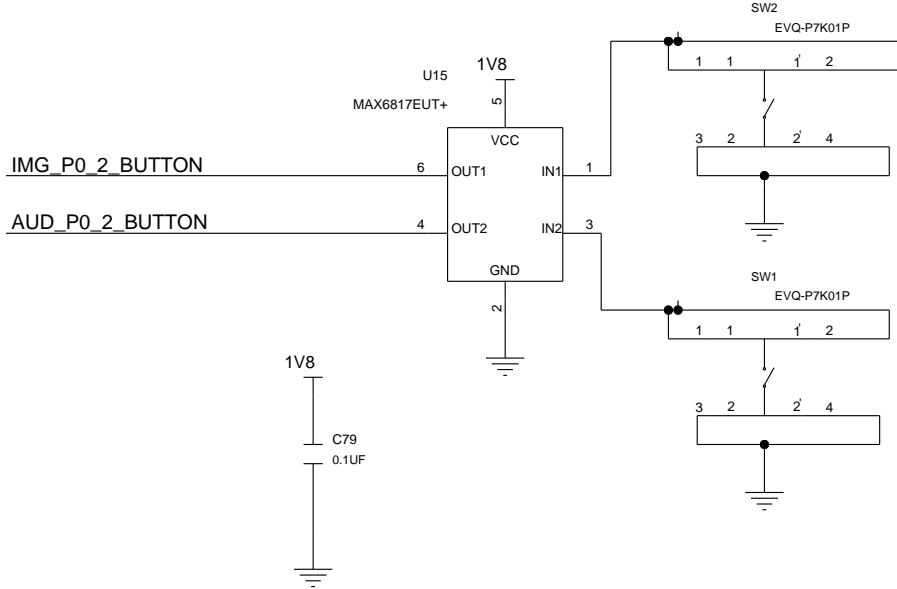
INTENDED TO WORK AS A DUAL PORT RAM:
WHEN NEEDED HOST BOARD CAN SELECT THE PART
AND LEAVE THE BUS TO ONE OF MAX78000S.
DRIVE THE SRAM_CS LINE WITH OPEN-DRAIN
OUTPUTS. WHEN NOT CONTROLLING THE SRAM,
SET SRAM_CS PORT ON MICROCONTROLLER AS
INPUT. ONLY ONE CHIP SHOULD DRIVE SRAM_CS.

32.786KHZ CLOCK OSCILLATOR

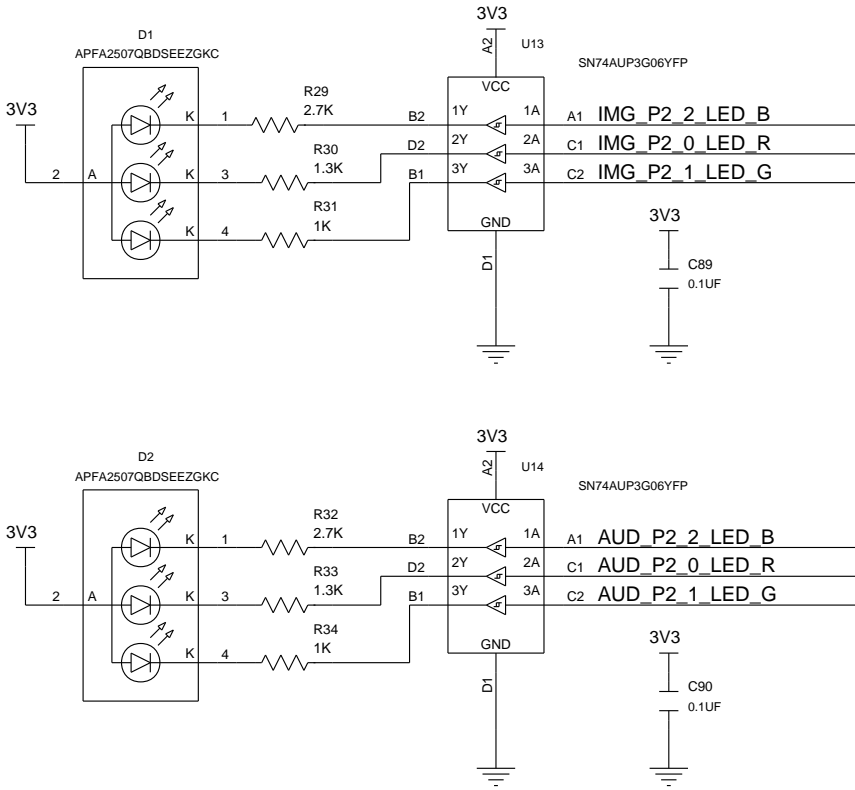


BUTTON DEBOUNCER

TWO RIGHT ANGLE BUTTONS :
SW1 BUTTON FOR MAX78000-AUDIO
SW2 BUTTON FOR MAX78000-IMAGE



RGB LEDS



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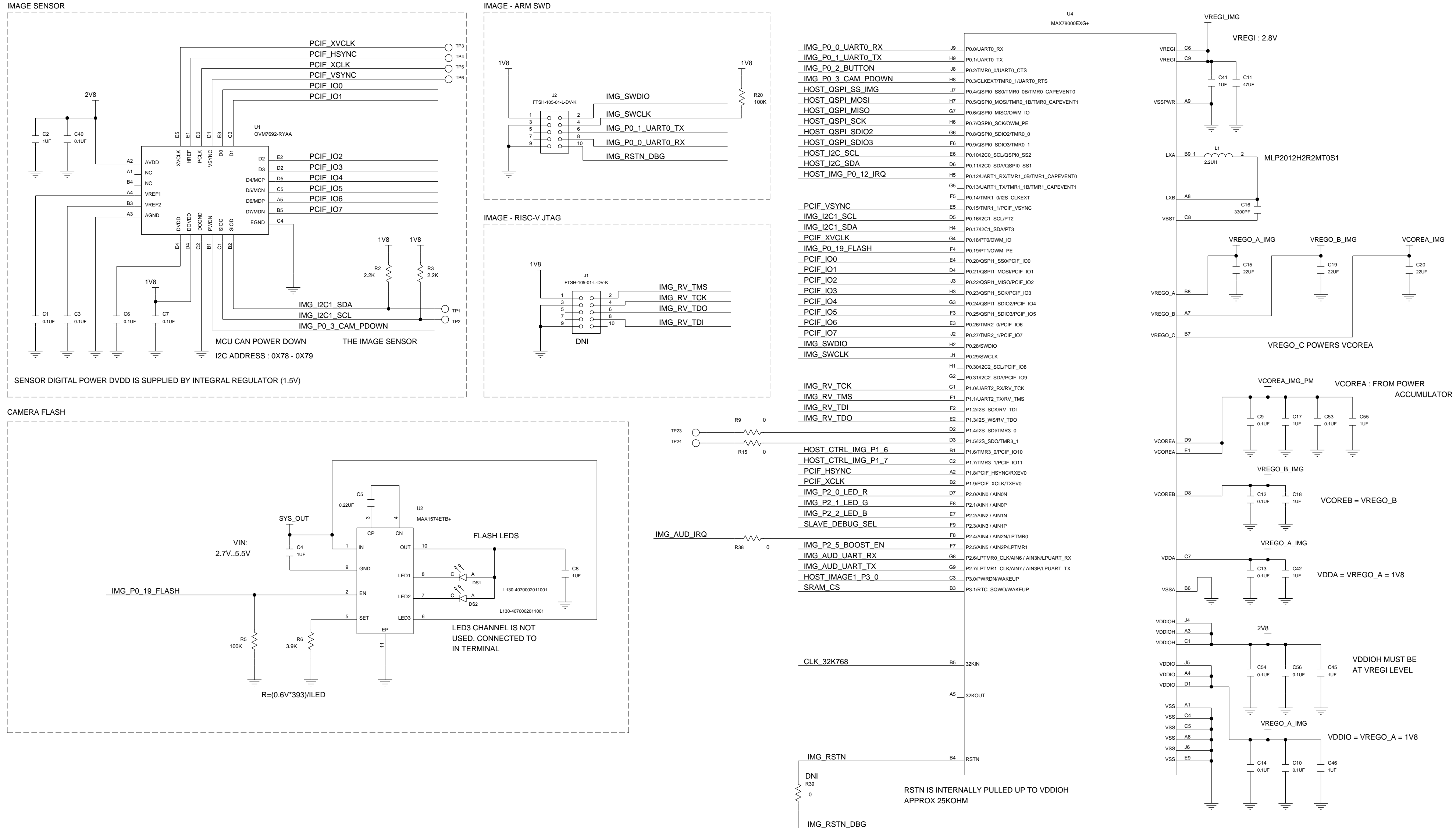
PROJECT TITLE: MAXREFDES178_AI_BOARD_APPS_B		
DRAWING TITLE:		
SIZE: C	HARDWARE NUMBER:	DATE: 02/09/2021
ENGINEER:	DRAWN BY:	REV.: B
TEMPLATE REV.:		SHEET 1 OF 3

IMG PREFIX IS FOR MAX78000 DEDICATED FOR IMAGE APPLICATIONS
AUD PREFIX IS FOR MAX78000 DEDICATED FOR AUDIO APPLICATIONS

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

MAX78000-IMAGE





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PROJECT TITLE: MAXREFDES178_AI_BOARD_APPS_B		
DRAWING TITLE:		
SIZE: C	HARDWARE NUMBER:	DATE: 02/09/2021
ENGINEER: -	DRAWN BY: -	REV.: B
TEMPLATE REV.: -		SHEET 2 OF 3



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PROJECT TITLE:				MAXREFDES178_AI_BOARD_APPS_B			
DRAWING TITLE:							
SIZE: 		HARDWARE NUMBER: -				DATE: 02/09/2021	
ENGINEER:			DRAWN BY:			REV.:	
						B	
			TEMPLATE REV.:			SHEET 3 OF 3	

PROJECT TITLE:				MAXREFDES178_AI_BOARD_APPS_B			
DRAWING TITLE:							
SIZE: 		HARDWARE NUMBER: -				DATE: 02/09/2021	
ENGINEER:			DRAWN BY:			REV.:	
						B	
			TEMPLATE REV.:			SHEET 3 OF 3	

