

ECO1 REV

DESCRIPTION

APP. ENG.

DATE

-

2

JIAN L.

9-30-13

REVISION HISTORY

L1 - TOP SIDE

L2 - GND PLANE

L3 - TRACE/GND PLANE

L4 - TRACE/GND PLANE

L5 - GND PLANE

L6 - BOTTOM SIDE

LAYER STRUCTURE

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.

2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYER AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.

3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.

4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER

5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.

6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.

7. PCBs ARE TO BE RoHS COMPLIANT.

8. SCORING FOR PANELIZED PCB (PRODUCTION ONLY):

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES

TOLERANCES:
0.XX = +/- 0.01
0.XXX = +/- 0.005

INTERPRET DIM AND TOL PER ASME Y14.5M-1994

THIRD ANGLE PROJECTION

APPROVALS

PCB DES.

HZ

APP ENG.

JIAN L.

SCALE = NONE

LINEAR TECHNOLOGY

1530 MCCARTHY BLVD
MILPITAS, CA 95035
TEL: 408.438.4400
WWW.LTPROD.COM
LTC CONFIDENTIAL -
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING

HIGH EFFICIENCY, DUAL-OUTPUT SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DRC INDUCTOR

SIZE

IC NO.

REV

N/A

LTC3875EUJ

2

FILENAME: DC2000A-2.PCB

SHT 1 OF 1

4.00

4.60

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.003
0.012	468	X	YES	+/-0.003
0.067	19	□	YES	+/-0.003
0.03543	12	◇	YES	+/-0.003
0.055	20	⊗	YES	+/-0.003
0.07	4	⊗	NO	+/-0.003
0.035	14	⊕	YES	+/-0.003
0.189	6	⊕	YES	+/-0.003
0.006	8	⊕	YES	+/-0.003
0.01	9	⊕	YES	+/-0.003