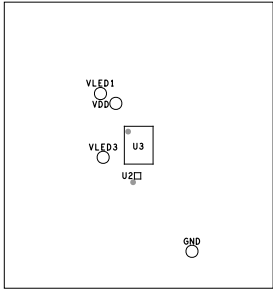



ASSEMBLY NOTES:

- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
- BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS. SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.
- ASSEMBLY VENDOR SHOULD INSTALL ACCUMOLD 28800X CHAMBER.
- ASSEMBLY VENDOR SHOULD INSTALL RIVETS (M045128).



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	19NOV21	A. UY
B	ECR-111942	24MAR23	A. UY
C	ECR-115064	28JUL23	A. UY

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div> <b>ANALOG DEVICES</b></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>					
TOLERANCES			TEMPLATE ENGINEER N/A		N/A							
DECIMALS   FRACTIONS   ANGLES .XX   -.010   --1/32   -- 2 .XXX   -.005 .XXXX   -.0050			HARDWARE SERVICES M. VALE		19NOV21		TITLE  ASSEMBLY  EVAL-CN0583-SOM CUSTOMER EVALUATION Z					
MATERIAL			HARDWARE SYSTEMS N/A		N/A							
			TEST ENGINEER N/A		N/A							
			COMPONENT ENGINEER N. CATALAN		13JUL23							
			TEST PROCESS N/A		N/A							
			HARDWARE RELEASE K. JABATAN		19NOV21							
FINISH			DESIGNER M. VALE		19NOV21		SIZE	FSCM NO	DRAWING NUMBER		REV	
			PTD ENGINEER A. UY		19NOV21		C	24355	01-068743		C	
			CHECKER N/A		N/A							
DO NOT SCALE DWG							SCALE   1 / 1				SHEET   1 OF 2	

CUSTOMER EVALUATION BOARD

4

3

2

1

D

D

C

C

B

B

A

A

4

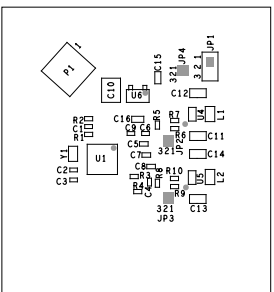
3

2

1

## ASSEMBLY NOTES 2

8. APPLY SOLDER BLOB ON JP2, JP3 AND JP4 PIN 1 AND 2.
9. SHUNT M028265 TO BE INSTALLED ON JP1 PIN 1 AND 2.



## SECONDARY SIDE

