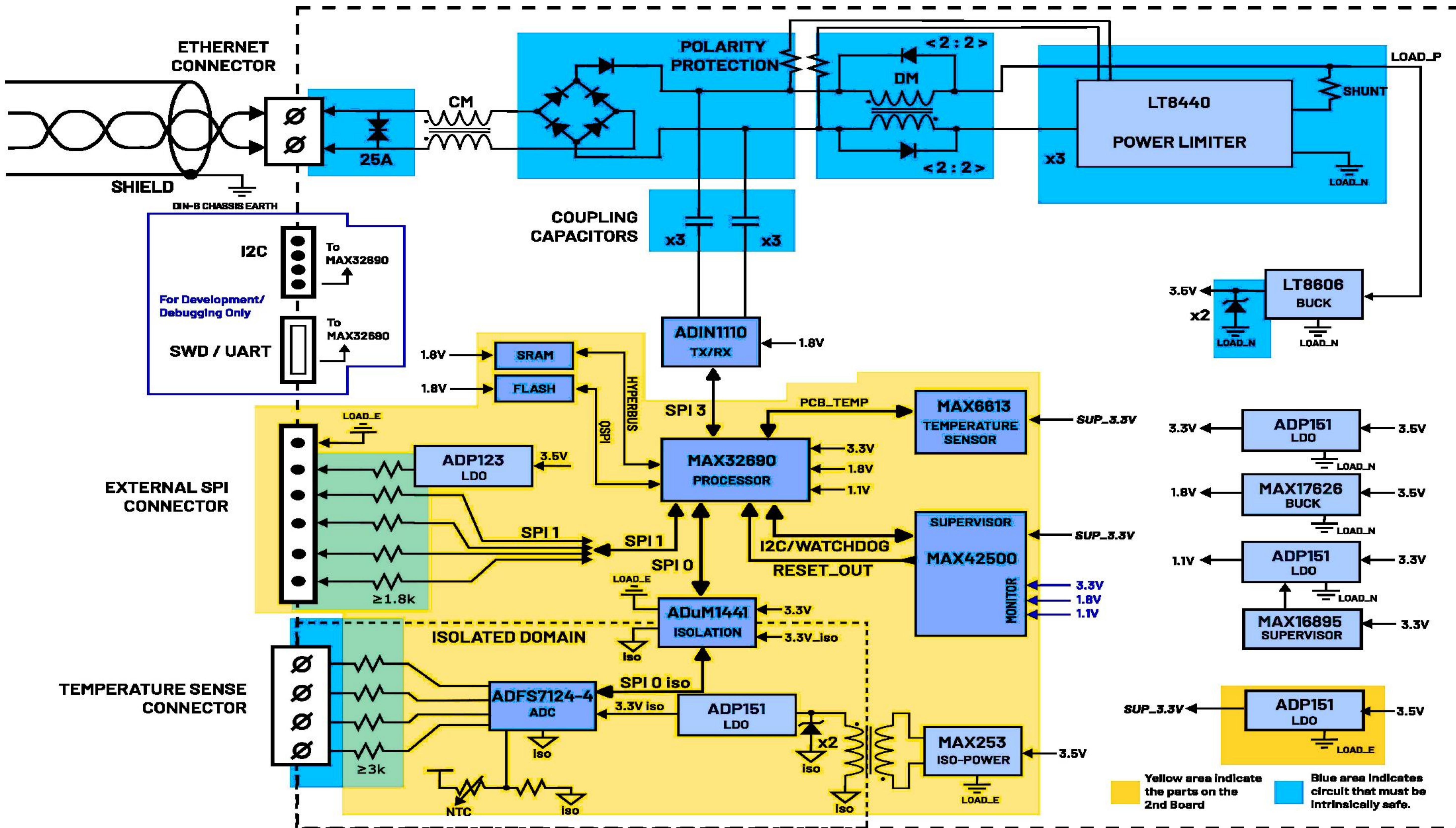



8		7		6		5		4		3		2		1							
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RELAY CONTROL CHART					JP#	ON	OFF														
					1																
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					* SEE ASSEMBLY INSTRUCTIONS																
PCB-2 DIGITAL CIRCUIT																					
AD-ETHERNETAPLDEVICE-SL																					
8		7		6		5		4		3		2		1							
P.O SPEC.		BK/BD SPEC.		SOCKET OEM		OEM PART#		HANDLER		SCHEMATIC		ANALOG DEVICES									
										HW TYPE : Customer Evaluation Z											
										Product(s): AD7124-4											
										: MAX32690											
										PACKAGE : LeadCount-lead BodySize PackageType-family											
										: Pitch-pitch StyleVendor Style											
										<User Define>											
										<User Define>											
										<User Define>											
										DESIGNER		19AUG24		MASTER PROJECT TEMPLATE		TESTER TEMPLATE		DRAWING NO.		REV.	
										D. VILLANUEVA		TBD		067706_a		02_083153		E			
										PTD ENGINEER		19AUG24		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES							
										D. SUDOY				TOLERANCES							
										CHECKER				DECIMALS FRACTIONS ANGLES							
										-				X.XX +0.010 X.XXX +-0.005 +-1/32 +-2							
														SIZE		SCALE		CODE ID NO.		SHEET 1 OF 10	
														D		1:1		CodeID			

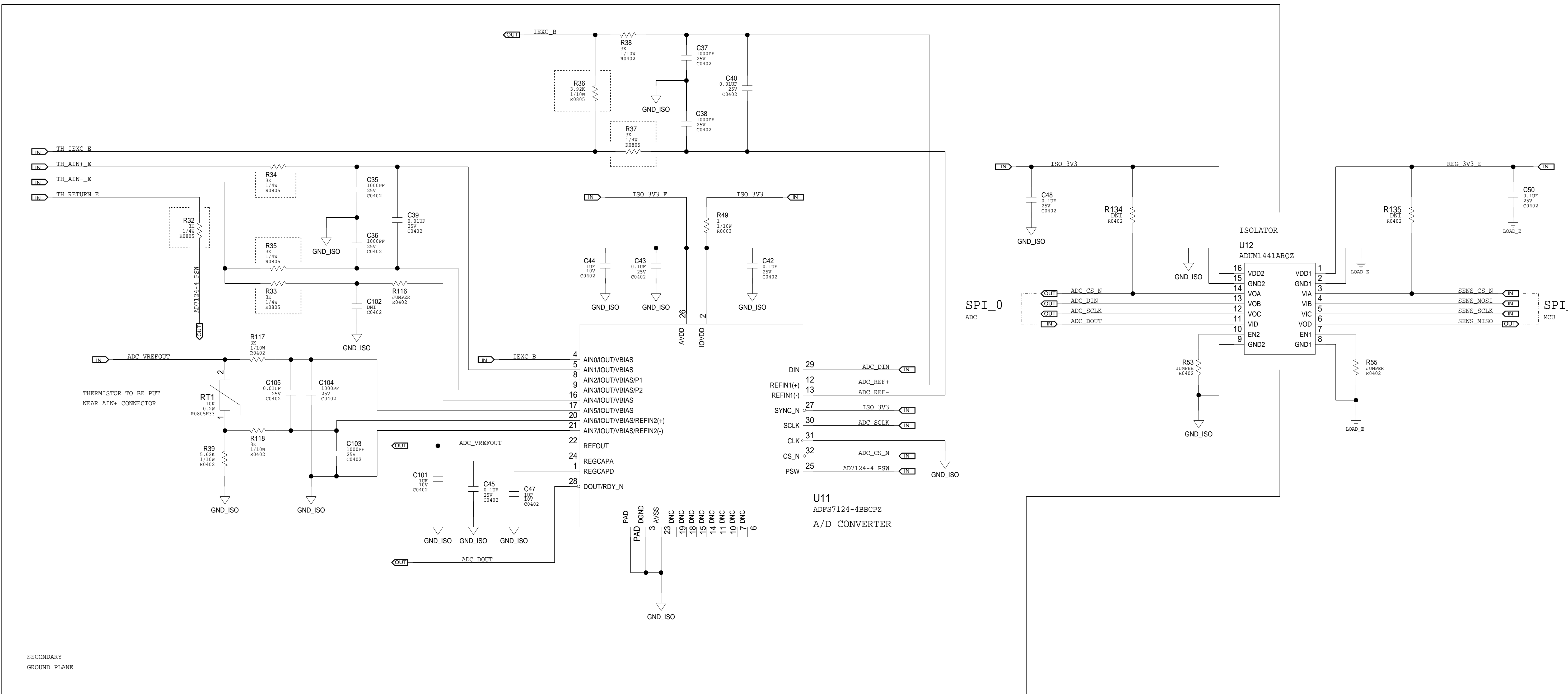
AD-ETHERNETAPLDEVICE-SL - BLOCK DIAGRAM



 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Z Product (s) : AD7124-4 : MAX32690			
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSES DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_083153	REV E
	PTD ENGINEER D. SUDOY	SIZE D	SCALE 1:1	SHEET 2 OF 10

SENSOR SIGNAL AND ISOLATION PATH

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



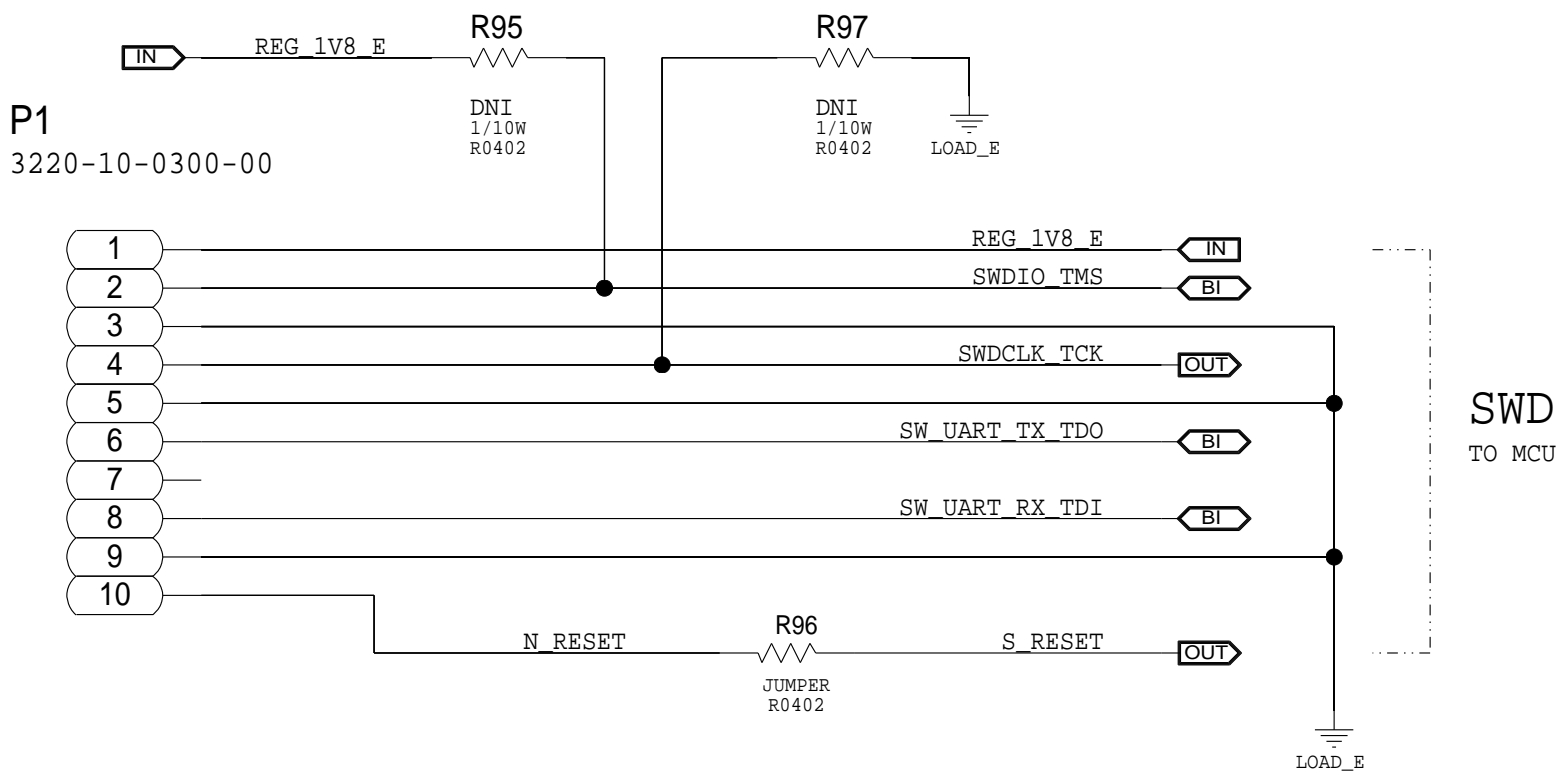
LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

LEGEND:
----- OR ----- 0.7MM MINIMUM CLEARANCE
----- OR ----- INTRINSIC SAFETY SPECS CONSIDERED

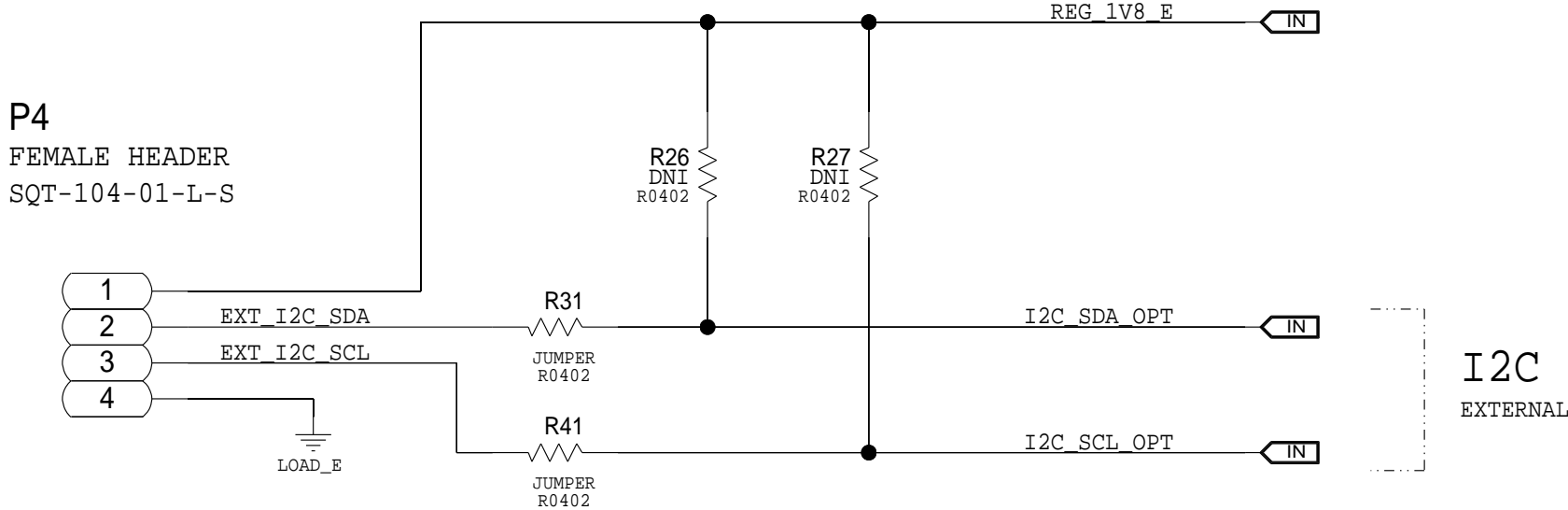
SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): AD7124-4 : MAX32690			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_083153	REV E	
PTD ENGINEER D. SUDOY	SIZE D	SCALE 1:1	SHEET 6 OF 10

CONNECTORS

SWD/PROGRAMMER CONNECTOR
FOR DEVELOPMENT ONLY

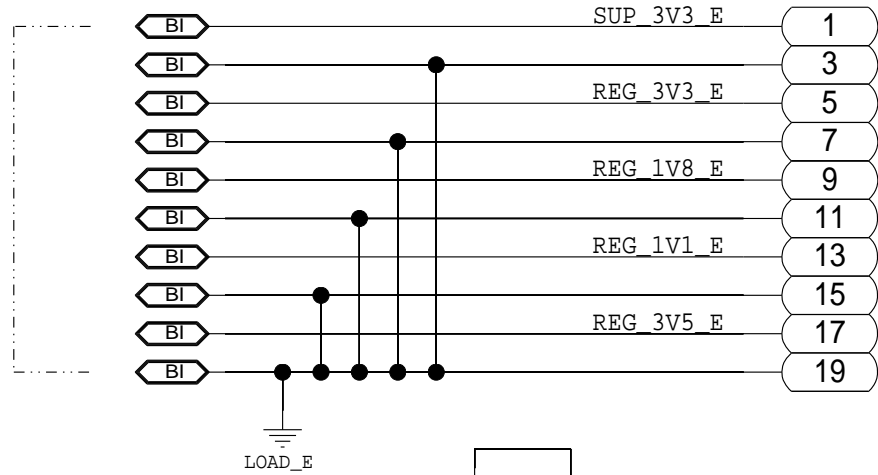


I2C CONNECTION FOR
USER DEVELOPMENT/DEBUGGING ONLY

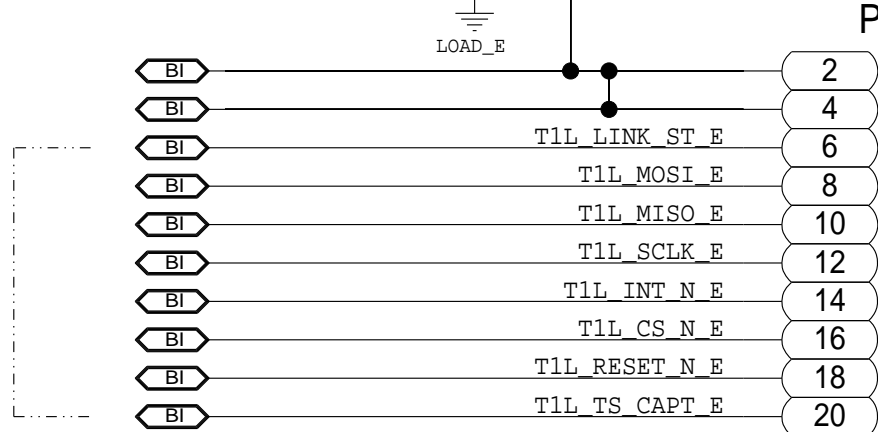


FEMALE HEADER
SFM-110-01-L-D-A

SUPPLY

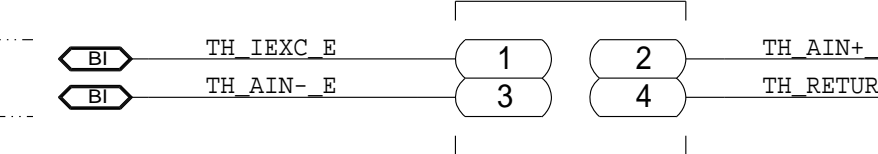


SPI_3
COMMS

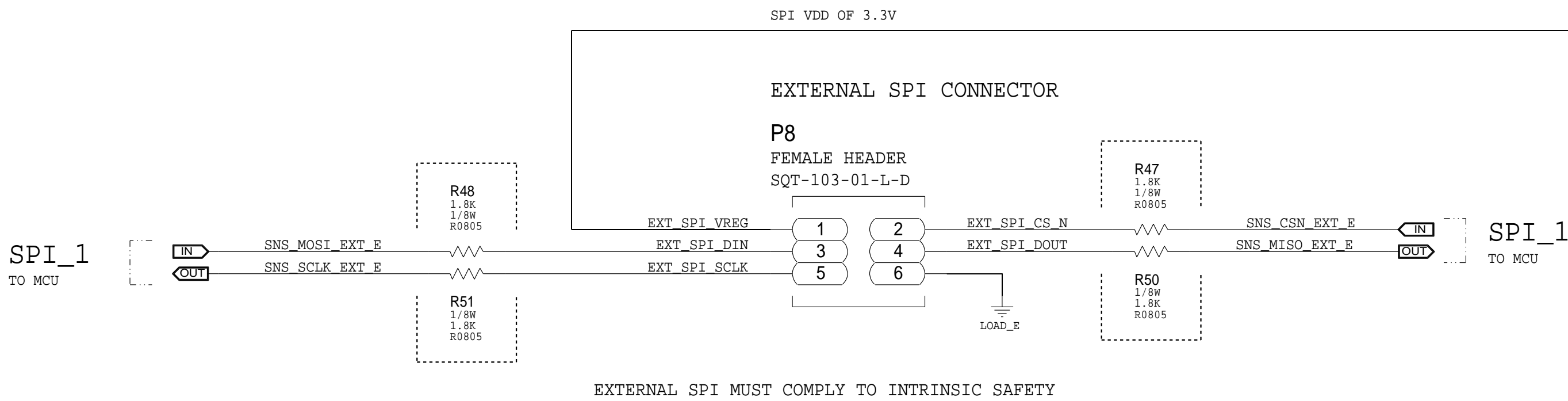


FEMALE HEADER
SFM-102-01-L-D-A

PT100
SENSOR TO ADC



PT100
SENSOR TO ADC



EXTERNAL SPI MUST COMPLY TO INTRINSIC SAFETY

LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

LEGEND:
..... OR [] 0.7MM MINIMUM CLEARANCE
[] INTRINSIC SAFETY SPECS CONSIDERED



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SCHEMATIC

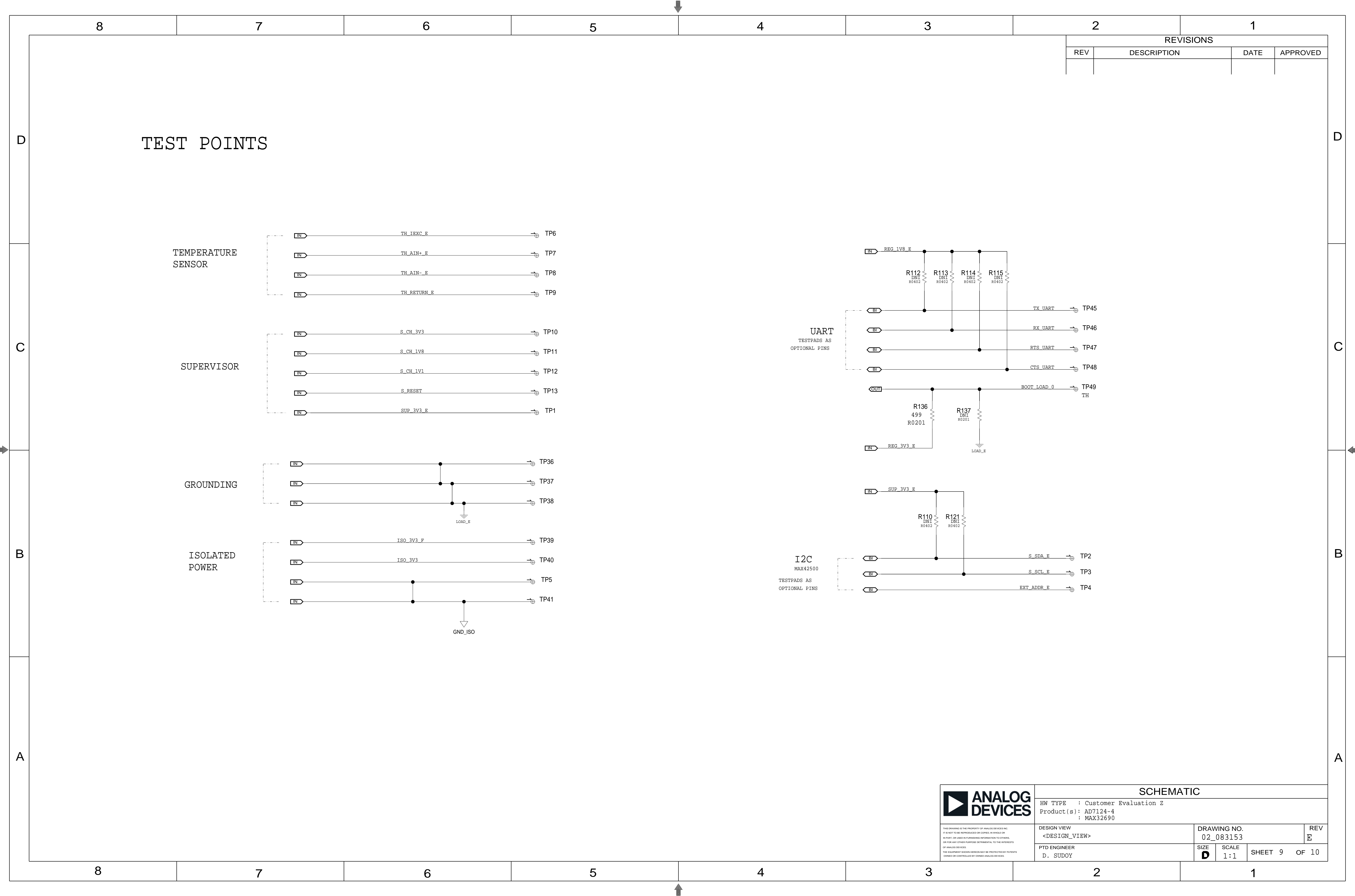
HW TYPE : Customer Evaluation Z
Product(s): AD7124-4
: MAX32690

DESIGN VIEW
<DESIGN_VIEW>
PTD ENGINEER
D. SUDOY

DRAWING NO.
02_083153

SIZE D SCALE 1:1 SHEET 8 OF 10

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ANALOG DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation Z

Product(s): AD7124-4

: MAX32690

DESIGN VIEW

<DESIGN_VIEW>

PTD ENGINEER

D. SUDOY

DRAWING NO.

02_083153

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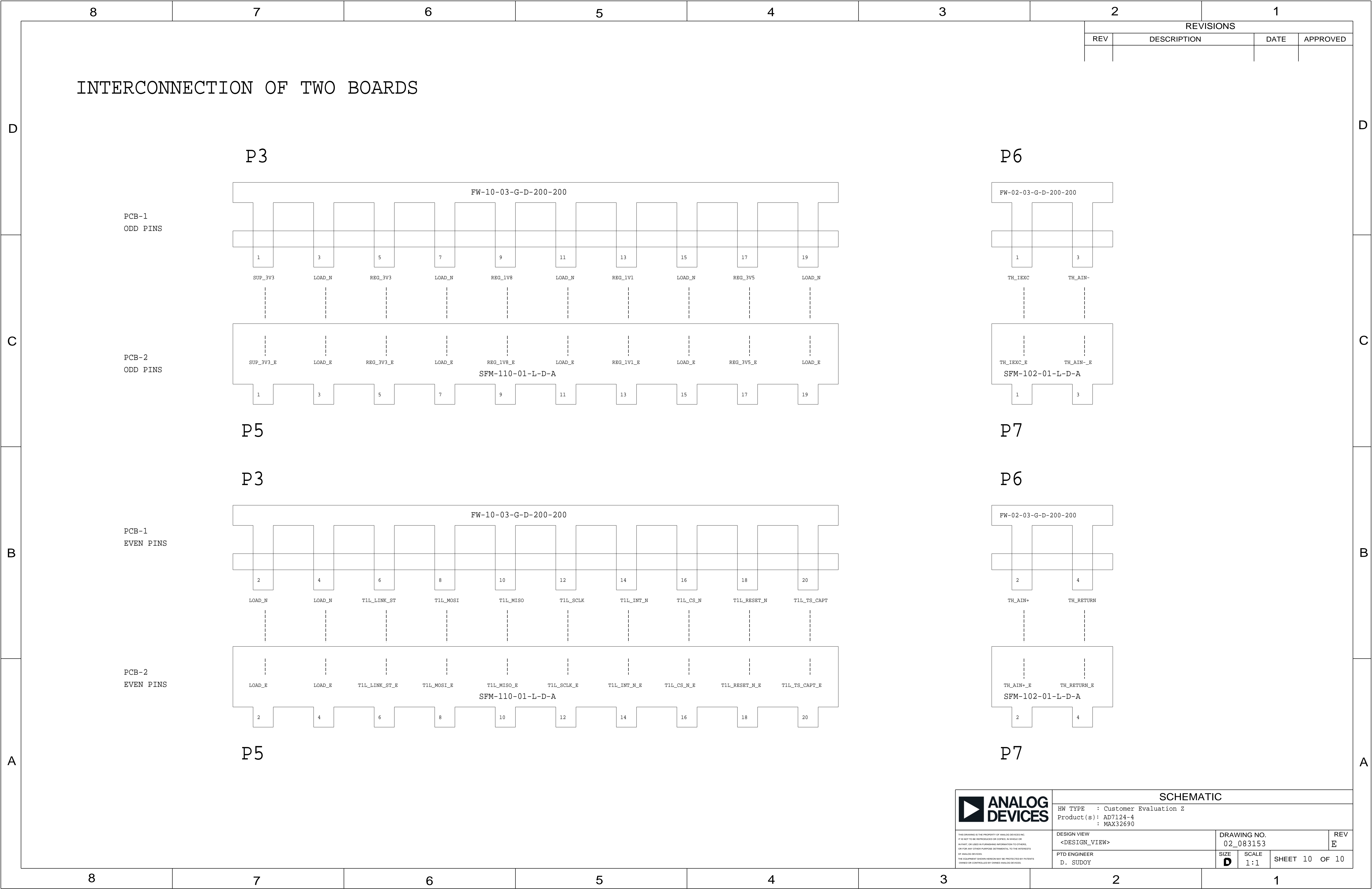
1:1

SHEET

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10



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SIZE

SCALE

SHEET 10 OF 10