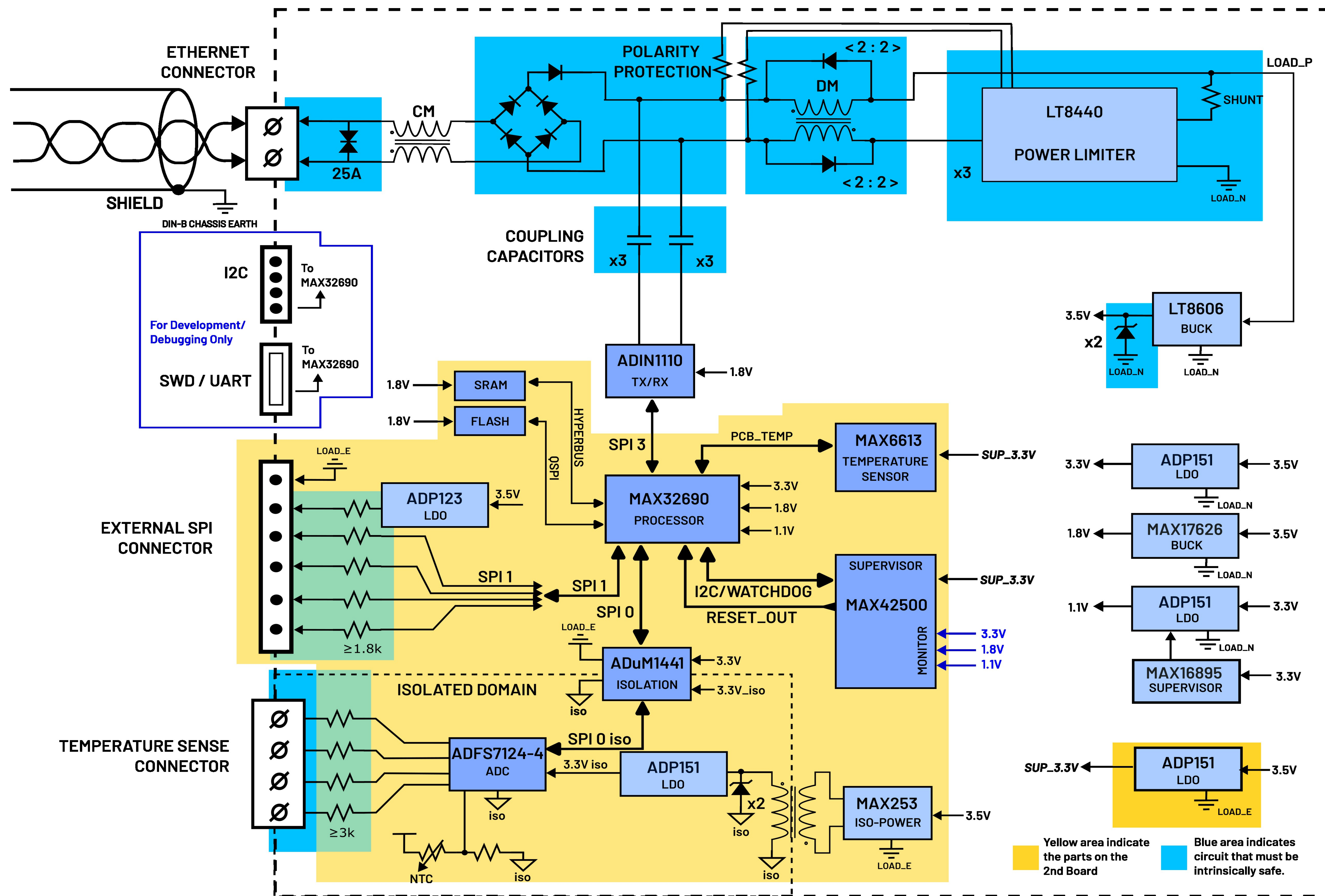


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D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					<table><tr><th colspan="3">JUMPER TABLE</th></tr><tr><th>JP#</th><th>ON</th><th>OFF</th></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table> <p>* SEE ASSEMBLY INSTRUCTIONS</p>										JUMPER TABLE			JP#	ON	OFF	1			2			3			4			5																																																																																
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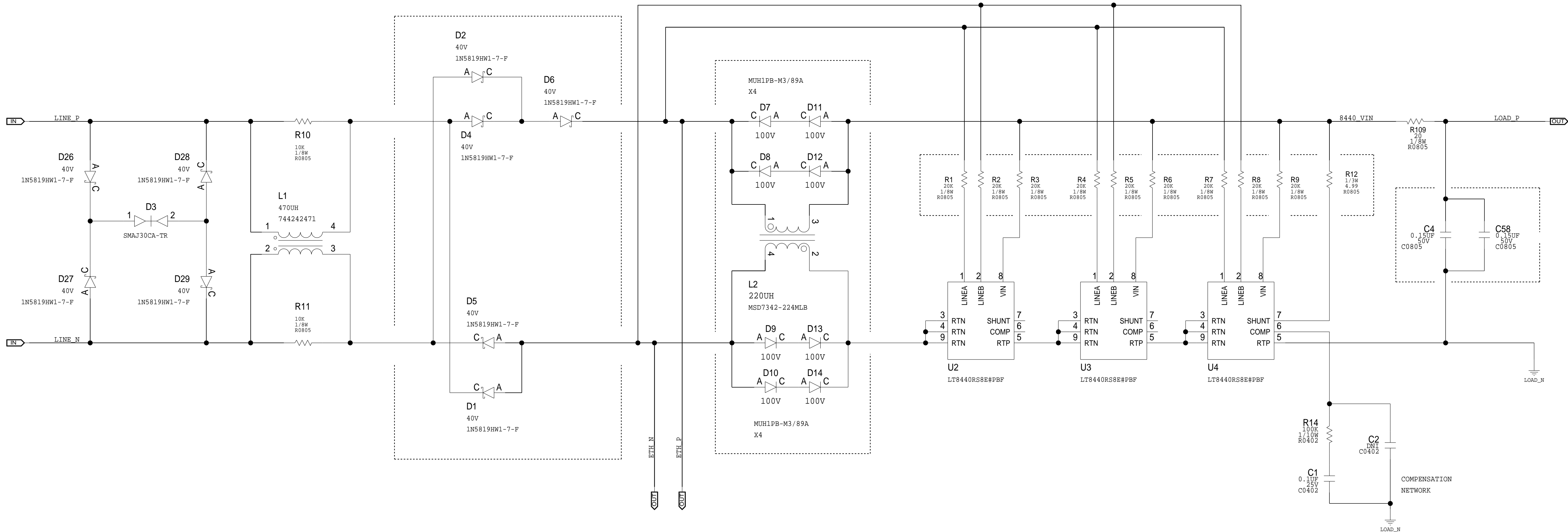
AD-ETHERNETAPLDEVICE-SL - BLOCK DIAGRAM

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
ANALOG DEVICES <small>THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>			
HW TYPE : Customer Evaluation Z Product(s): LT8440 : ADIN1110		DRAWING NO. 02_083152	REV B
DESIGN VIEW <DESIGN_VIEW> PTD ENGINEER D. SUDOY		SCALE 1:1	SHEET 2 OF 8

INPUT AND POWER CONDITIONING PATH

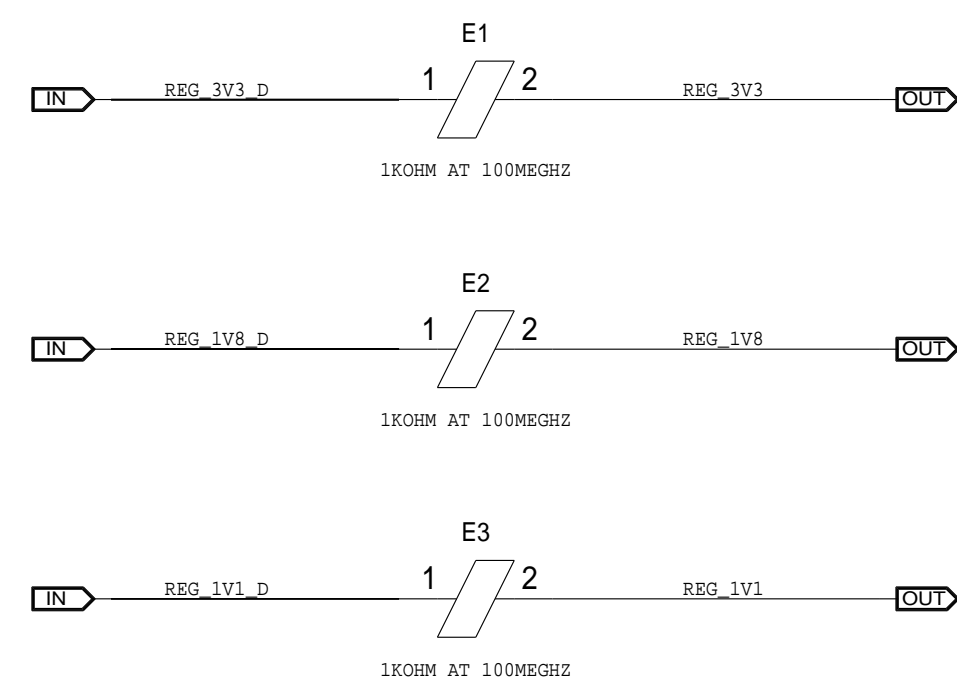
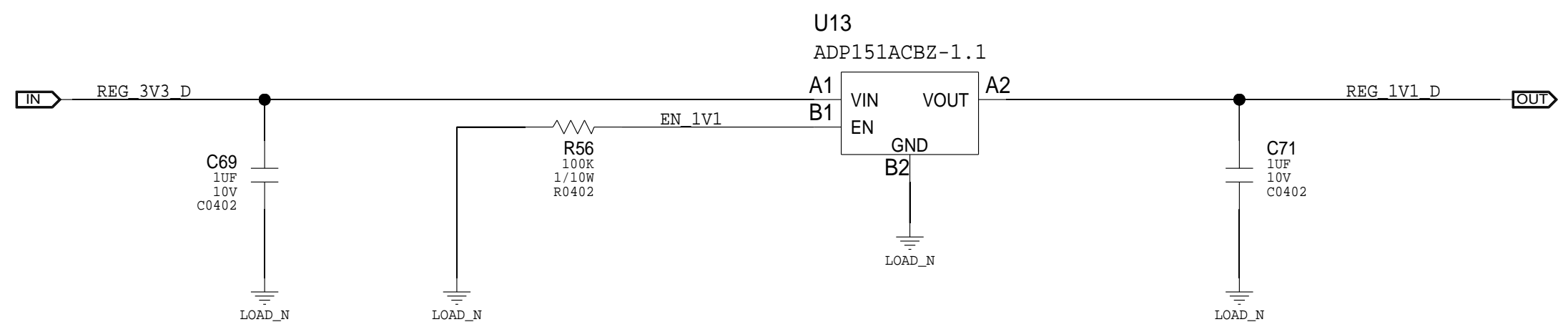
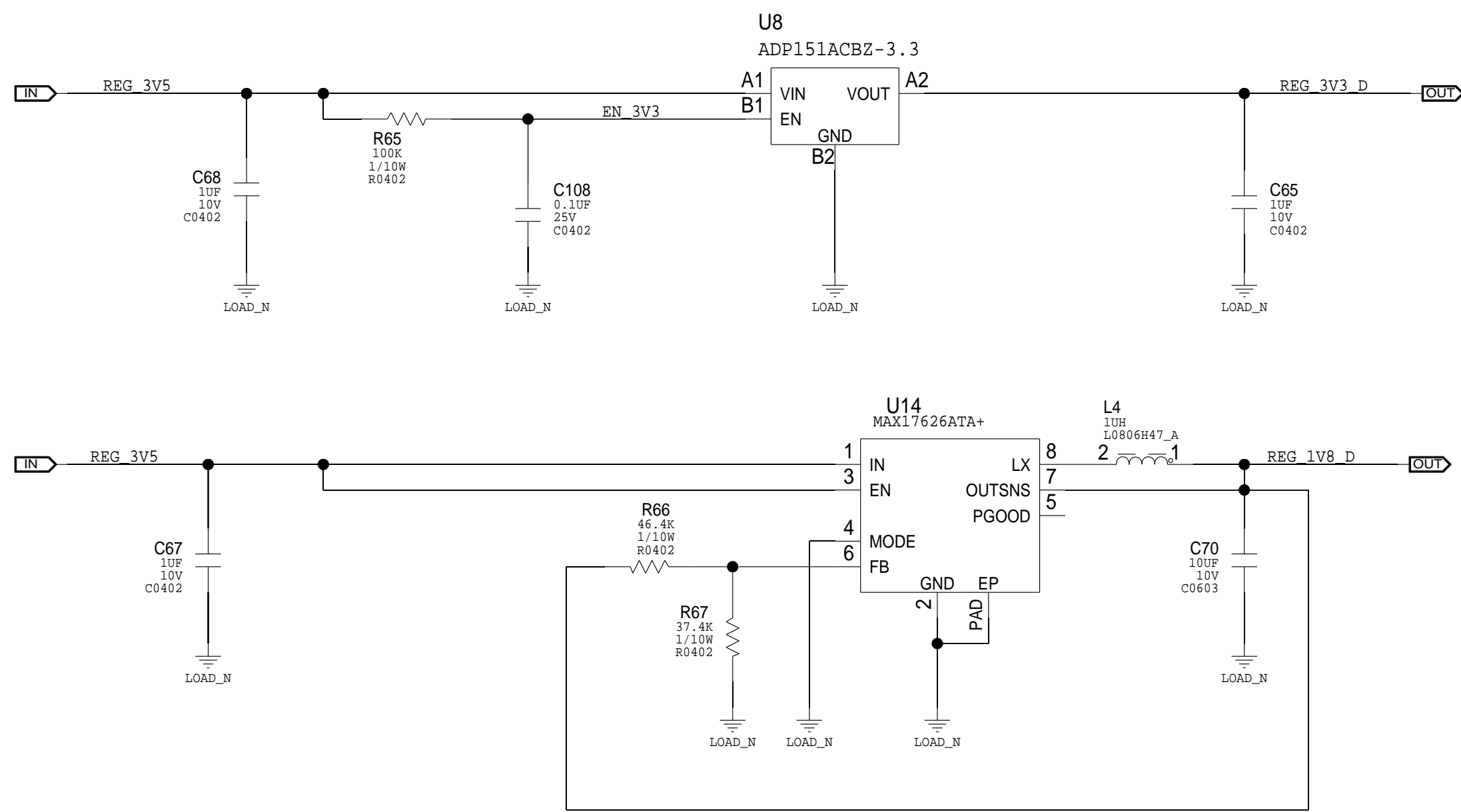
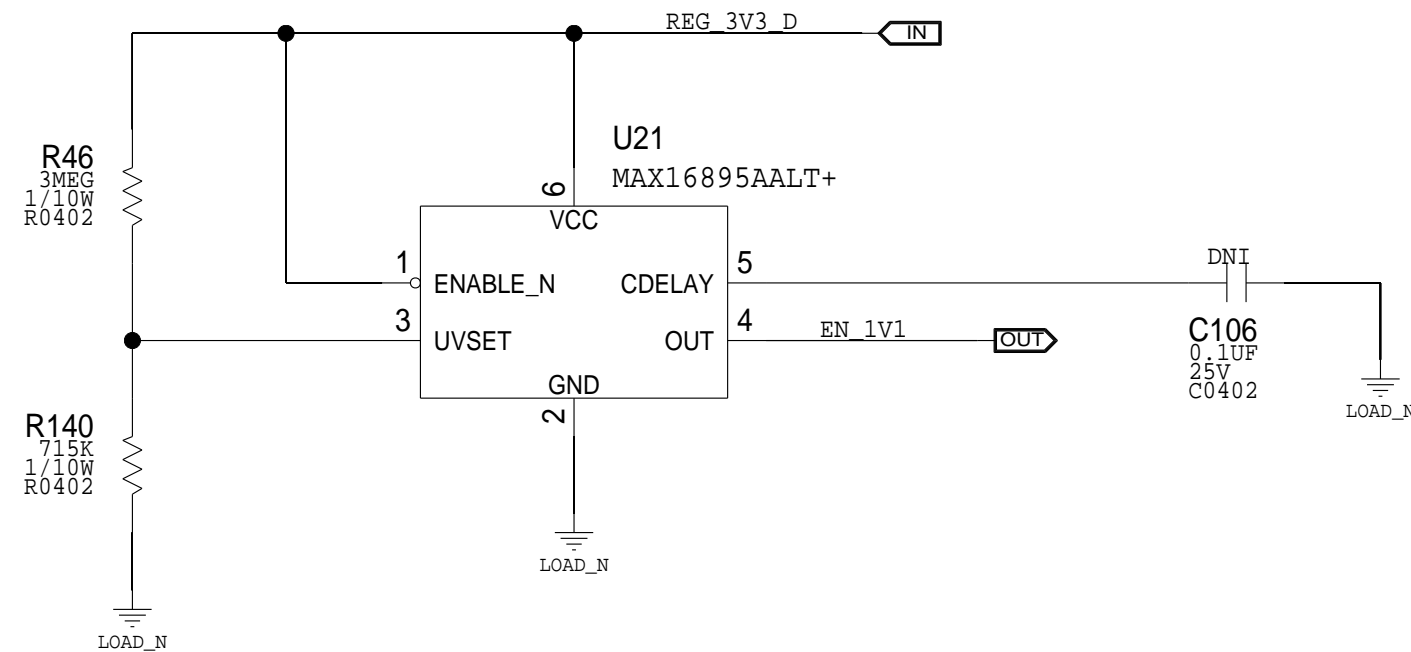
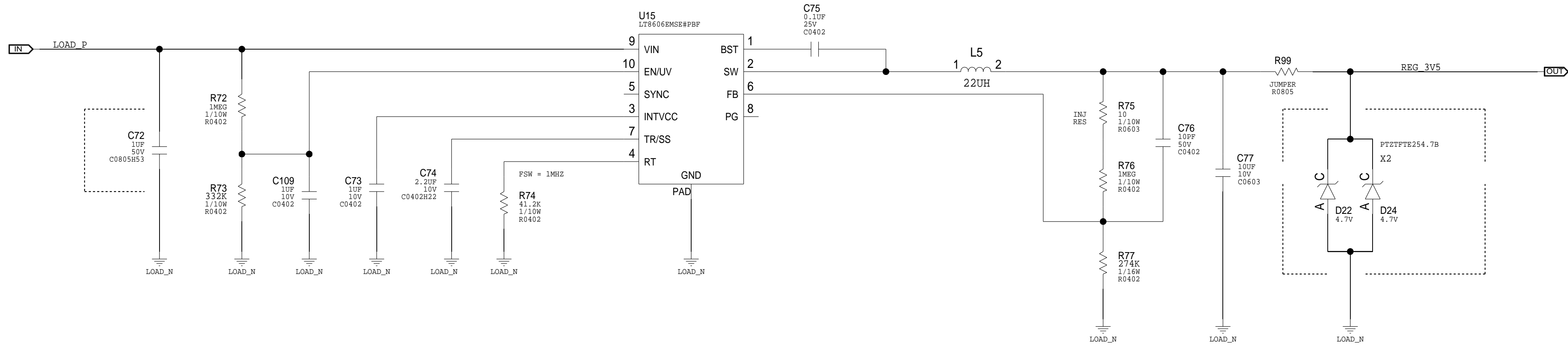


LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

LEGEND:
..... OR 0.7MM MINIMUM CLEARANCE
..... INTRINSIC SAFETY CONSIDERED

SCHEMATIC				
HW TYPE : Customer Evaluation Z				
Product(s): LT8440 : ADIN1110				
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_083152		REV B	
PTD ENGINEER D. SUDOY	SIZE D	SCALE 1:1	SHEET 3	OF 8

POWER RAILS PATH
3V3, 1V8, 1V1



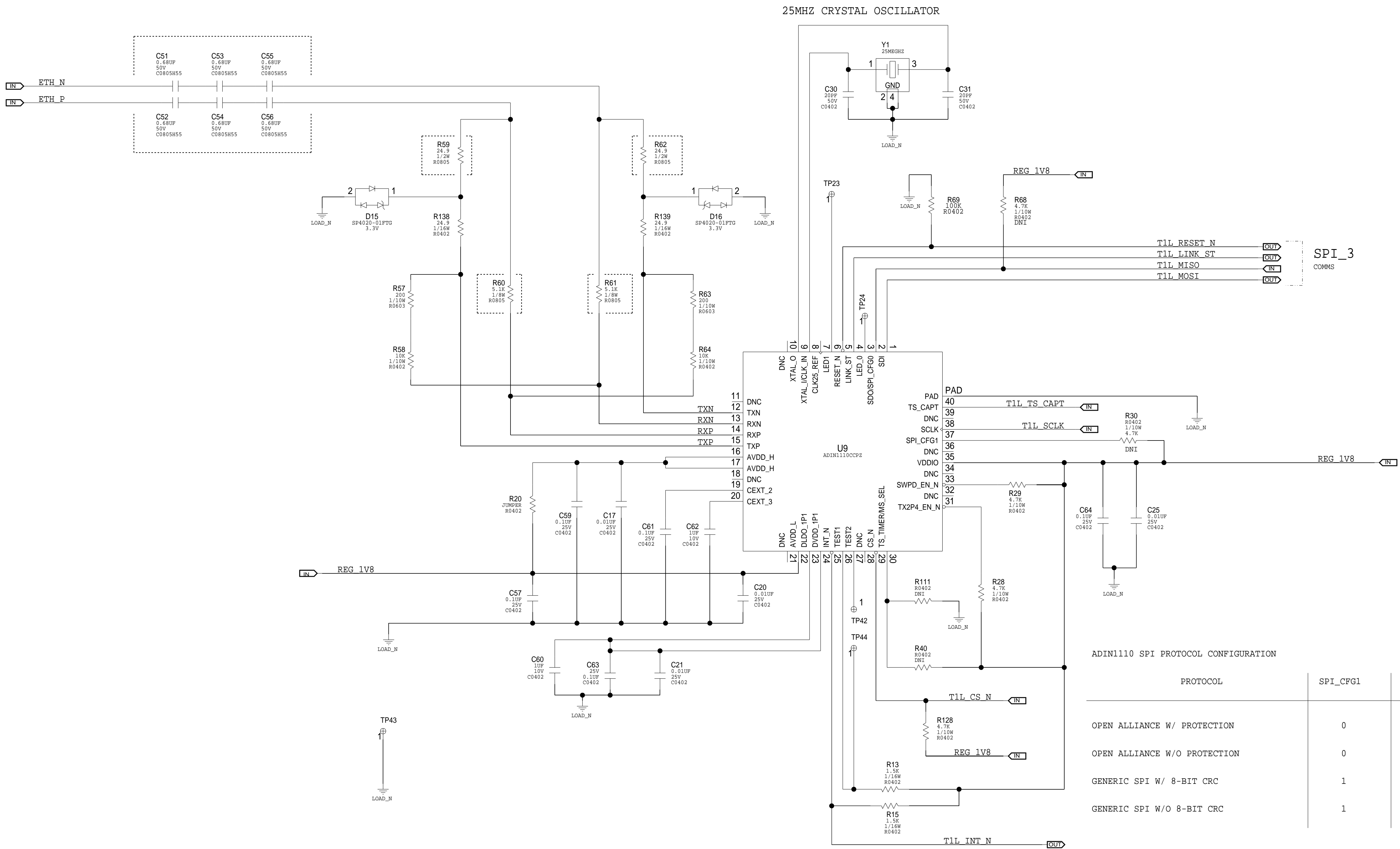
LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

LEGEND:
..... OR 0.7MM MINIMUM CLEARANCE
..... INTRINSIC SAFETY CONSIDERED

SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): LT8440 : ADIN1110			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_083152	REV B	
PTD ENGINEER D. SUDOY	SIZE D	SCALE 1:1	SHEET 4 OF 8

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

NETWORK SIGNAL PATH



LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

LEGEND:
..... OR 0.7MM MINIMUM CLEARANCE
..... INTRINSIC SAFETY SPECS CONSIDERED

SCHEMATIC

HW TYPE : Customer Evaluation Z
Product(s) : LT8440
: ADIN1110

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
D. SUDOY

DRAWING NO.
02_083152

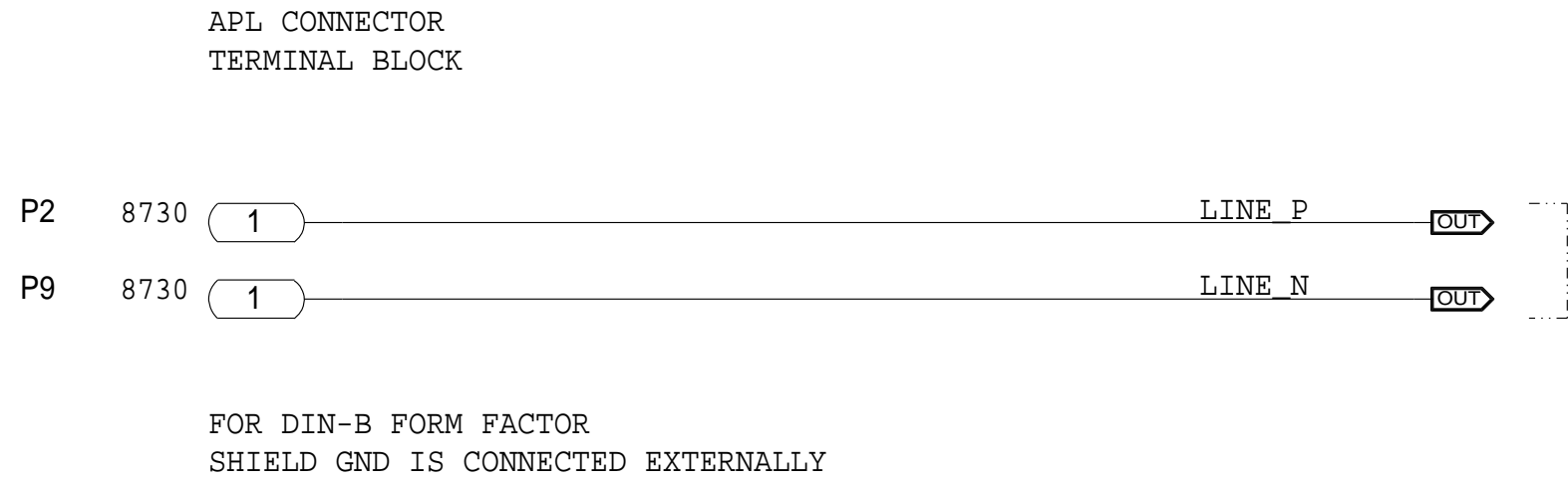
SIZE
D

SCALE
1:1

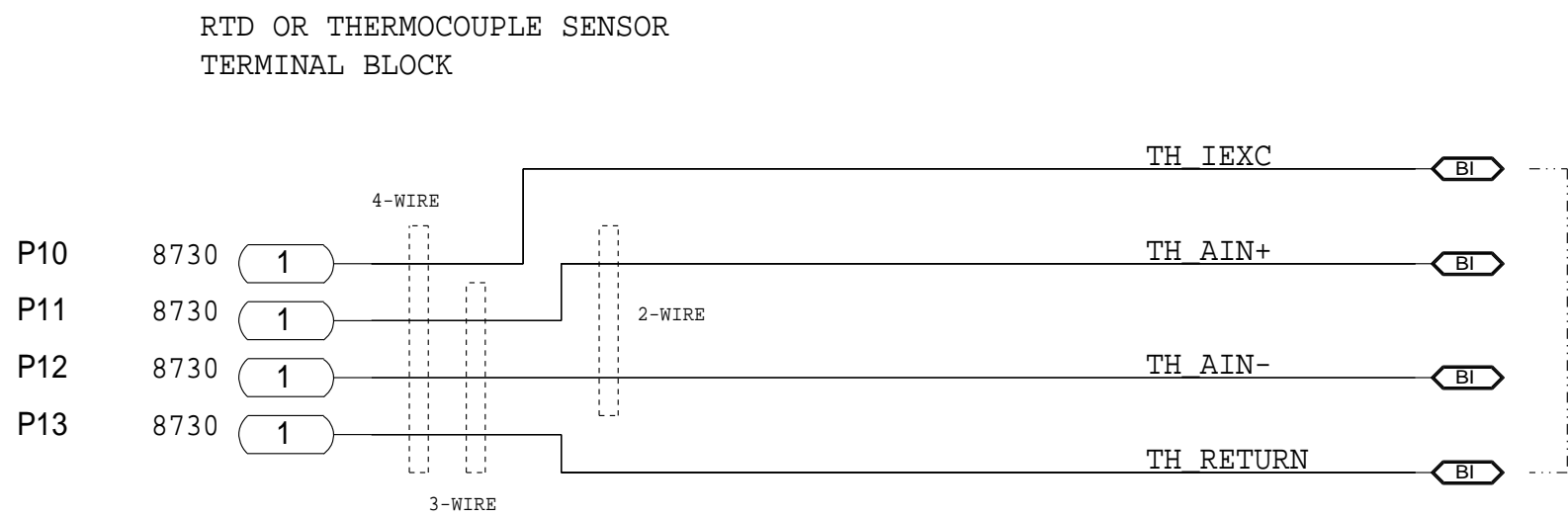
REV
B

SHEET 5 OF 8

CONNECTORS

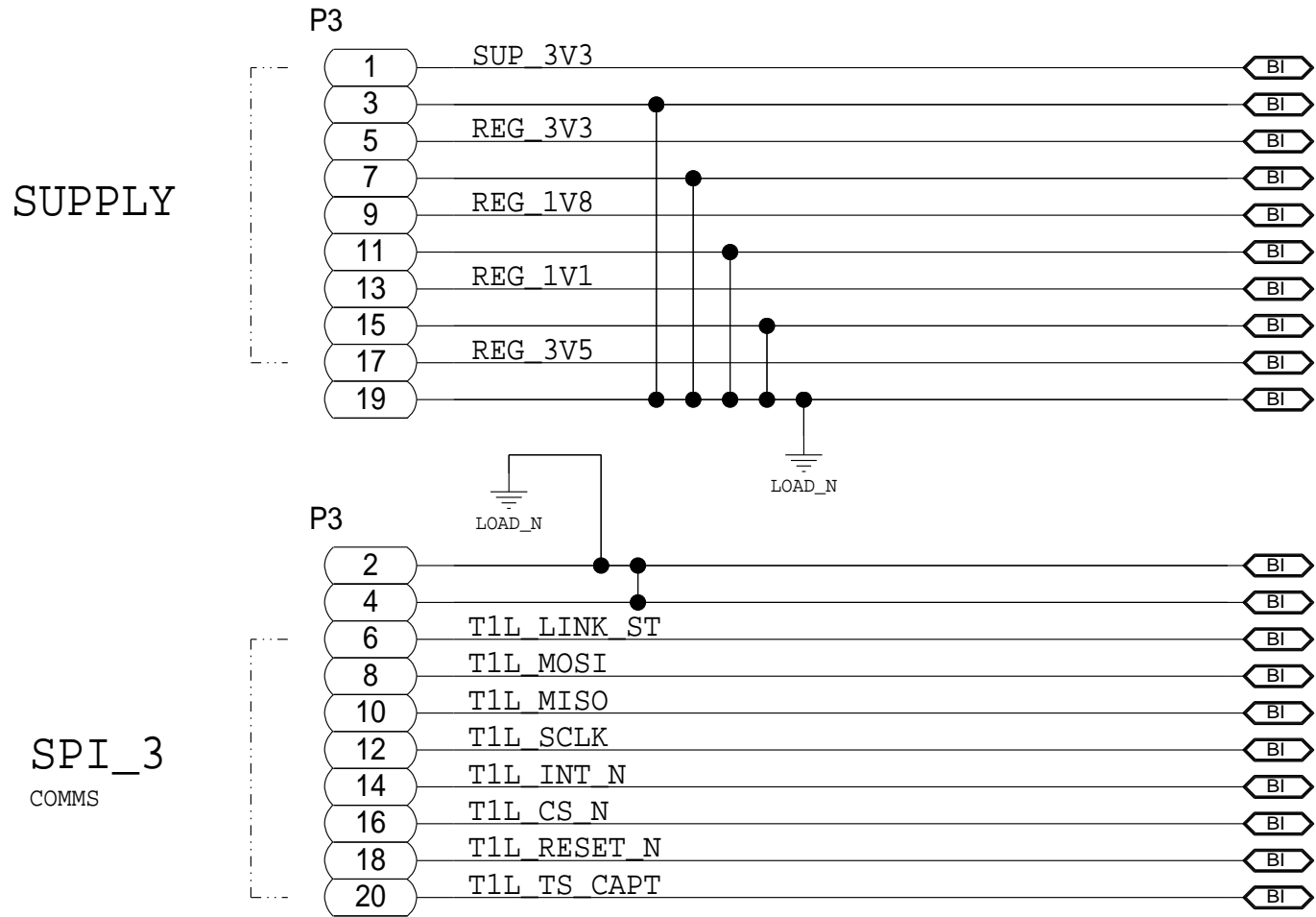


APL INPUT

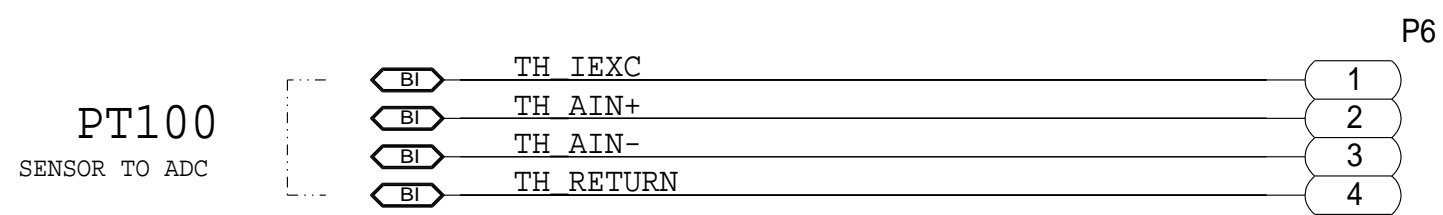


PT100
SENSOR TO ADC

FW-10-03-G-D-200-200
MALE HEADER



FW-02-03-G-D-200-200
MALE HEADER



LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT



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SCHEMATIC				
HW TYPE : Customer Evaluation Z				
Product(s): LT8440				
: ADIN1110				
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_083152		REV B	
PTD ENGINEER D. SUDYO	SIZE D	SCALE 1:1	SHEET 6	OF 8

