

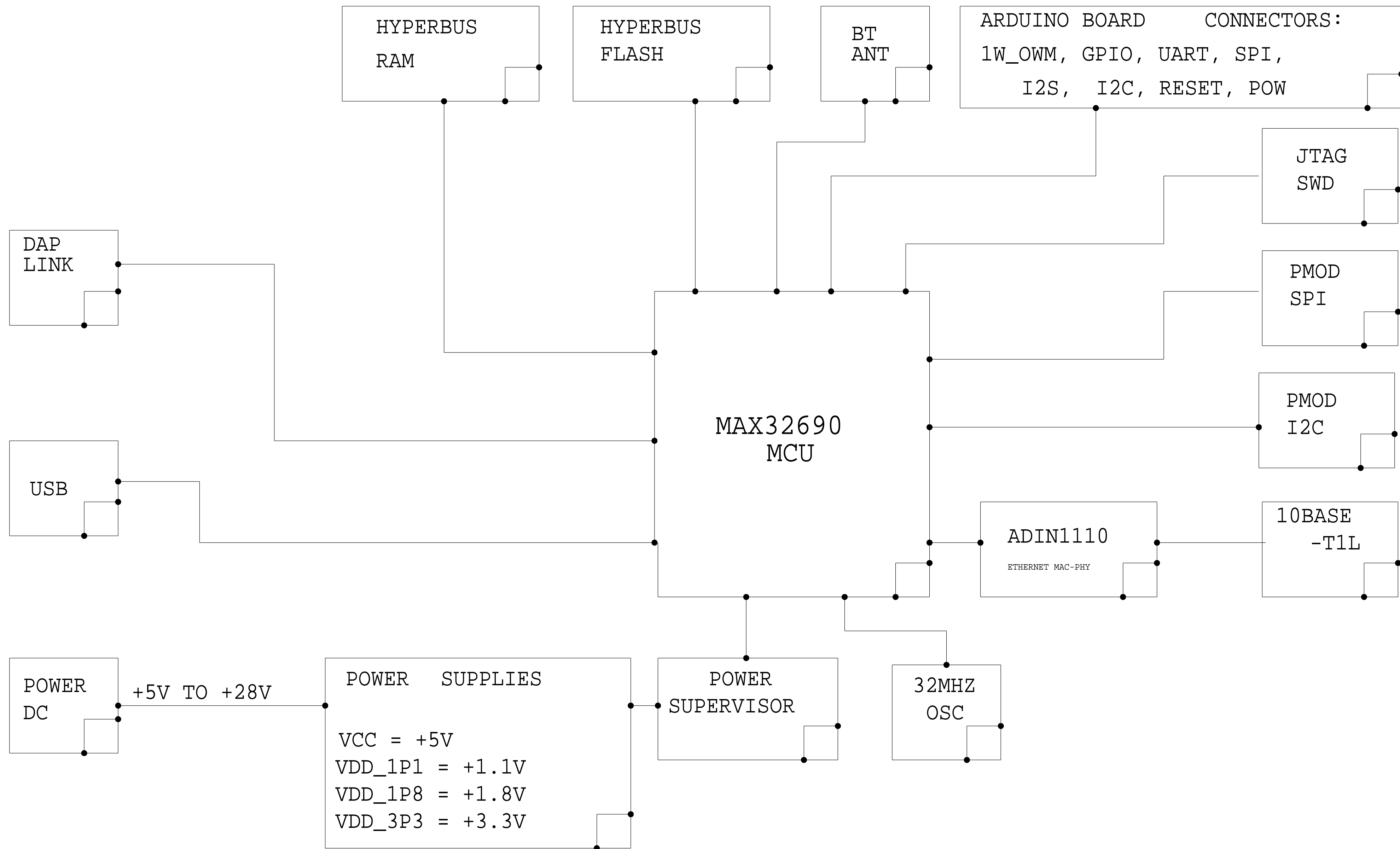
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D	<div>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</div>					<div>JUMPER TABLE</div> <table><tr><td>JP#</td><td>ON</td><td>OFF</td></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table>			JP#	ON	OFF	1			2			3			4			5													<div>REVISIONS</div> <table><tr><td>REV</td><td>DESCRIPTION</td><td>DATE</td><td>APPROVED</td></tr><tr><td>A</td><td>INITIAL RELEASE</td><td>03MAY23</td><td>M. BANCISOR</td></tr></table>				REV	DESCRIPTION	DATE	APPROVED	A	INITIAL RELEASE	03MAY23	M. BANCISOR
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	CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																																											
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					<div>TEMPLATE ENGINEER -</div> <div>DATE</div> <div>03MAY23</div> <div>HARDWARE SERVICES M. VALE</div> <div>HARDWARE SYSTEMS -</div> <div>TEST ENGINEER -</div> <div>COMPONENT ENGINEER N. CATALAN</div> <div>TEST PROCESS -</div> <div>HARDWARE RELEASE K. JABATAN</div> <div>DESIGNER S. DE CASTRO</div> <div>PTD ENGINEER M. BANCISOR</div> <div>CHECKER -</div> <div>DATE</div> <div>03MAY23</div> <div>03MAY23</div> <div>MASTER PROJECT TEMPLATE TBD</div> <div>TESTER TEMPLATE 067676_b</div> <div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</div> <div>DECIMALS X.XX +0.010 X.XXX +0.005</div> <div>TOLERANCES FRACTIONS +1/32</div> <div>ANGLES +2</div> <div>DRAWING NO. 02_073637</div> <div>REV. A</div> <div>SIZE D</div> <div>SCALE 1:1</div> <div>CODE ID NO. CodeID</div> <div>SHEET 1 OF 7</div>																																											
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER																																												
8		7		6		5		4		3		2		1																																		

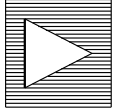
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PAGE 6: POWER SUPPLIES  
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

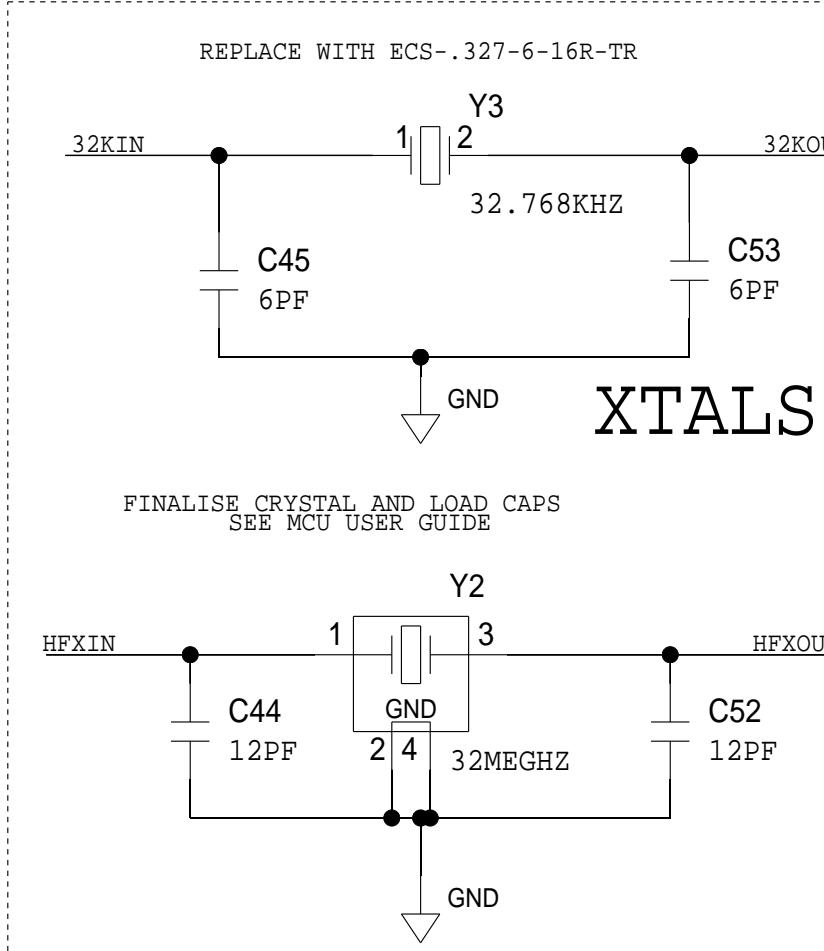
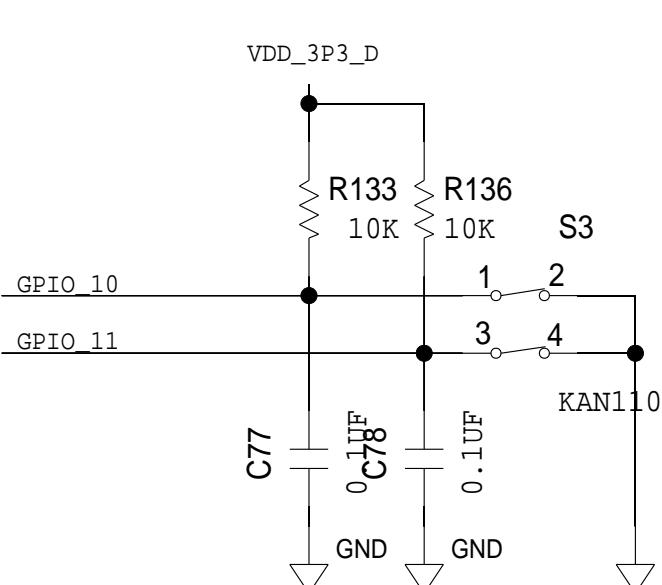
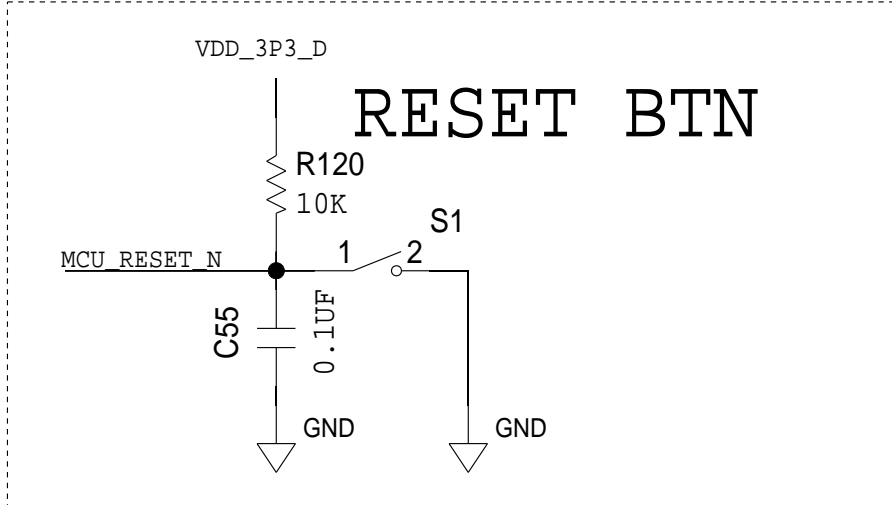
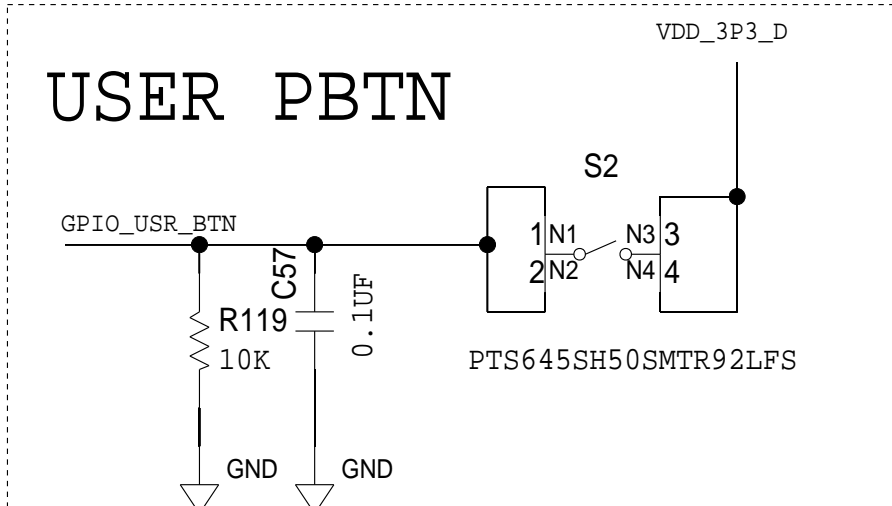
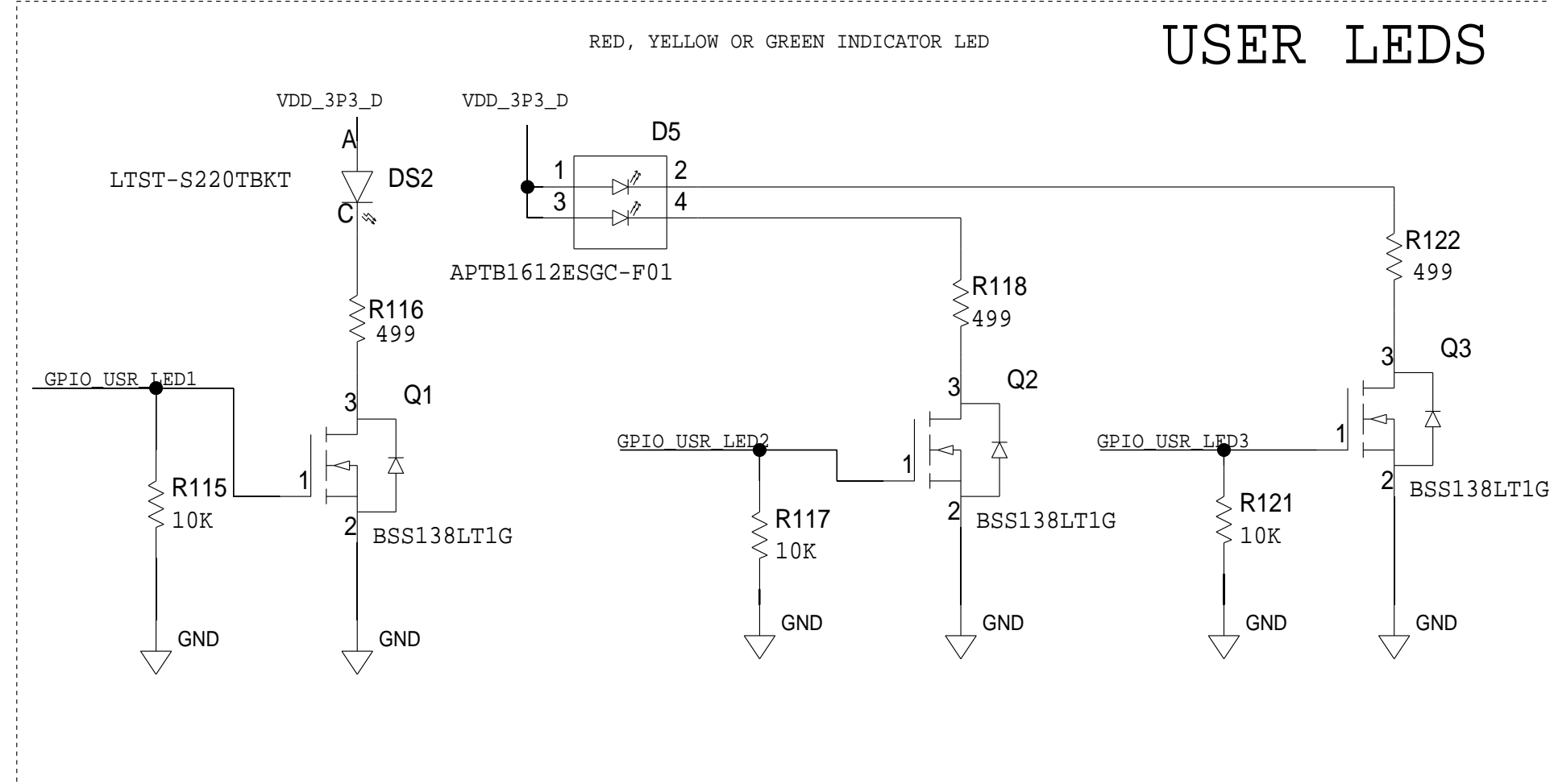
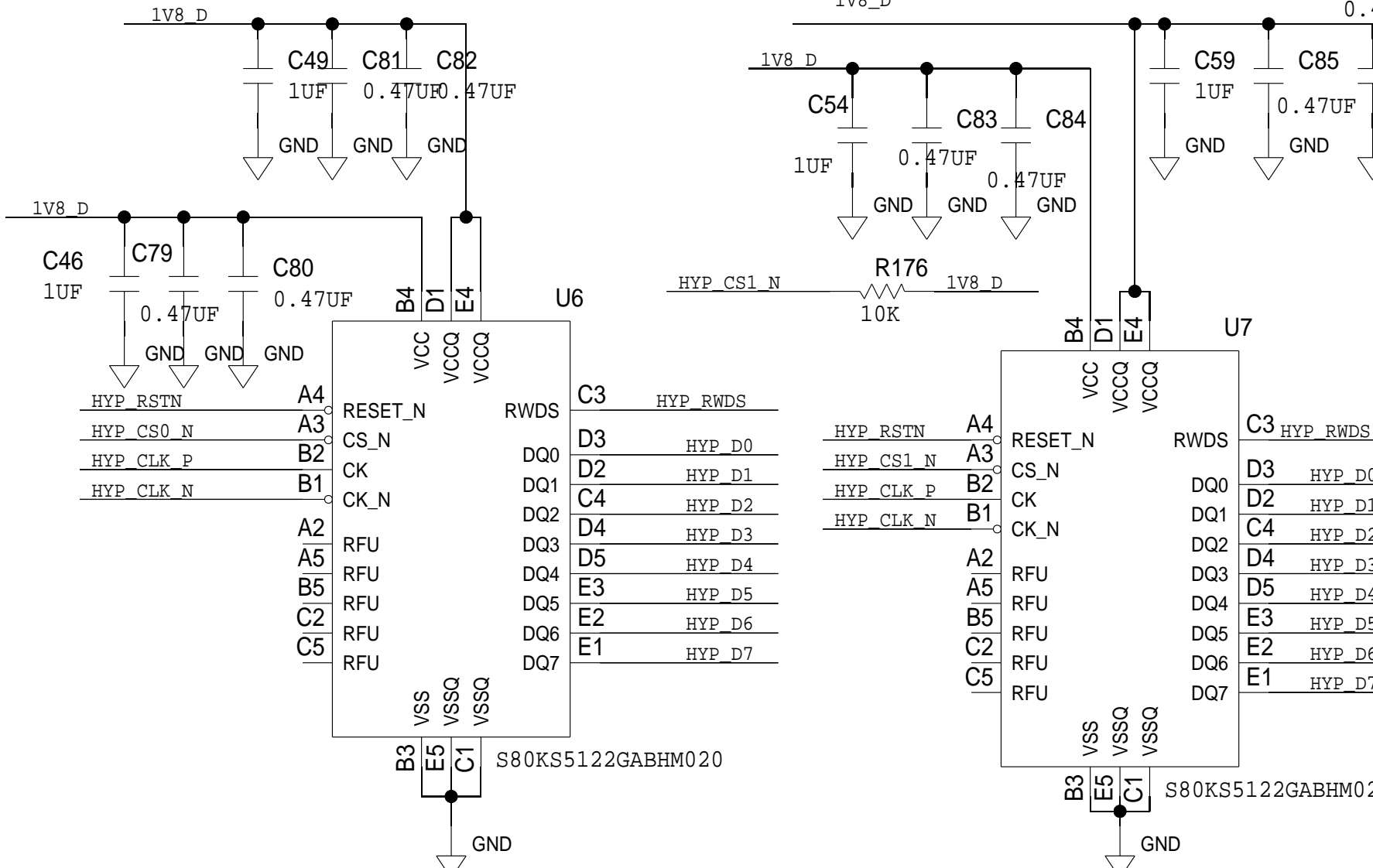
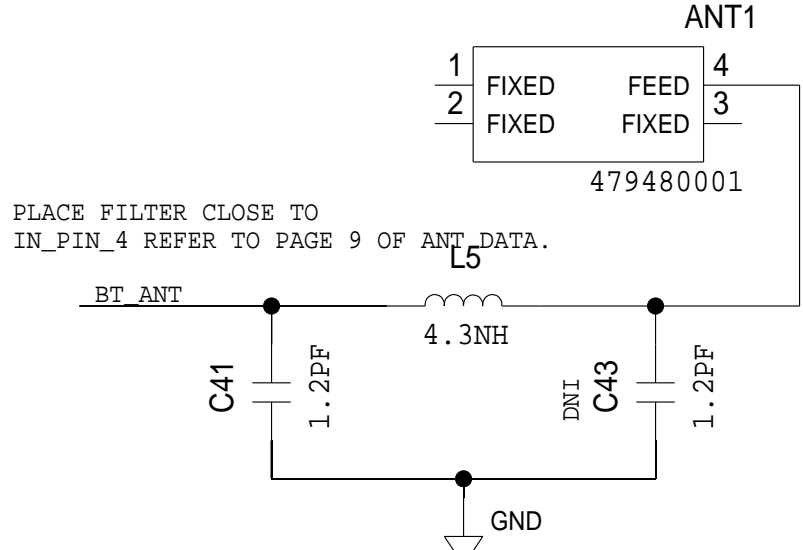
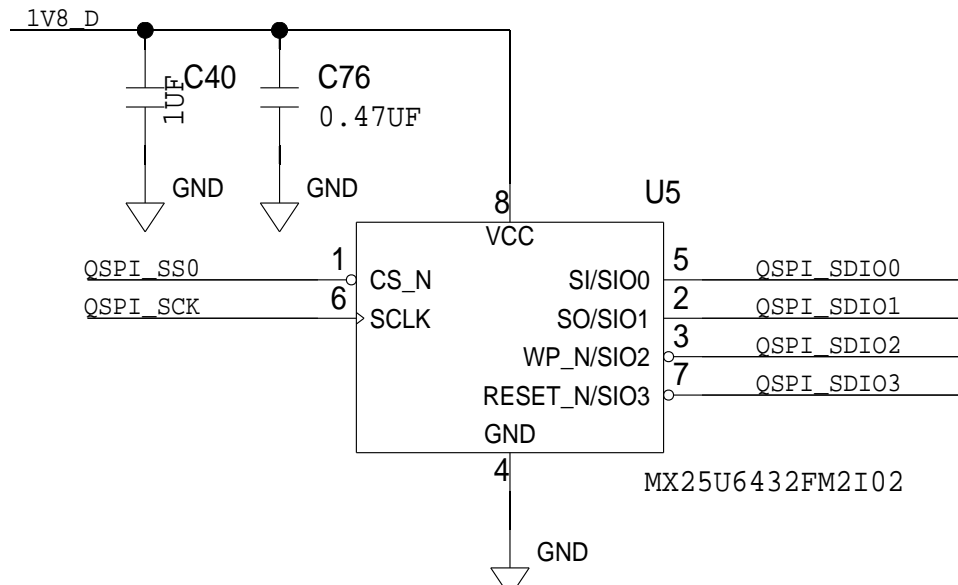
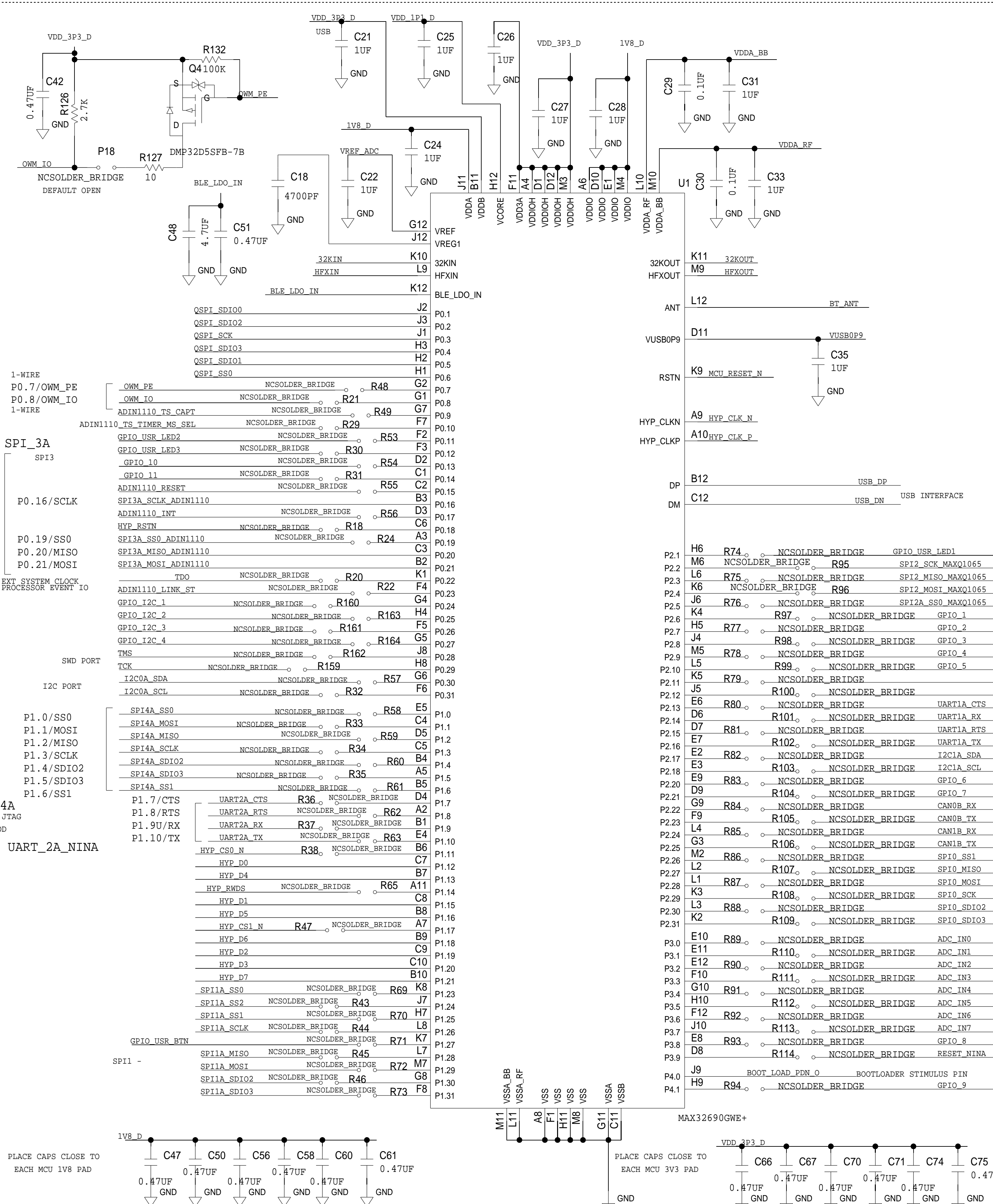
BLOCK DIAGRAM




 <b>ANALOG DEVICES</b> <small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	<b>SCHEMATIC</b>		
	HW TYPE : Eval_MAX32690_ARDZ Product(s): MAX32690 : MAX32690		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073637	REV A
	PTD ENGINEER M. BANCISOR	SIZE <b>D</b>	SCALE 1:1

# MCU & MEMORY

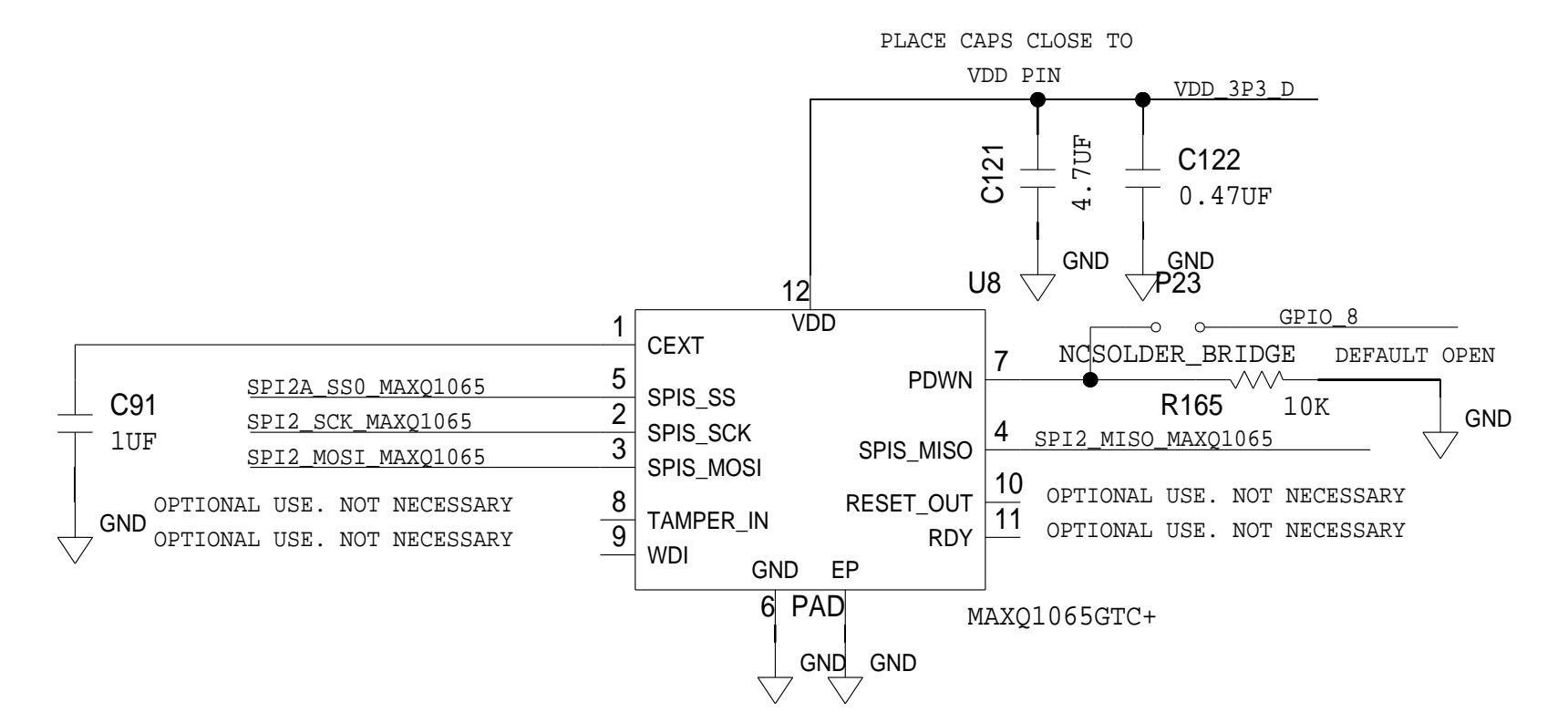
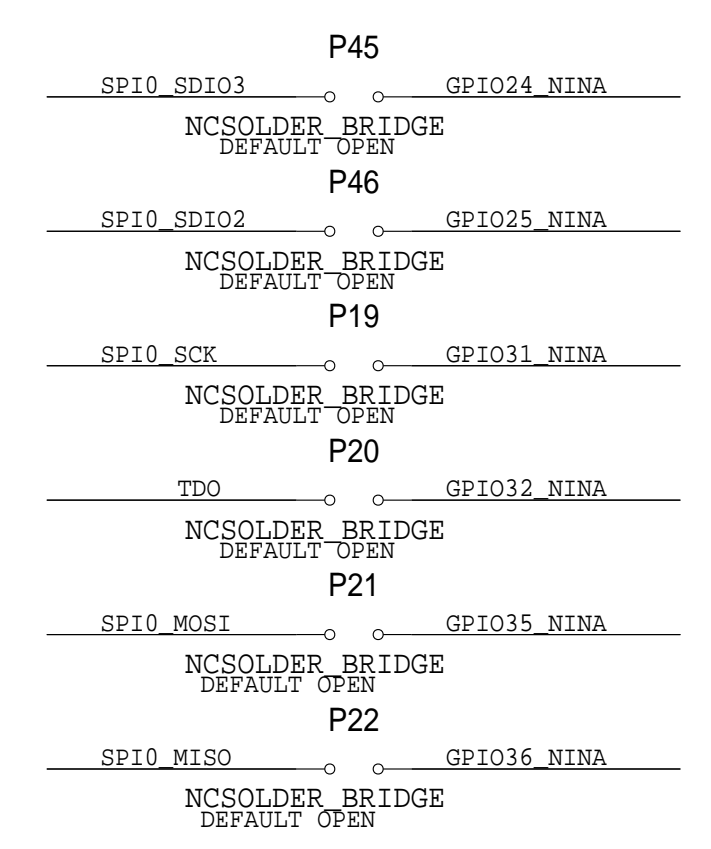
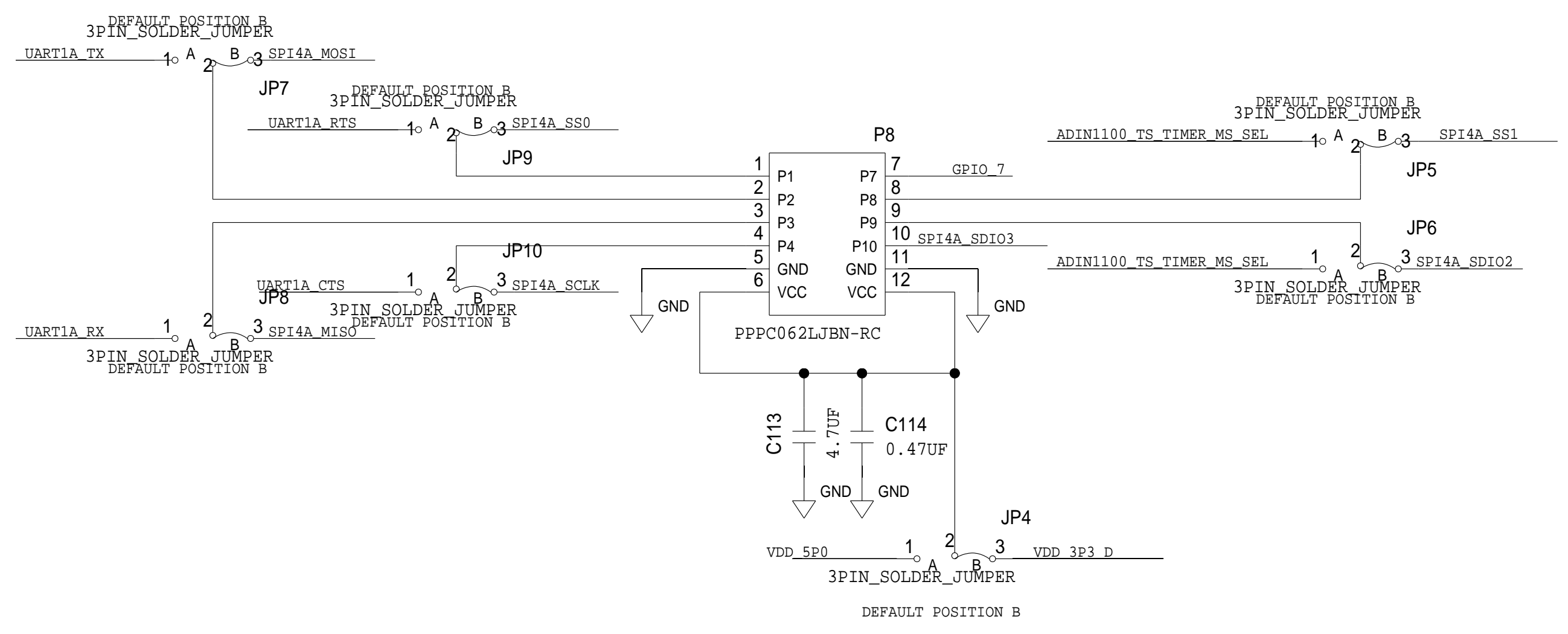
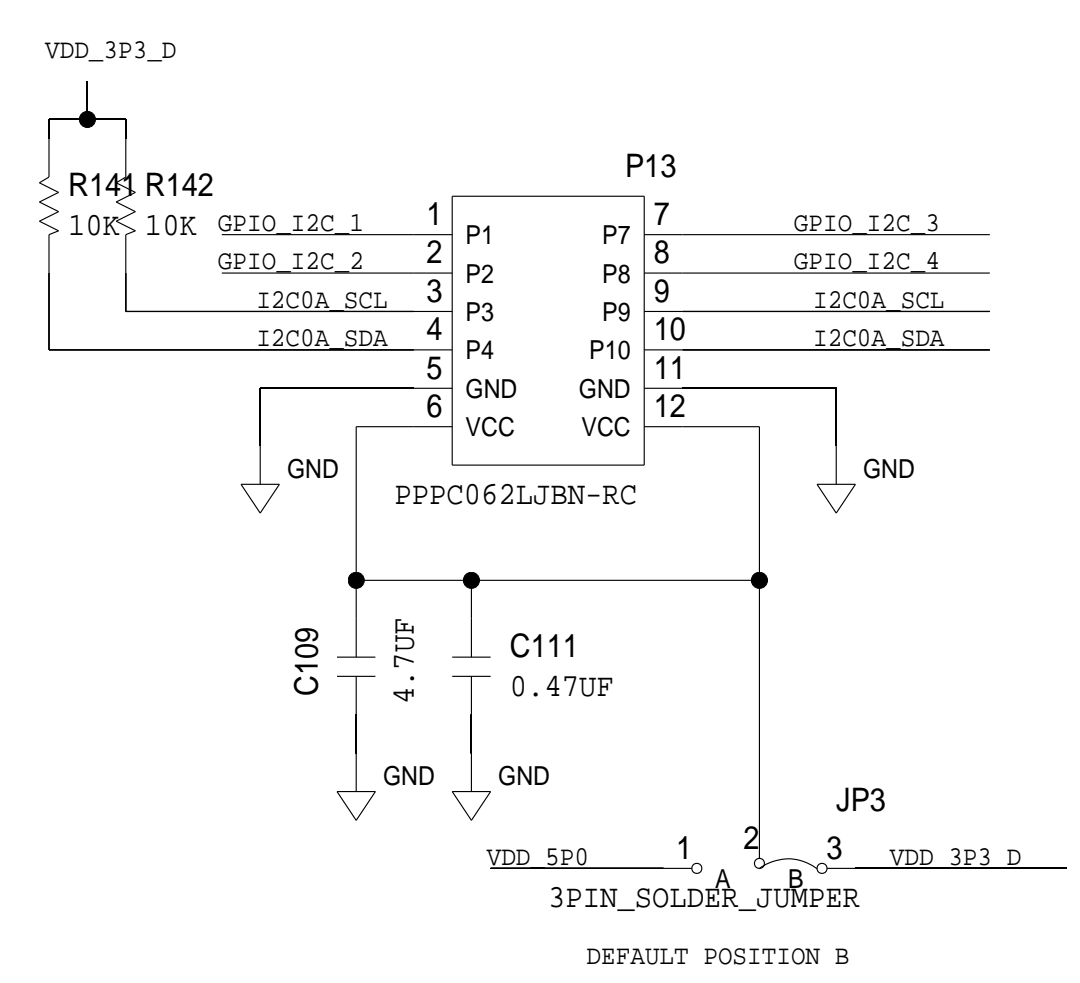
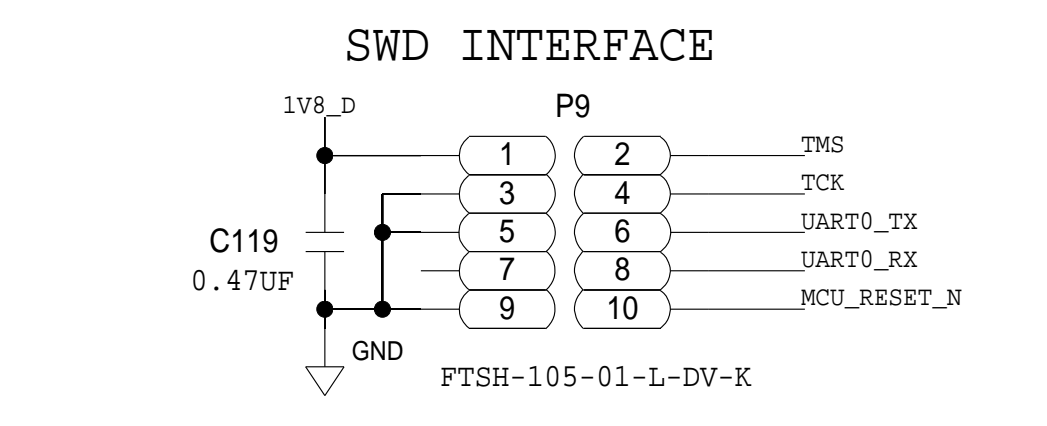
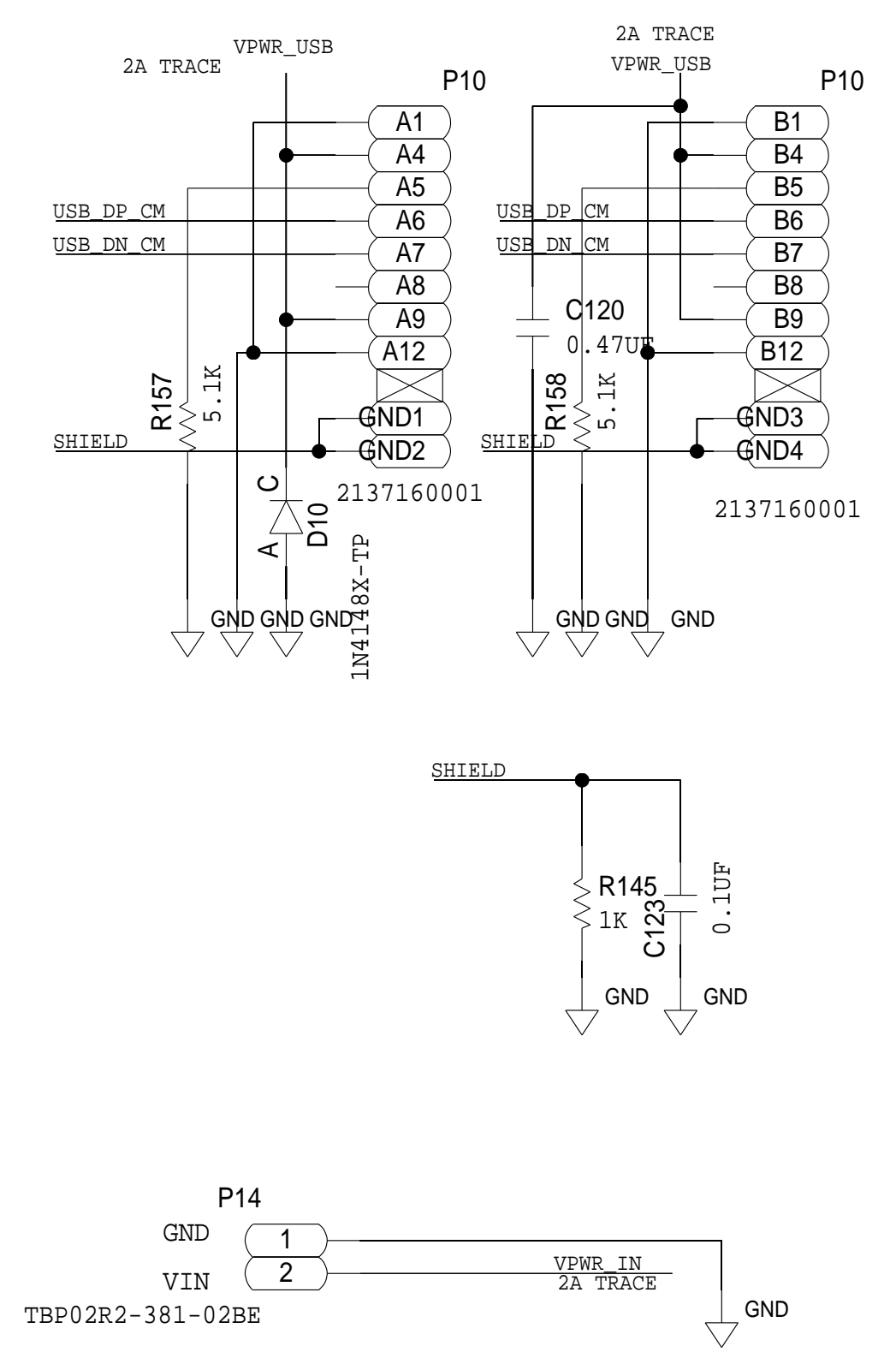
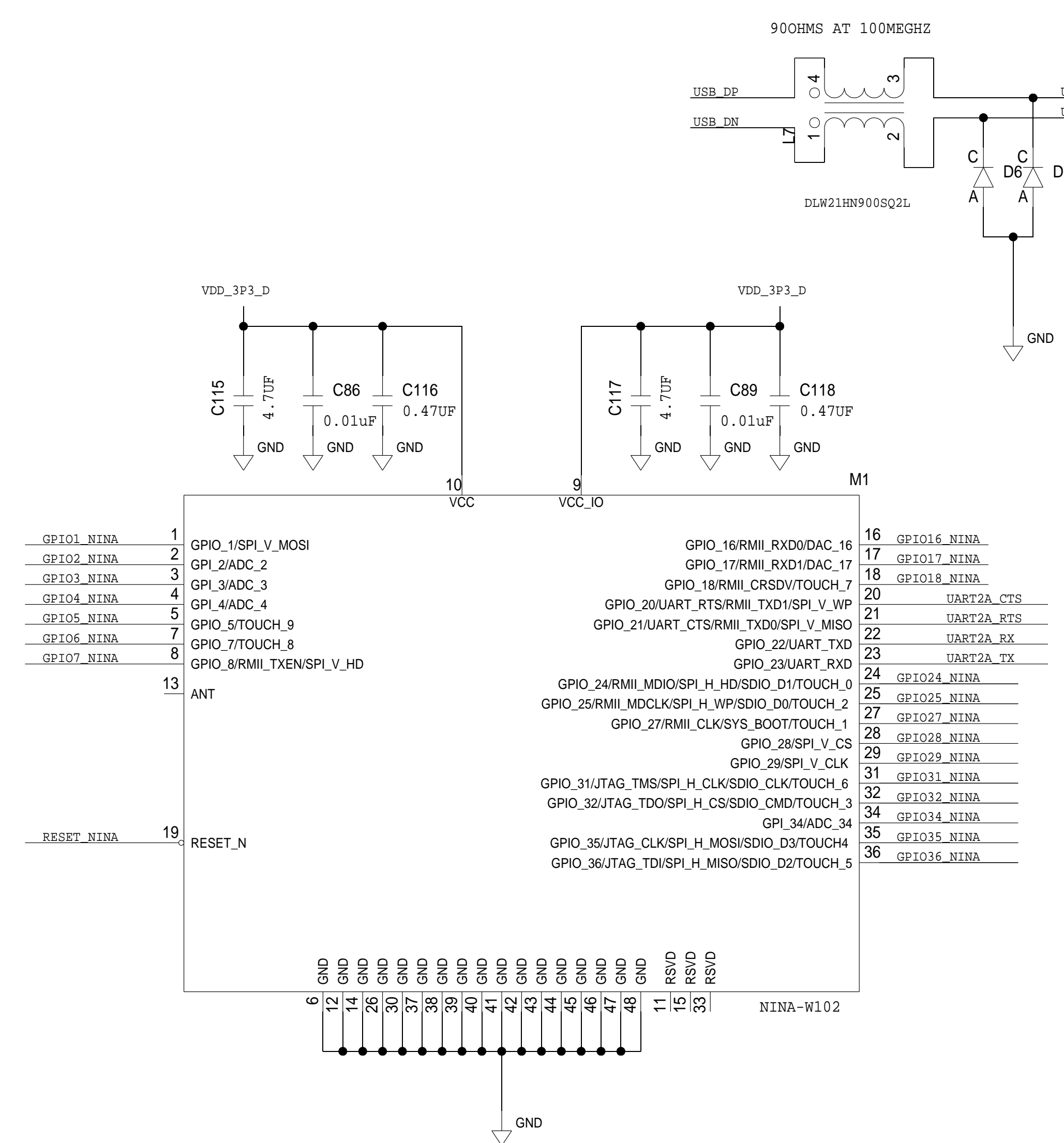
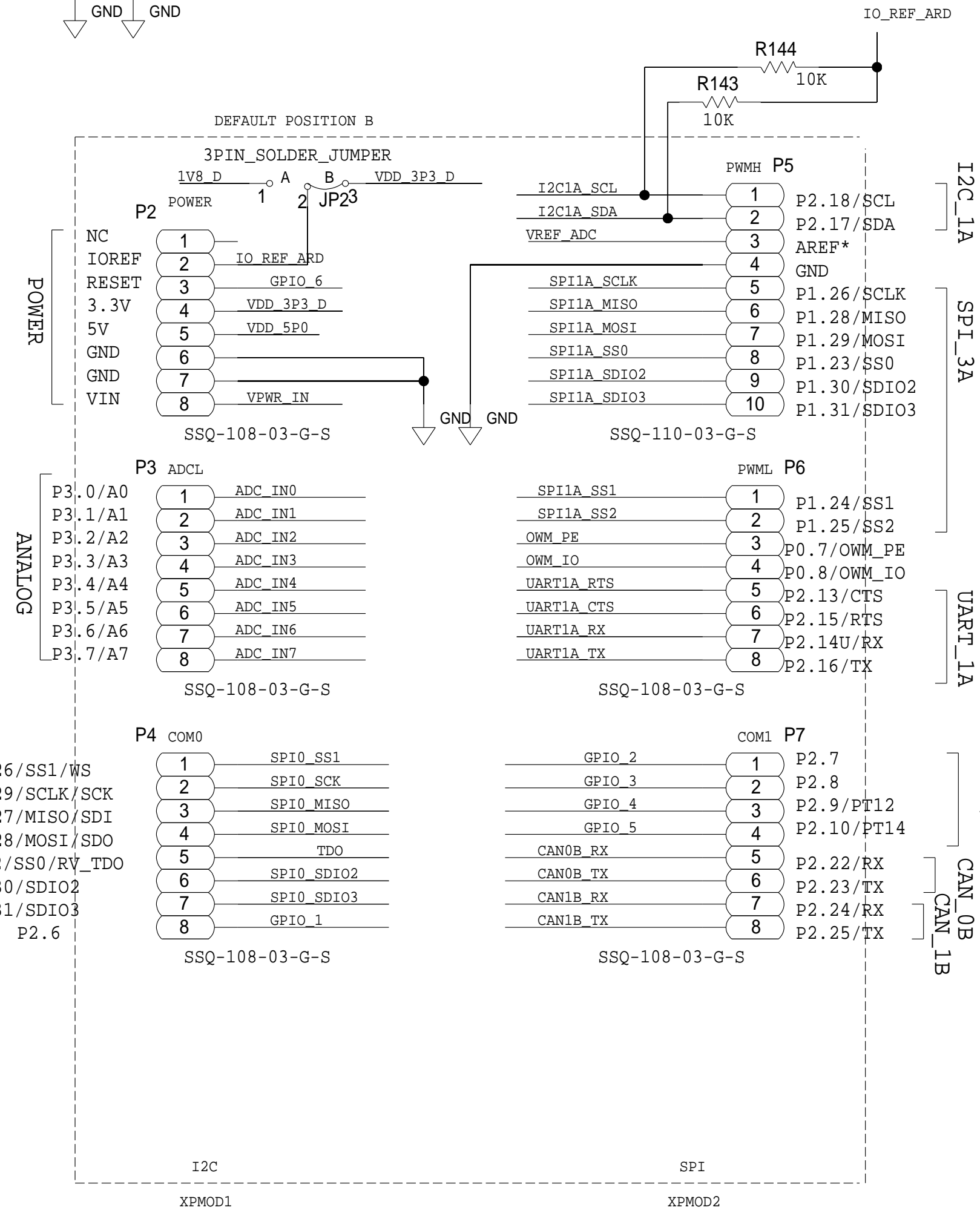
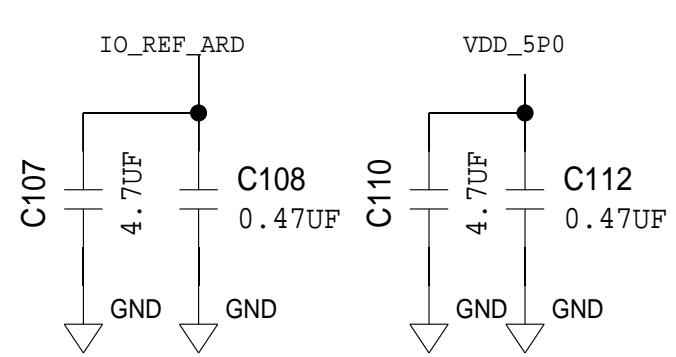
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



<div><div>ANALOG DEVICES</div></div>		<div>SCHEMATIC</div> <div>HW TYPE : Eval_MAX32690_ARDZ Product(s) : MAX32690                   : MAX32690</div>							
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<div>THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OR OTHER INTELLECTUAL PROPERTY RIGHTS. ANALOG DEVICES ASSUMES NO LIABILITY FOR INFRINGEMENT OF SUCH RIGHTS.</div>		<div>PTD ENGINEER M. RANCIC</div>		<div>SIZE C</div>		<div>SCALE 1:1</div>		<div>SHEET 3 OF 7</div>	

# CONNECTORS AND WIFI

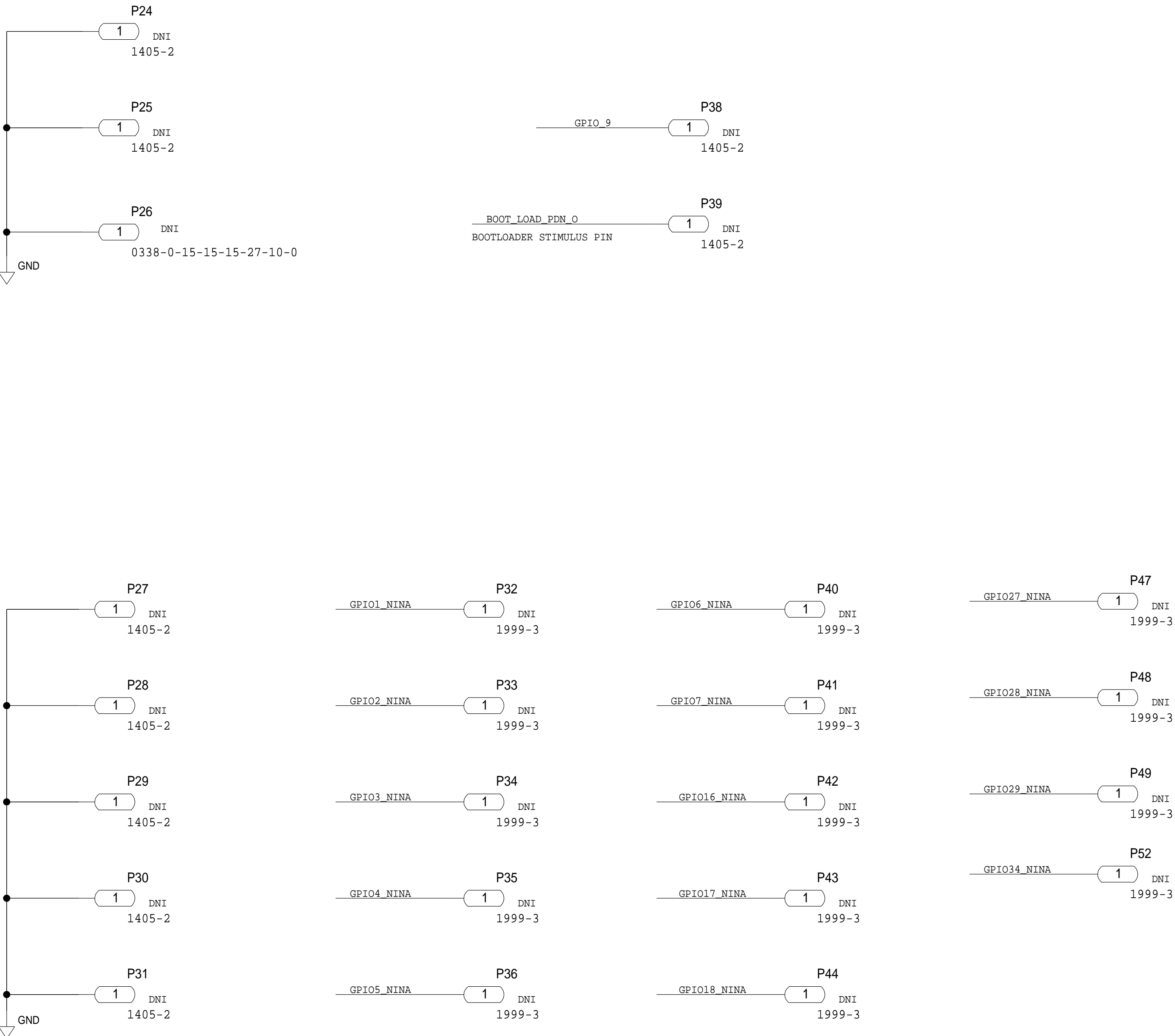
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED




SCHEMATIC			
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DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073637	REV A	
PTD ENGINEER M. BANCISOR	SIZE D	SCALE 1:1	SHEET 4 OF 7

## TH TESTPOINTS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



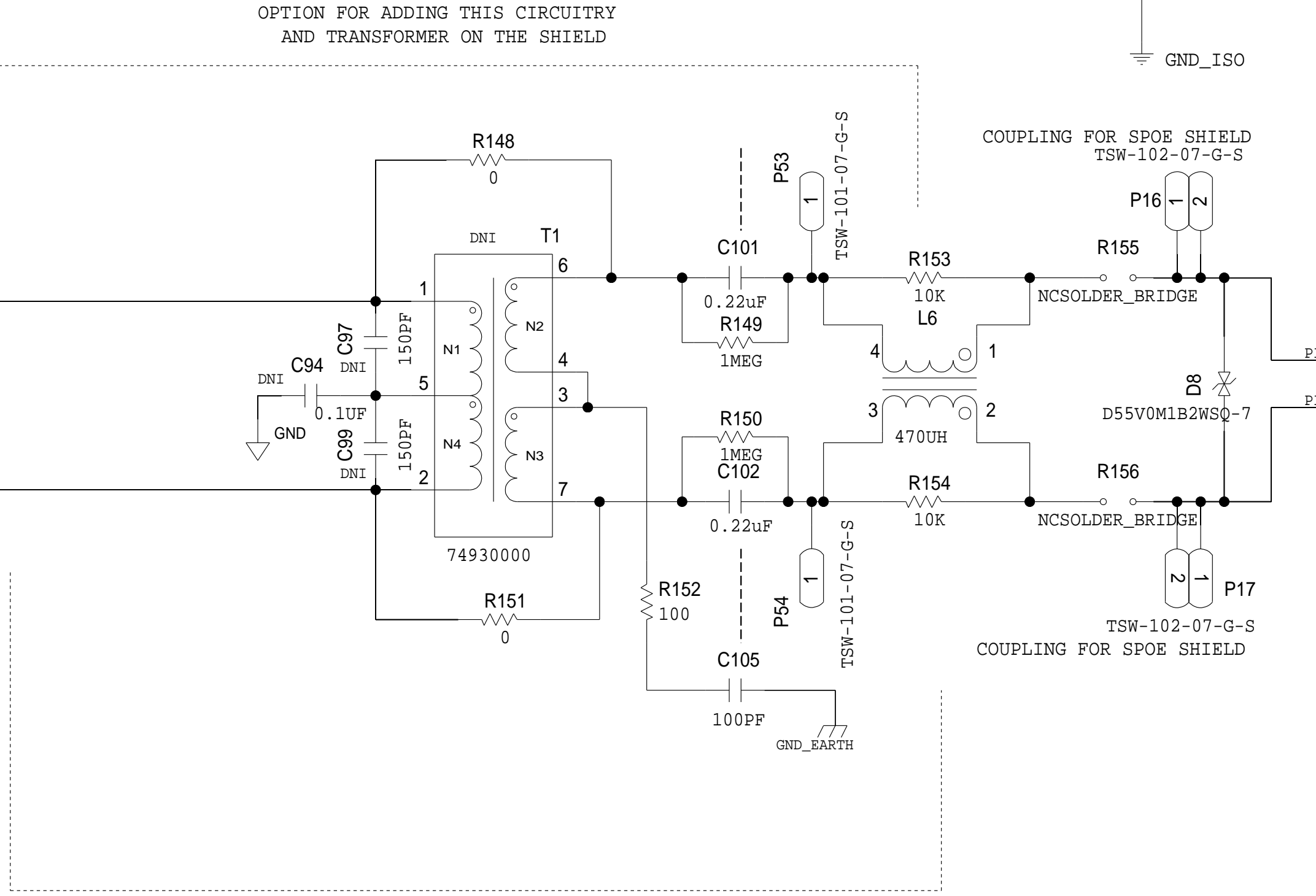
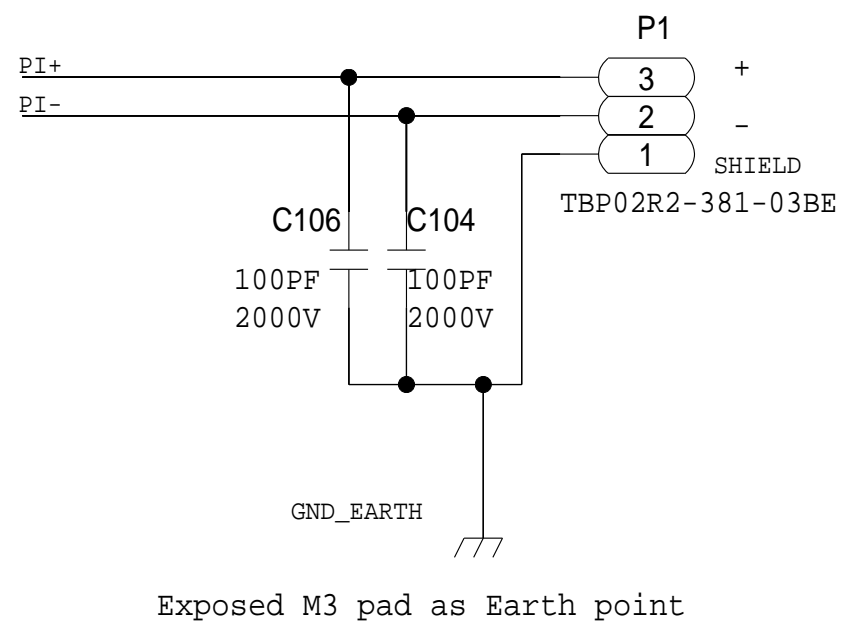
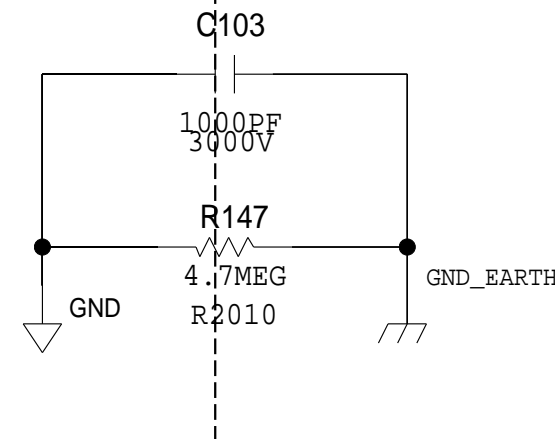
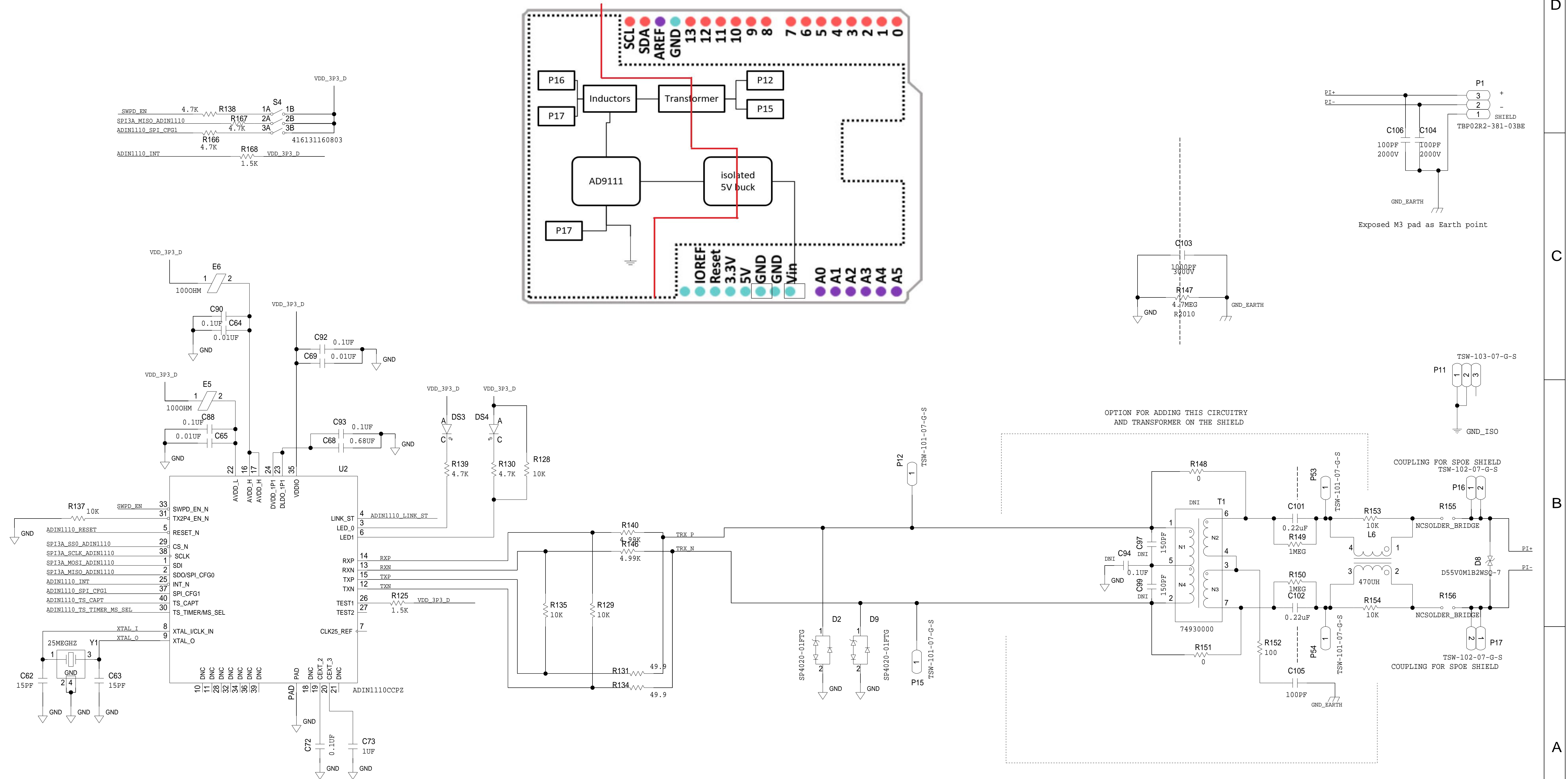
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<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE PERTAINING TO THE INTERESTS OF ANALOG DEVICES.</small>	<b>DESIGN VIEW</b> <b>&lt;DESIGN_VIEW&gt;</b>		<b>DRAWING NO.</b> <b>02_073637</b>		<b>REV</b> <b>A</b>
<small>PTD ENGINEER</small> <b>M. BANCISOR</b> <small>THIS EQUIPMENT SCHEMATIC DRAWING MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ORNED ANALOG DEVICES</small>	<b>SIZE</b> <b>D</b>	<b>SCALE</b> <b>1:1</b>	<b>SHEET 5 OF 7</b>		





ADIN1110 10BASE-T1L MACPHY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Eval_MAX32690_ARDZ Product (s) : MAX32690 : MAX32690			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073637	REV A	
PTD ENGINEER M. BANCISOR	SIZE D	SCALE 1:1	SHEET 7 OF 7