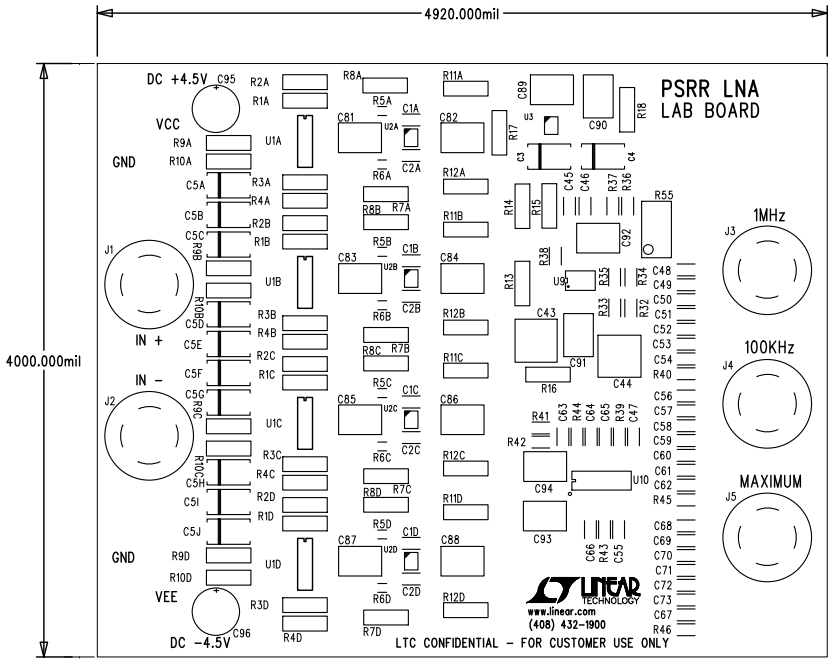
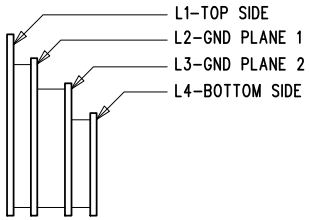


TOP SILKSCREEN
LINEAR TECHNOLOGY DATE: 11–20–14
PSRR LAB BOARD

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	1	1ST PROTOTYPE		11–20–14

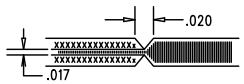
PRODUCTION

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC–A–600.
2. MATERIAL: –EPOXY FIBERGLASS, NEMA GRADE FR–4
   –FINISHED THICKNESS TO BE 0.062" +/– .005"
   –TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
   –FLAMMABILITY RATING: 94 V–O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
4. DRILLING: –DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   –ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   –HOLE LOCATION TOLERANCES ARE +/–0.003" IN RELATION TO CENTER
   –FOR VIAS SIZE > 0.01", VIAS HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
5. FINISH: –SMOBC USING LPI BOTH SIDES, COLOR BLACK.
   –GOLD IMMERSION BOTH SIDES.
   (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
   –FOR SILKSCREEN: BOTH SIDES USE WHITE NON–CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL PER ASME Y14.5M–1994
THIRD ANGLE PROJECTION

APPROVALS	
PCB DES.	
APP ENG.	

SCALE = NONE

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TITLE: FABRICATION DRAWING
PSRR LNA

SIZE	IC NO.	REV
N/A	LAB BOARD	1

FILENAME: SHT 1 OF 1