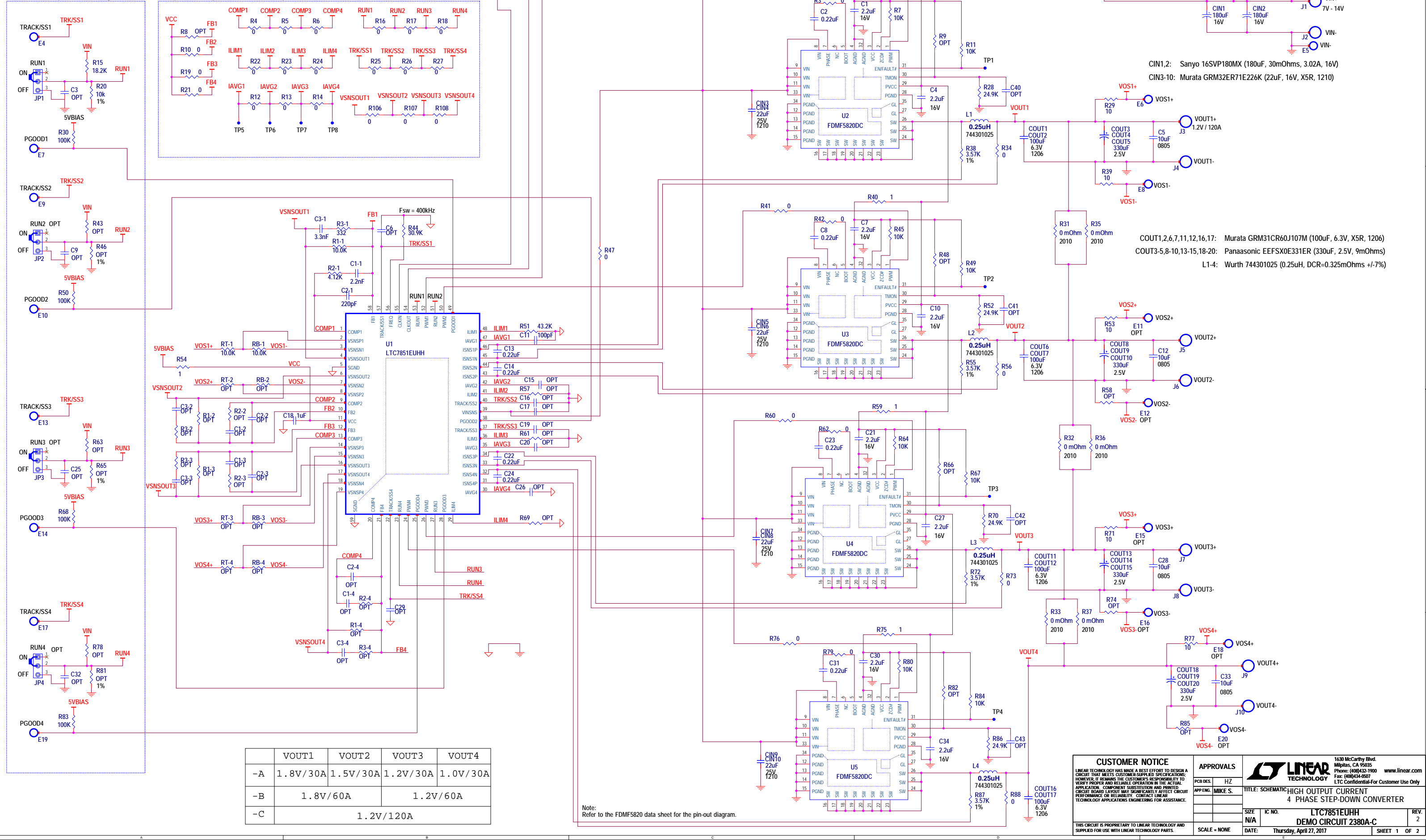


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION	Mike S.	10-21-15

Place these components on edge of board

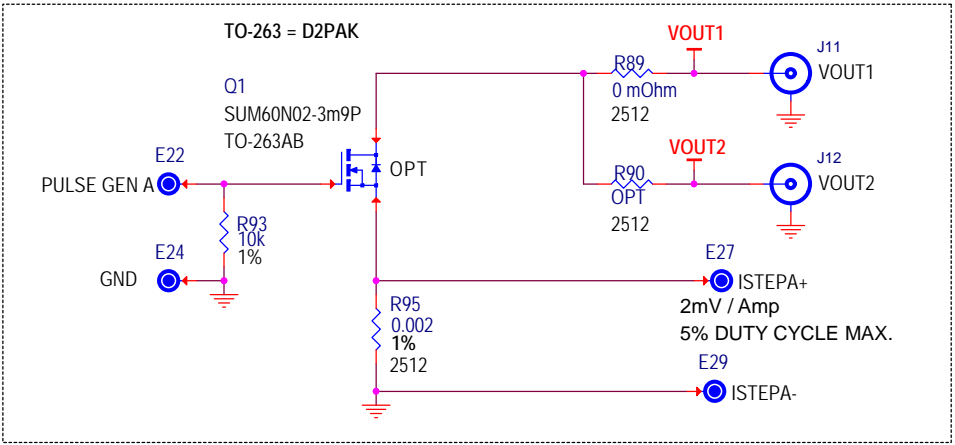
Components to parallel two or more phases

Place on edge of board

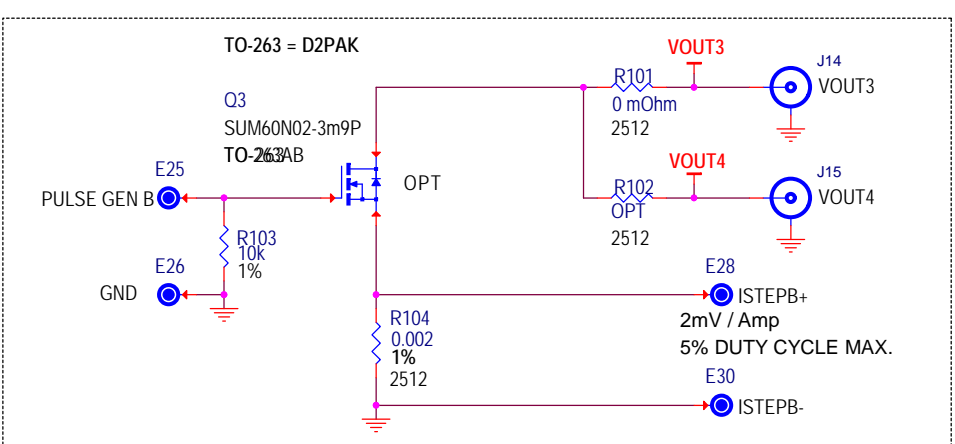


Note:
Refer to the FDMF5820 data sheet for the pin-out diagram.

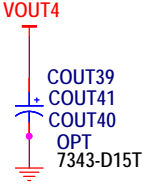
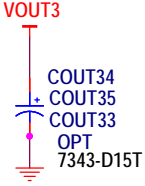
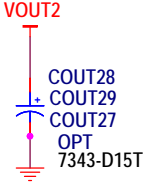
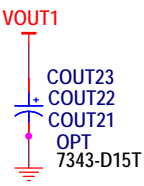
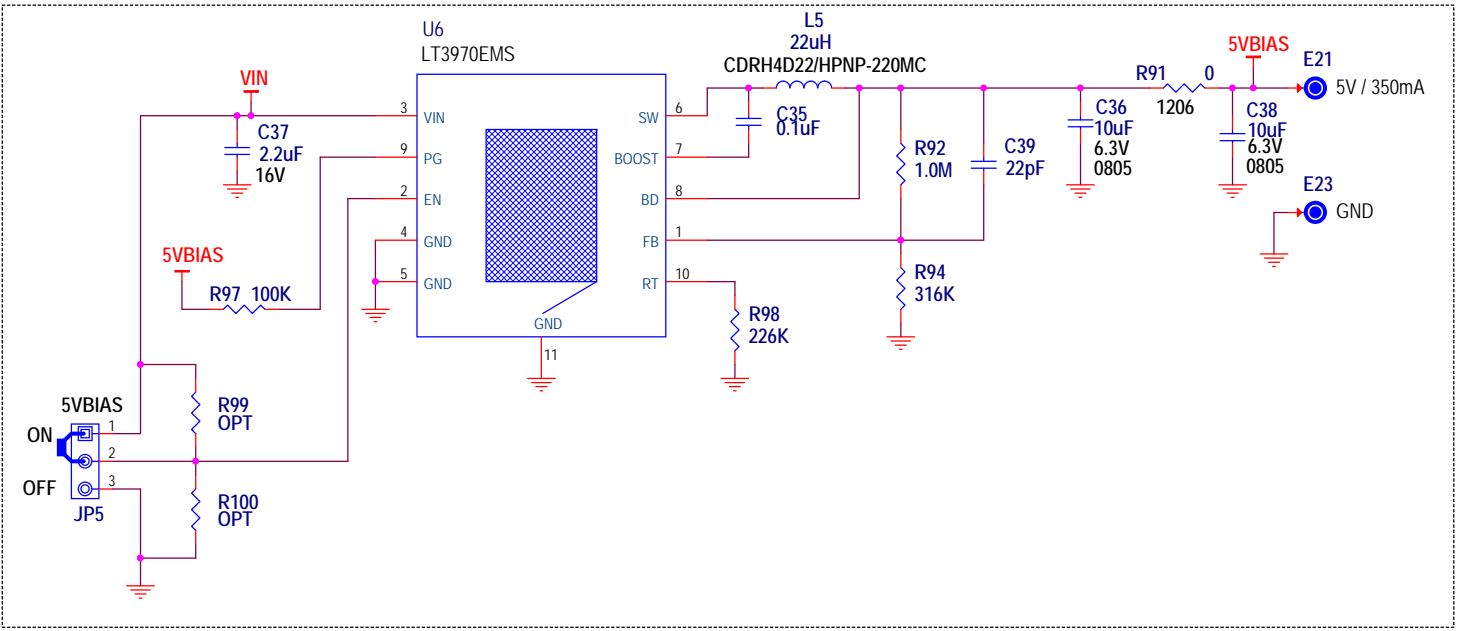
DYNAMIC LOAD CIRCUIT FOR VOUT1 AND VOUT2



DYNAMIC LOAD CIRCUIT FOR VOUT3 AND VOUT4



BIAS SUPPLY FOR DrMOS and LTC7851



CUSTOMER NOTICE		APPROVALS		LINEAR TECHNOLOGY	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	HZ	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
		APP ENG.	MIKE S.		
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		TITLE: SCHEMATIC HIGH OUTPUT CURRENT 4 PHASE STEP-DOWN CONVERTER	
				SIZE N/A	REV. 2
		DATE: Tuesday, April 04, 2017		SHEET 2 OF 2	