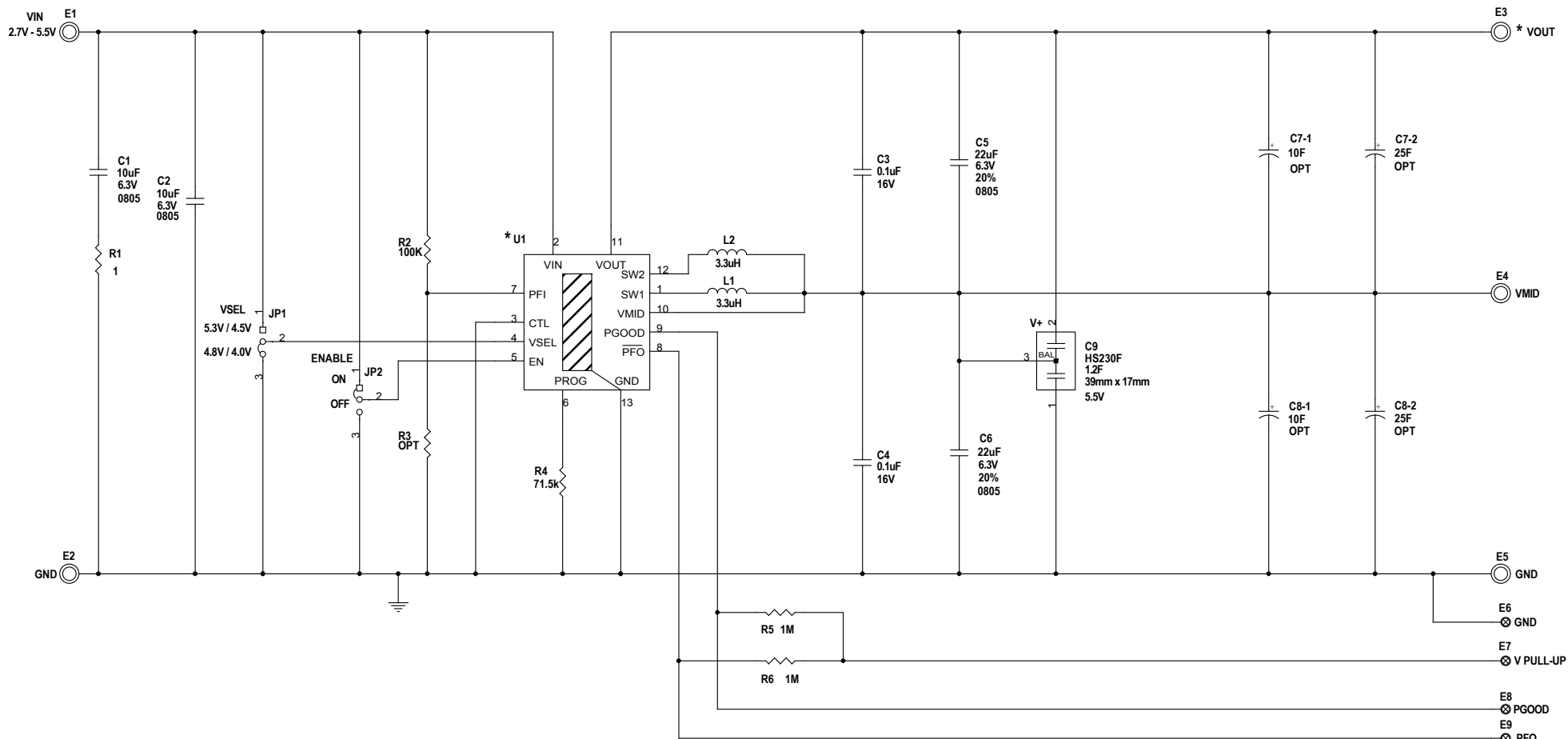


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION FAB	J. DREW	06-15-10



*

ASSY TYPE	U1	VOUT
- A	LTC3625EDE	5.3V - 4.8V
- B	LTC3625EDE-1	4.5V - 4.0V

Unless noted:
Resistors: Ohms, 0402, 1%, 1/16W
Capacitors: uF, 0402, 6.3V

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	NC
APP ENG.	J. DREW

SCALE = NONE



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TITLE: SCHEMATIC
1A HIGH EFFICIENCY 2-CELL SUPERCAPACITOR CHARGER WITH
AUTOMATIC CELL BALANCING

SIZE N/A IC NO. LTC3625EDE / LTC3625EDE-1
DEMO CIRCUIT 1583A-A/B

DATE: Monday, August 23, 2010 SHEET 1 OF 1