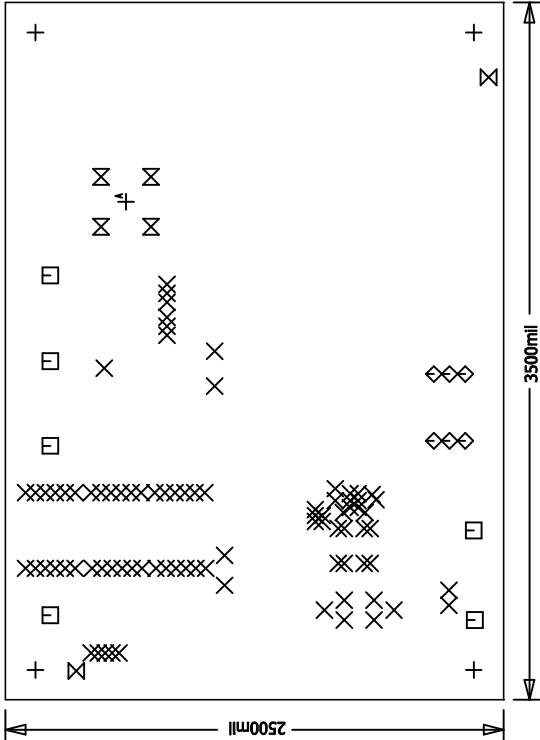


REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
-	1	PRODUCTION	JIM N.
			DATE
			01-21-10

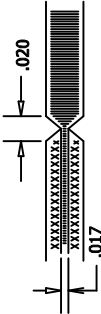
Demo Circuit 1451A  
Linear Tech. Corp.

Component Side



**NOTES: UNLESS OTHERWISE SPECIFIED**

1. FAB PER IPC-A-600
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE  
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM .  
0.00" ARE PRIMARY DATUMS.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPT BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3Mils
8	85	X	YES	+/-3Mils
94	6	□	YES	+/-3Mils
31	6	◇	YES	+/-3Mils
70	2	⊗	NO	+/-3Mils
52	4	⊗	YES	+/-3Mils
59	1	⊕	YES	+/-3Mils

<b>UNLESS OTHERWISE SPECIFIED</b> DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION			
<b>APPROVALS</b>			
PCB DES.	RSB		
APP ENG.	JIM N.		
SCALE = NONE			
<b>LINEAR TECHNOLOGY</b> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL FOR CUSTOMER USE ONLY			
TITLE: FABRICATION DRAWING HIGH EFFICIENCY BUCK-BOOST DC/DC CONVERTER WITH ADJUSTABLE INPUT CURRENT FOR PULSE LOADS			
SIZE	IC NO.	REV	
N/A	LTC3127EDD	1	
FILENAME: DC1451A-1.PCB		SHT 1 OF 1	