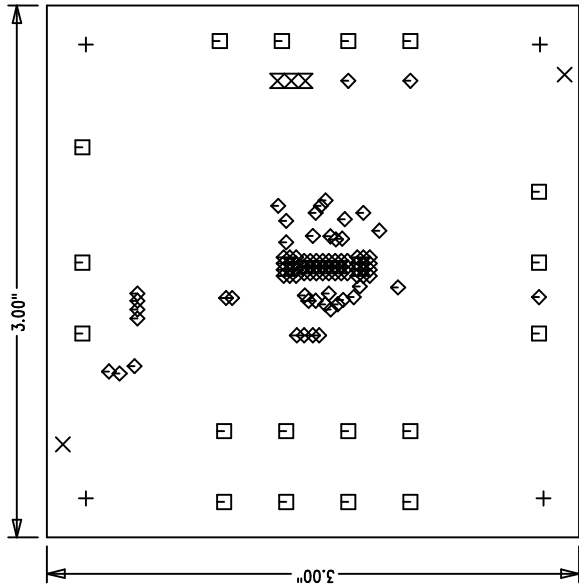
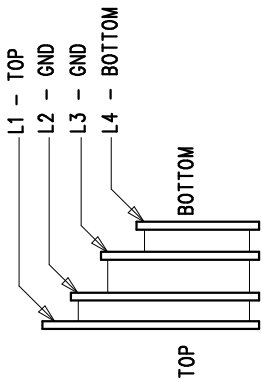


REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
1	1	REBUILD WITH CHANGES	WALKER B.
			DATE
			4-6-10



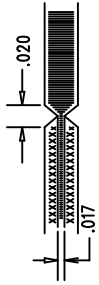
Linear Tech. Corp.
Demo Circuit 1327B

SIZE	QTY	SYM	PLATED	TOL
0.07	2	X	NO	+/-0.003
0.095	18	□	YES	+/-0.003
0.01	88	◇	YES	+/-0.003
0.035	3	⊠	YES	+/-0.003
0.187	4	+	YES	+/-0.003



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	HZ
APP ENG.	WALKER B.
SIZE	IC NO.
N/A	LT3599EFE
SCALE = NONE	REV
	1

LINEAR TECHNOLOGY

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LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
4 - CHANNEL 120mA LED DRIVER
WITH +/- 1.5% CURRENT MATCHING

FILENAME: DC1327B-1.PCB

SHT 1 OF 1