

LOW DOSE RADIATION TEST REPORT ADUM3190S

April 2018
Generic



Radiation Test Report	
Product:	ADuM3190S
Gamma:	0, 10k, 30k
Gamma Source:	Co60/TM1019 Condition D
Dose Rate:	7.9mRad/s
Facilities:	VPT RAD
Tested:	3/7/2018 – 4/30/2018

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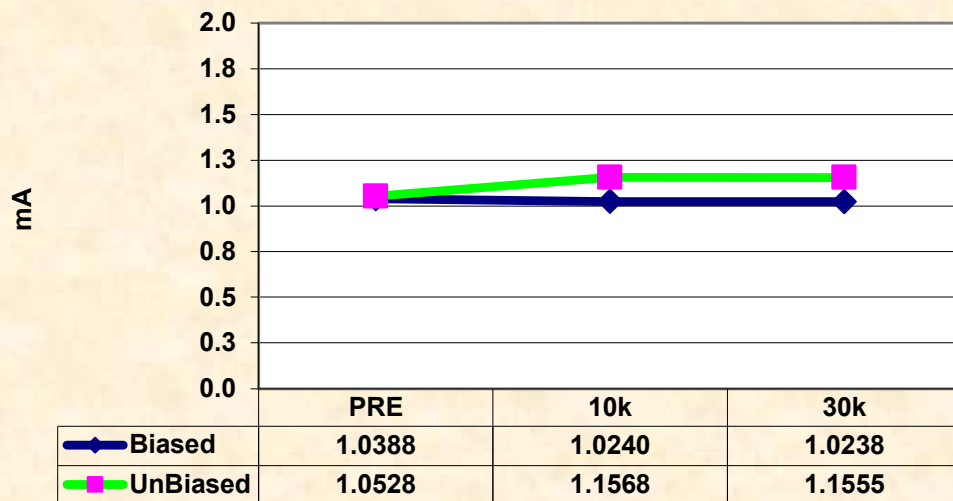
It is the responsibility of the Procuring Activity to screen products from Analog Devices, Inc. for compliance to Nuclear Hardness Critical Items (HCI) specifications.

Warning:

Analog Devices, Inc. does not recommend use of this data to qualify other product grades or process levels. Analog Devices, Inc. is not responsible and has no liability for any consequences, and all applicable Warranties are null and void if any Analog Devices product is modified in any way or used outside of normal environmental and operating conditions, including the parameters specified in the corresponding data sheet. Analog Devices, Inc. does not guarantee that wafer manufacturing is the same for all process levels.

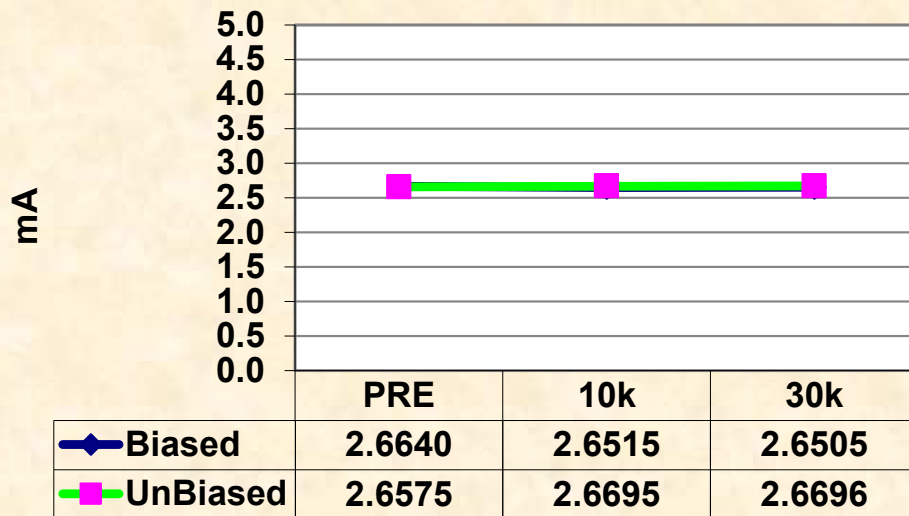
		IDD1 @ 3V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	1.1120	1.0270	1.0249
Biased	104	1.0120	1.0140	1.0149
	105	1.0340	0.9830	0.9825
	112	1.0450	1.0400	1.0400
	114	1.0640	1.0590	1.0578
	Min	1.0120	0.9830	0.9825
	Max	1.0640	1.0590	1.0578
	Mean	1.0388	1.0240	1.0238
UnBiased	106	0.9590	1.2420	1.2404
	107	0.9620	1.1060	1.1052
	120	1.2110	1.2050	1.2032
	122	1.0790	1.0740	1.0730
	Min	0.9590	1.0740	1.0730
	Max	1.2110	1.2420	1.2404
	Mean	1.0528	1.1568	1.1555

IDD1 @ 3V

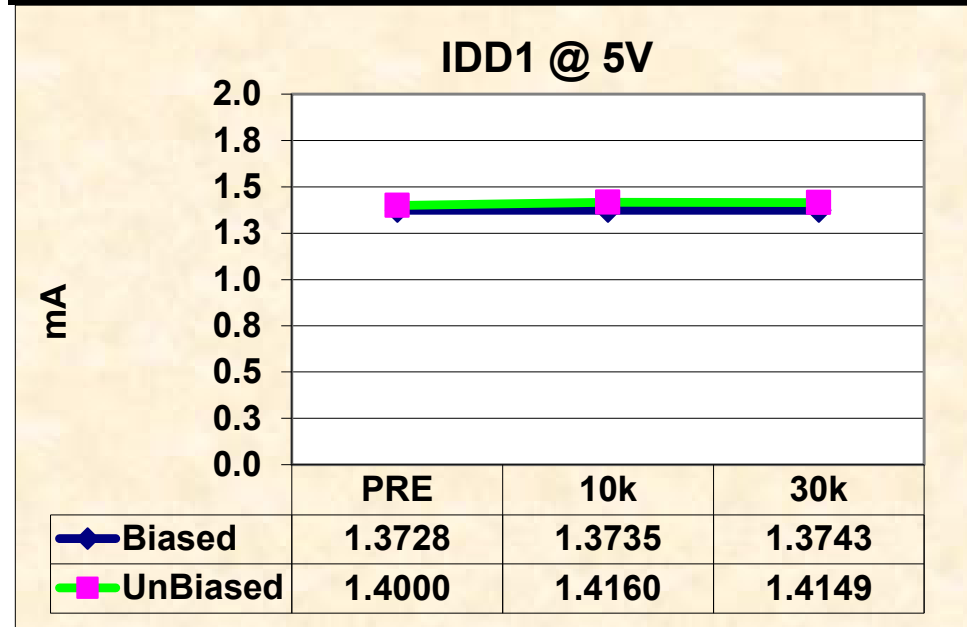


		IDD2 @ 3V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.6900	2.6710	2.6679
Biased	104	2.7030	2.6650	2.6616
	105	2.6840	2.6920	2.6936
	112	2.6560	2.6470	2.6445
	114	2.6130	2.6020	2.6021
	Min	2.6130	2.6020	2.6021
	Max	2.7030	2.6920	2.6936
	Mean	2.6640	2.6515	2.6505
UnBiased	106	2.6030	2.6850	2.6893
	107	2.6900	2.6780	2.6768
	120	2.6670	2.6560	2.6542
	122	2.6700	2.6590	2.6579
	Min	2.6030	2.6560	2.6542
	Max	2.6900	2.6850	2.6893
	Mean	2.6575	2.6695	2.6696

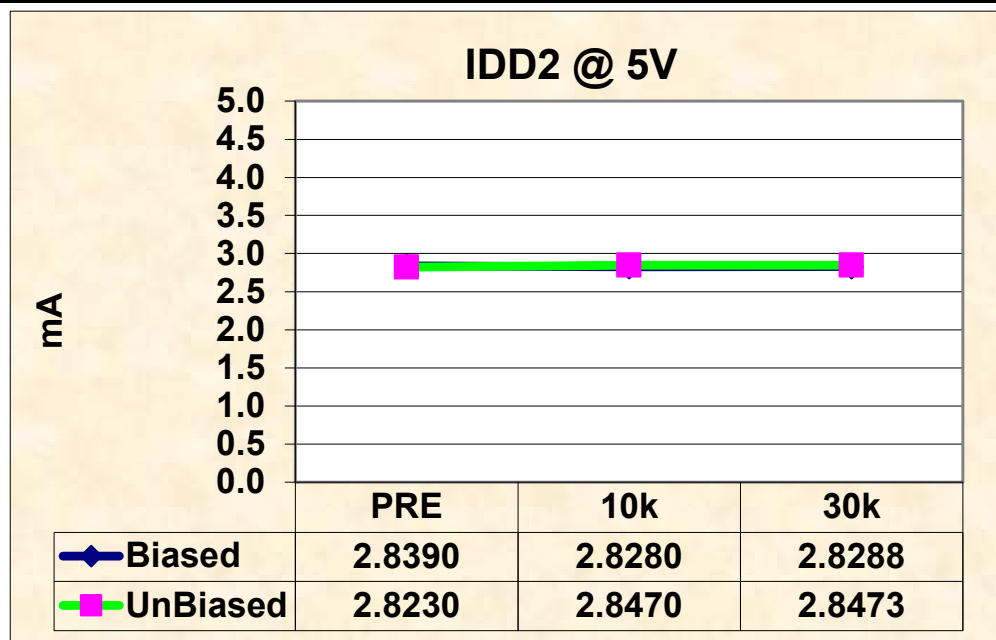
IDD2 @ 3V



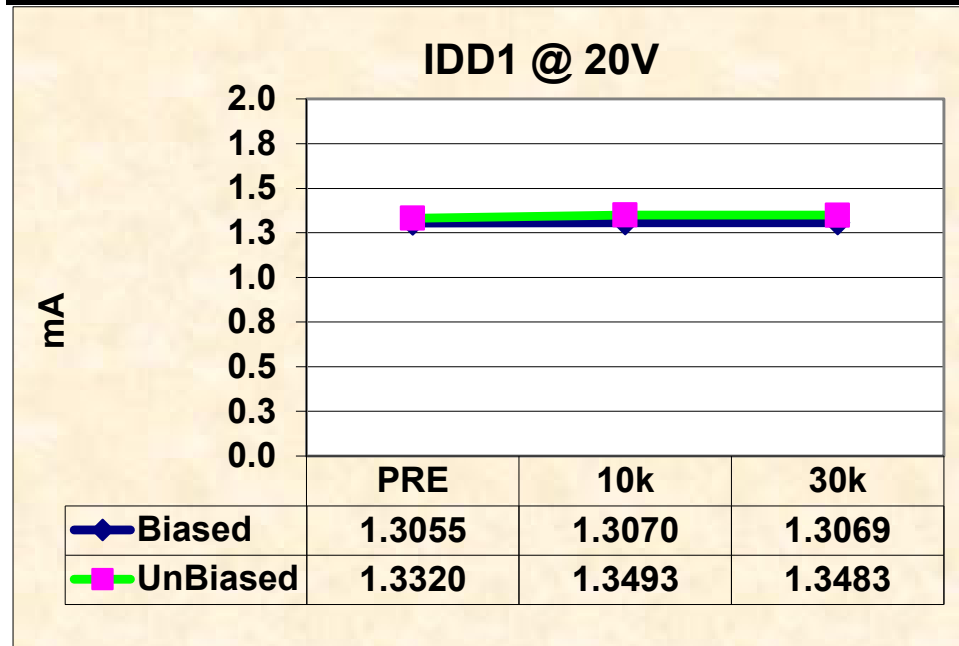
		IDD1 @ 5V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	1.4550	1.3980	1.3964
Biased	104	1.3690	1.4270	1.4257
	105	1.4080	1.3650	1.3681
	112	1.3420	1.3360	1.3379
	114	1.3720	1.3660	1.3653
	Min	1.3420	1.3360	1.3379
	Max	1.4080	1.4270	1.4257
	Mean	1.3728	1.3735	1.3743
UnBiased	106	1.3810	1.4010	1.3999
	107	1.3900	1.4490	1.4489
	120	1.4240	1.4170	1.4152
	122	1.4050	1.3970	1.3956
	Min	1.3810	1.3970	1.3956
	Max	1.4240	1.4490	1.4489
	Mean	1.4000	1.4160	1.4149



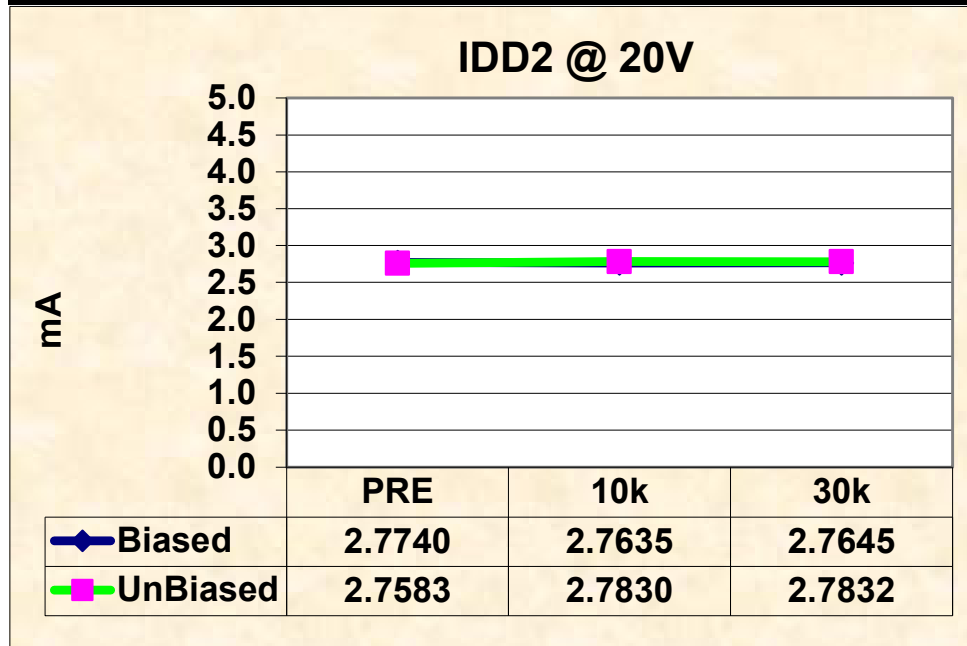
		IDD2 @ 5V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.8820	2.8390	2.8372
Biased	104	2.8880	2.8440	2.8424
	105	2.8530	2.8700	2.8747
	112	2.8230	2.8160	2.8148
	114	2.7920	2.7820	2.7833
	Min	2.7920	2.7820	2.7833
	Max	2.8880	2.8700	2.8747
	Mean	2.8390	2.8280	2.8288
UnBiased	106	2.7680	2.8690	2.8752
	107	2.8540	2.8690	2.8682
	120	2.8390	2.8300	2.8272
	122	2.8310	2.8200	2.8186
	Min	2.7680	2.8200	2.8186
	Max	2.8540	2.8690	2.8752
	Mean	2.8230	2.8470	2.8473



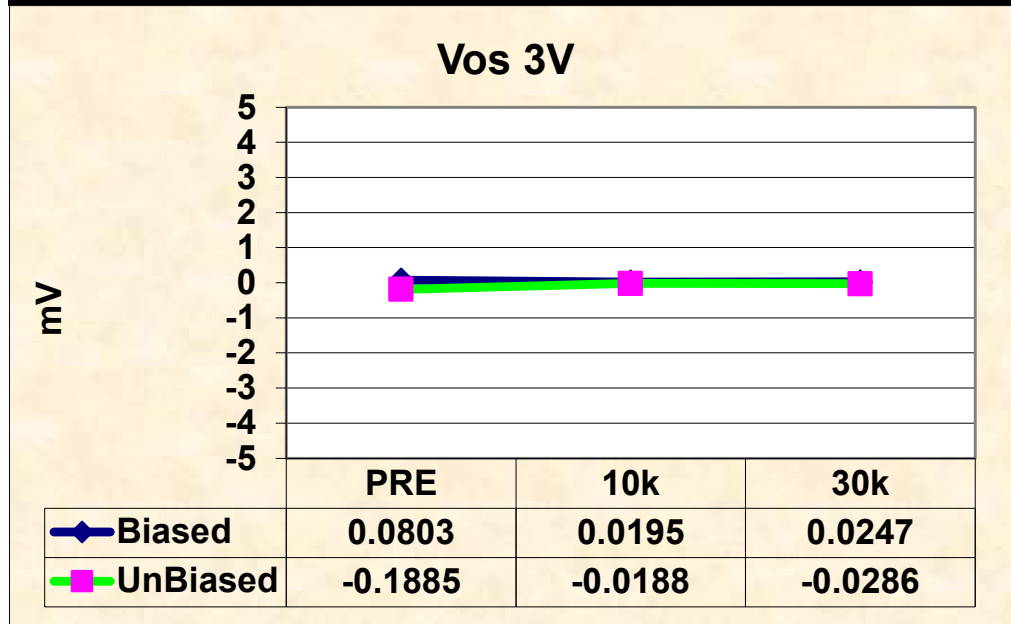
		IDD1 @ 20V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	1.3890	1.3300	1.3276
Biased	104	1.3030	1.3610	1.3593
	105	1.3400	1.2980	1.3001
	112	1.2730	1.2690	1.2694
	114	1.3060	1.3000	1.2989
	Min	1.2730	1.2690	1.2694
	Max	1.3400	1.3610	1.3593
	Mean	1.3055	1.3070	1.3069
UnBiased	106	1.3110	1.3340	1.3328
	107	1.3220	1.3820	1.3826
	120	1.3570	1.3500	1.3477
	122	1.3380	1.3310	1.3300
	Min	1.3110	1.3310	1.3300
	Max	1.3570	1.3820	1.3826
	Mean	1.3320	1.3493	1.3483



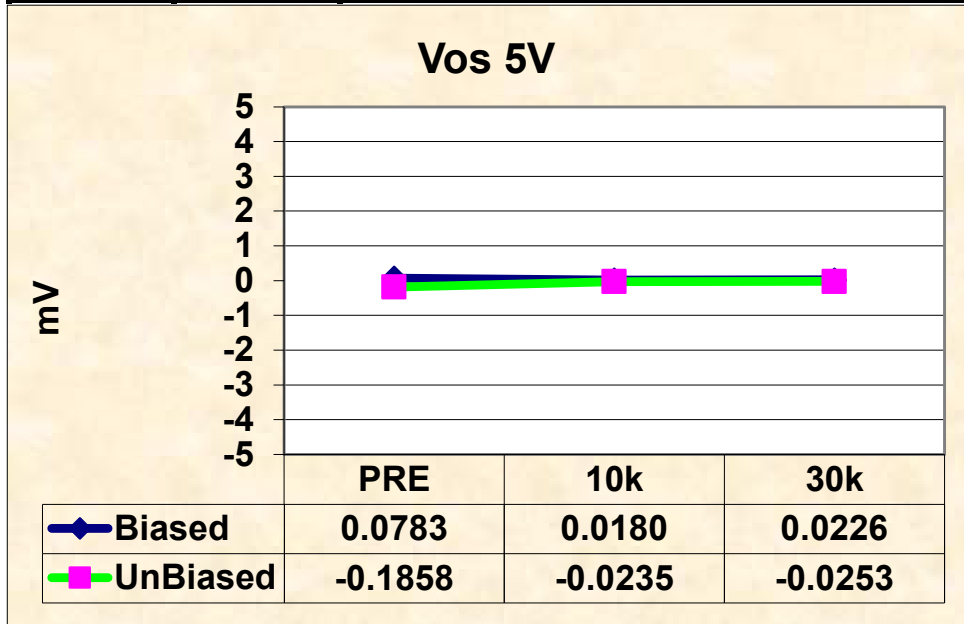
		IDD2 @ 20V (mA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.8170	2.7740	2.7713
Biased	104	2.8230	2.7780	2.7773
	105	2.7890	2.8070	2.8111
	112	2.7560	2.7500	2.7484
	114	2.7280	2.7190	2.7212
	Min	2.7280	2.7190	2.7212
	Max	2.8230	2.8070	2.8111
	Mean	2.7740	2.7635	2.7645
UnBiased	106	2.7020	2.8040	2.8114
	107	2.7890	2.8050	2.8037
	120	2.7750	2.7660	2.7631
	122	2.7670	2.7570	2.7546
	Min	2.7020	2.7570	2.7546
	Max	2.7890	2.8050	2.8114
	Mean	2.7583	2.7830	2.7832



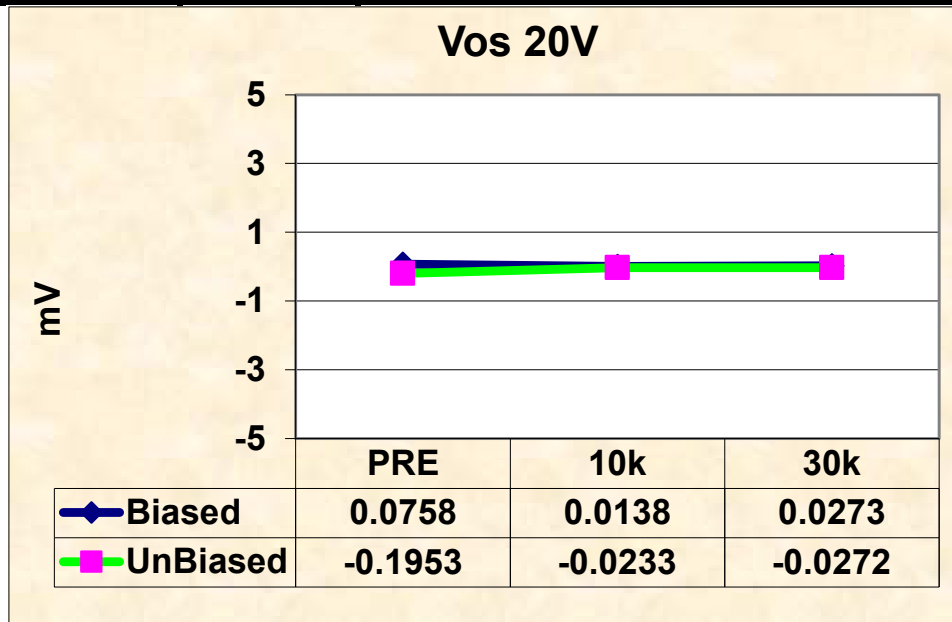
		Vos_3V (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-0.0510	0.1000	0.0872
Biased	104	0.1250	0.0810	0.1088
	105	0.0870	-0.0840	-0.1134
	112	-0.0860	-0.0890	-0.0887
	114	0.1950	0.1700	0.1919
	Min	-0.0860	-0.0890	-0.1134
	Max	0.1950	0.1700	0.1919
	Mean	0.0803	0.0195	0.0247
UnBiased	106	-0.0090	0.1330	0.1359
	107	-0.5700	-0.0380	-0.0306
	120	0.0420	0.0260	0.0076
	122	-0.2170	-0.1960	-0.2273
	Min	-0.5700	-0.1960	-0.2273
	Max	0.0420	0.1330	0.1359
	Mean	-0.1885	-0.0188	-0.0286



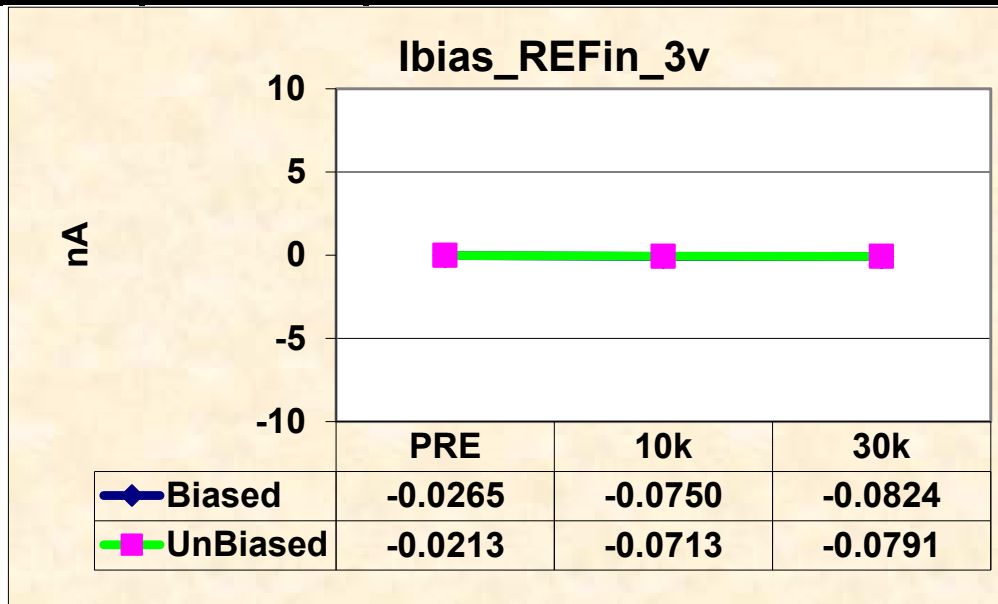
		Vos_5V (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-0.0470	0.1030	0.0866
Biased	104	0.1210	0.0860	0.0970
	105	0.0860	-0.1050	-0.1162
	112	-0.0930	-0.0820	-0.0698
	114	0.1990	0.1730	0.1795
	Min	-0.0930	-0.1050	-0.1162
	Max	0.1990	0.1730	0.1795
	Mean	0.0783	0.0180	0.0226
UnBiased	106	-0.0110	0.1400	0.1479
	107	-0.5760	-0.0480	-0.0365
	120	0.0520	0.0200	0.0089
	122	-0.2080	-0.2060	-0.2215
	Min	-0.5760	-0.2060	-0.2215
	Max	0.0520	0.1400	0.1479
	Mean	-0.1858	-0.0235	-0.0253



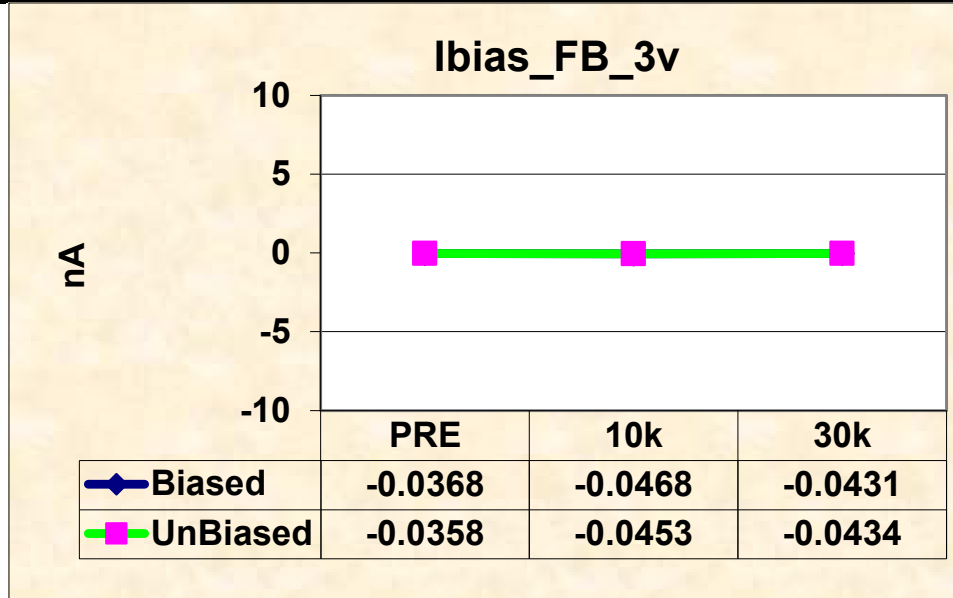
Wafer #	SN	Vos_20V (mV)		
		PRE	10k	30k
CTRL	99	-0.0450	0.0870	0.0809
Biased	104	0.1120	0.0790	0.1092
	105	0.0800	-0.1110	-0.1110
	112	-0.0920	-0.0830	-0.0784
	114	0.2030	0.1700	0.1895
	Min	-0.0920	-0.1110	-0.1110
	Max	0.2030	0.1700	0.1895
	Mean	0.0758	0.0138	0.0273
UnBiased	106	-0.0100	0.1420	0.1463
	107	-0.5840	-0.0450	-0.0431
	120	0.0380	0.0150	0.0062
	122	-0.2250	-0.2050	-0.2183
	Min	-0.5840	-0.2050	-0.2183
	Max	0.0380	0.1420	0.1463
	Mean	-0.1953	-0.0233	-0.0272



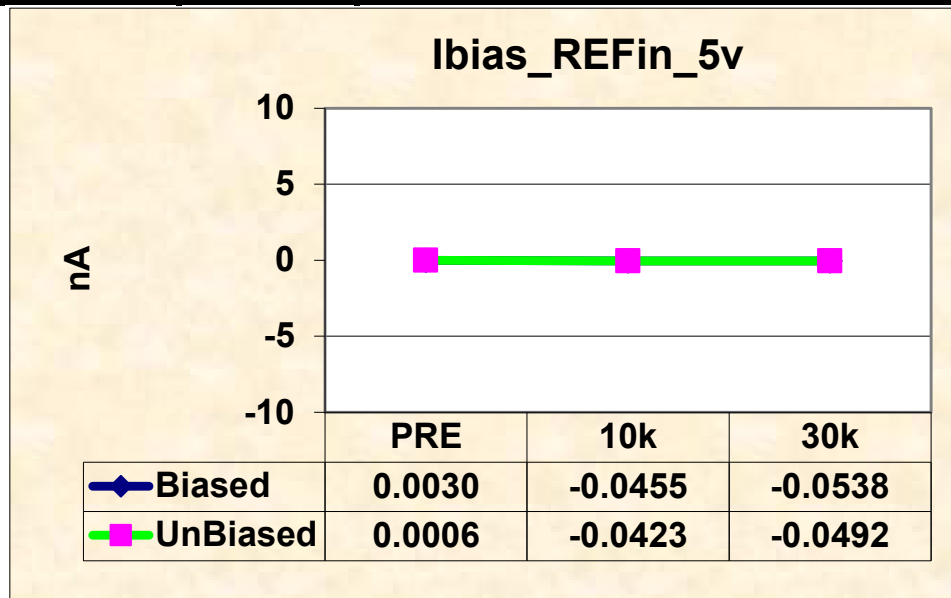
Wafer #	SN	Ibias_REFIn_3v (nA)		
		PRE	10k	30k
CTRL	99	-0.0270	-0.0670	-0.0723
Biased	104	-0.0210	-0.0680	-0.0768
	105	-0.0260	-0.0700	-0.0782
	112	-0.0350	-0.0870	-0.0925
	114	-0.0240	-0.0750	-0.0822
	Min	-0.0350	-0.0870	-0.0925
	Max	-0.0210	-0.0680	-0.0768
	Mean	-0.0265	-0.0750	-0.0824
UnBiased	106	-0.0270	-0.0680	-0.0751
	107	-0.0340	-0.0670	-0.0749
	120	-0.0240	-0.0770	-0.0861
	122	0.0000	-0.0730	-0.0801
	Min	-0.0340	-0.0770	-0.0861
	Max	0.0000	-0.0670	-0.0749
	Mean	-0.0213	-0.0713	-0.0791



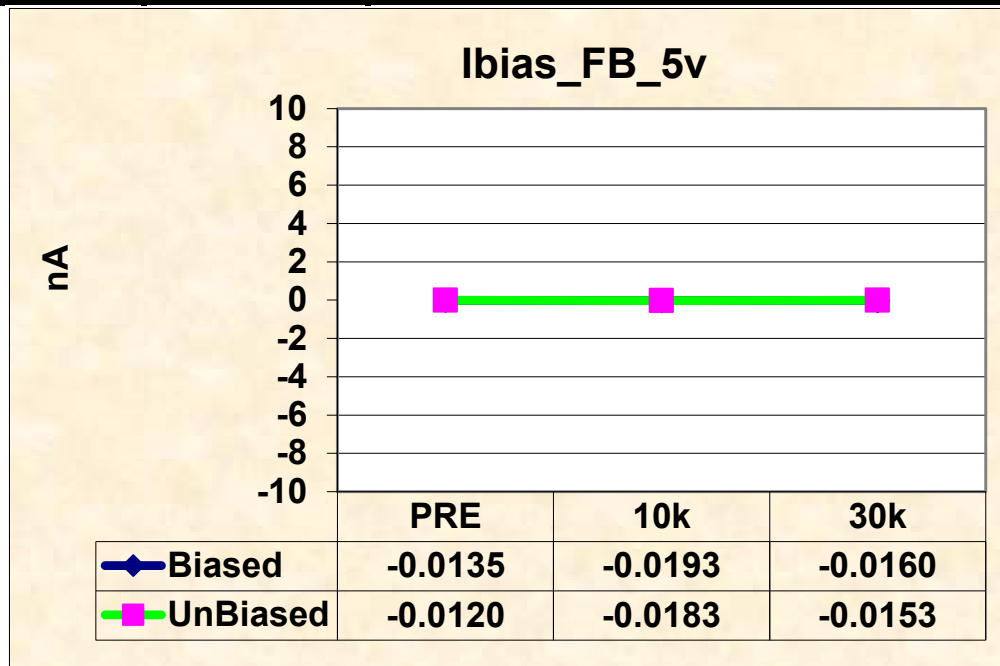
		Ibias_FB_3v (nA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-0.0380	-0.0440	-0.0391
Biased	104	-0.0350	-0.0440	-0.0418
	105	-0.0380	-0.0460	-0.0388
	112	-0.0370	-0.0490	-0.0450
	114	-0.0370	-0.0480	-0.0469
	Min	-0.0380	-0.0490	-0.0469
	Max	-0.0350	-0.0440	-0.0388
	Mean	-0.0368	-0.0468	-0.0431
UnBiased	106	-0.0330	-0.0430	-0.0395
	107	-0.0350	-0.0460	-0.0397
	120	-0.0360	-0.0460	-0.0471
	122	-0.0390	-0.0460	-0.0471
	Min	-0.0390	-0.0460	-0.0471
	Max	-0.0330	-0.0430	-0.0395
	Mean	-0.0358	-0.0453	-0.0434



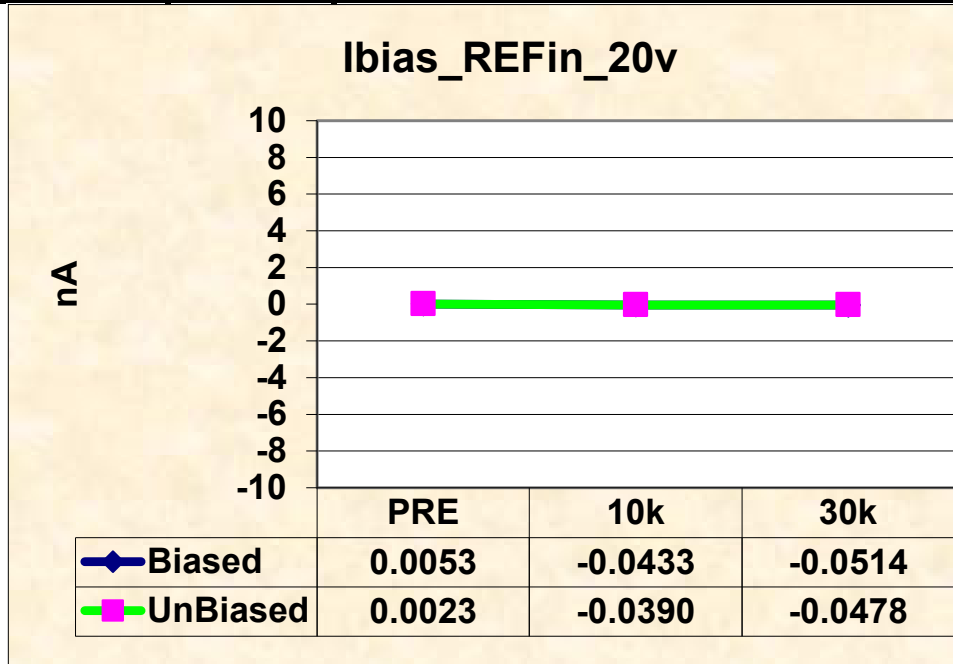
Wafer #	SN	Ibias_REFIn_5v (nA)		
		PRE	10k	30k
CTRL	99	0.0030	-0.0350	-0.0428
Biased	104	0.0070	-0.0380	-0.0475
	105	0.0050	-0.0360	-0.0467
	112	-0.0060	-0.0580	-0.0667
	114	0.0060	-0.0500	-0.0542
	Min	-0.0060	-0.0580	-0.0667
	Max	0.0070	-0.0360	-0.0467
	Mean	0.0030	-0.0455	-0.0538
UnBiased	106	0.0020	-0.0350	-0.0446
	107	-0.0010	-0.0380	-0.0435
	120	0.0010	-0.0480	-0.0562
	122	0.0004	-0.0480	-0.0523
	Min	-0.0010	-0.0480	-0.0562
	Max	0.0020	-0.0350	-0.0435
	Mean	0.0006	-0.0423	-0.0492



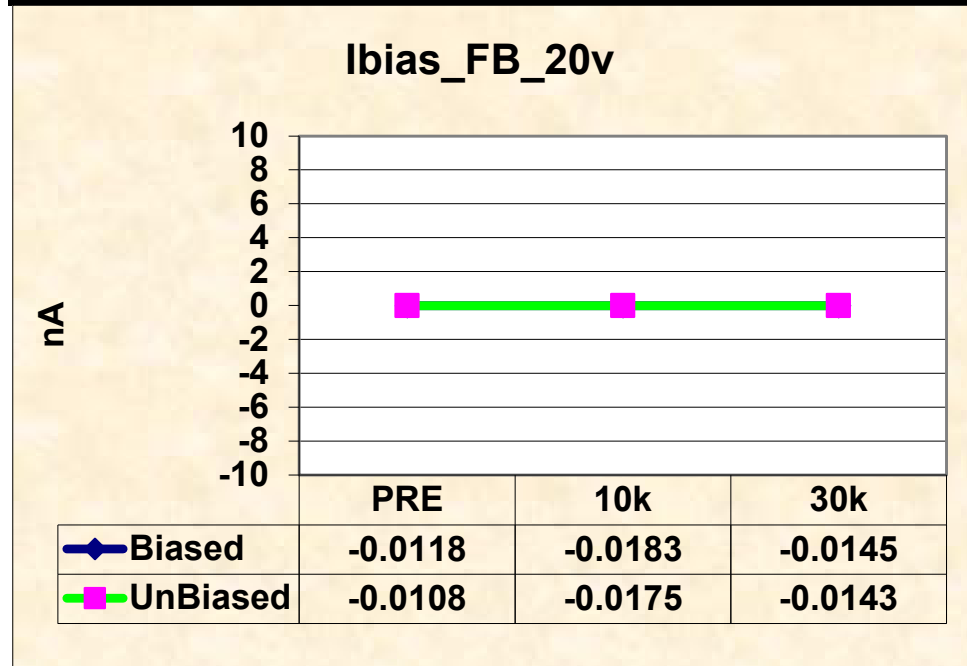
		Ibias_FB_5v (nA)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-0.0130	-0.0180	-0.0095
Biased	104	-0.0120	-0.0190	-0.0142
	105	-0.0130	-0.0170	-0.0126
	112	-0.0150	-0.0230	-0.0177
	114	-0.0140	-0.0180	-0.0194
	Min	-0.0150	-0.0230	-0.0194
	Max	-0.0120	-0.0170	-0.0126
	Mean	-0.0135	-0.0193	-0.0160
UnBiased	106	-0.0110	-0.0150	-0.0120
	107	-0.0110	-0.0200	-0.0114
	120	-0.0130	-0.0180	-0.0185
	122	-0.0130	-0.0200	-0.0191
	Min	-0.0130	-0.0200	-0.0191
	Max	-0.0110	-0.0150	-0.0114
	Mean	-0.0120	-0.0183	-0.0153



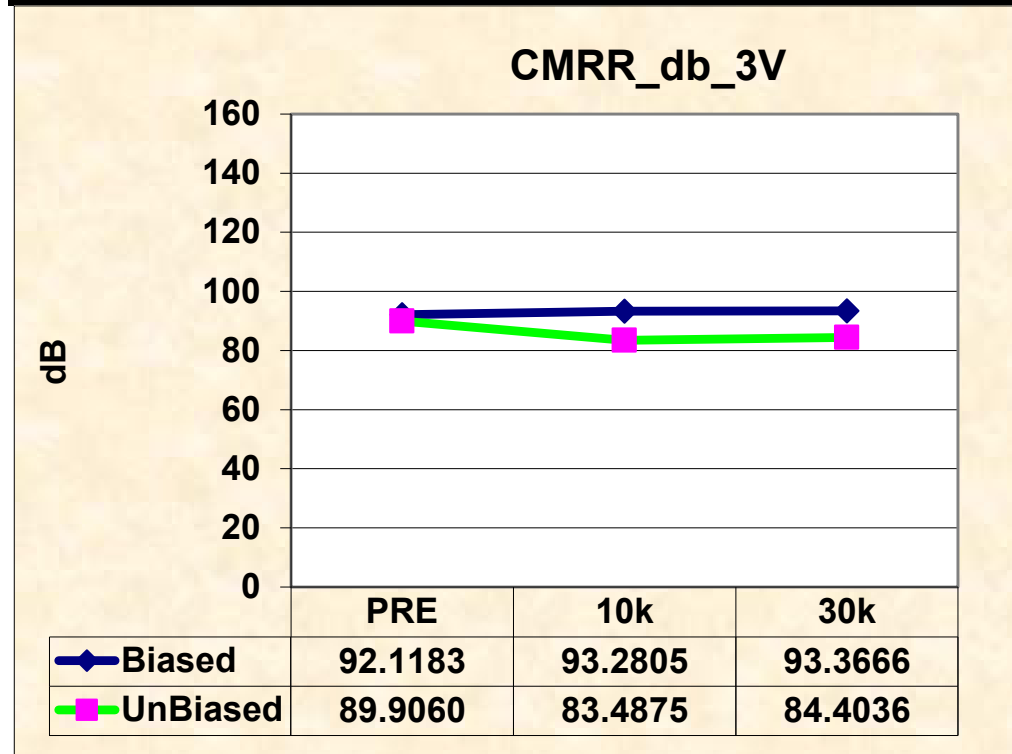
Wafer #	SN	Ibias_REFIn_20v (nA)		
		PRE	10k	30k
CTRL	99	0.0050	-0.0330	-0.0420
Biased	104	0.0100	-0.0360	-0.0444
	105	0.0070	-0.0340	-0.0451
	112	-0.0030	-0.0550	-0.0641
	114	0.0070	-0.0480	-0.0518
	Min	-0.0030	-0.0550	-0.0641
	Max	0.0100	-0.0340	-0.0444
	Mean	0.0053	-0.0433	-0.0514
UnBiased	106	0.0030	-0.0330	-0.0422
	107	0.0010	-0.0330	-0.0449
	120	0.0030	-0.0460	-0.0553
	122	0.0020	-0.0440	-0.0487
	Min	0.0010	-0.0460	-0.0553
	Max	0.0030	-0.0330	-0.0422
	Mean	0.0023	-0.0390	-0.0478



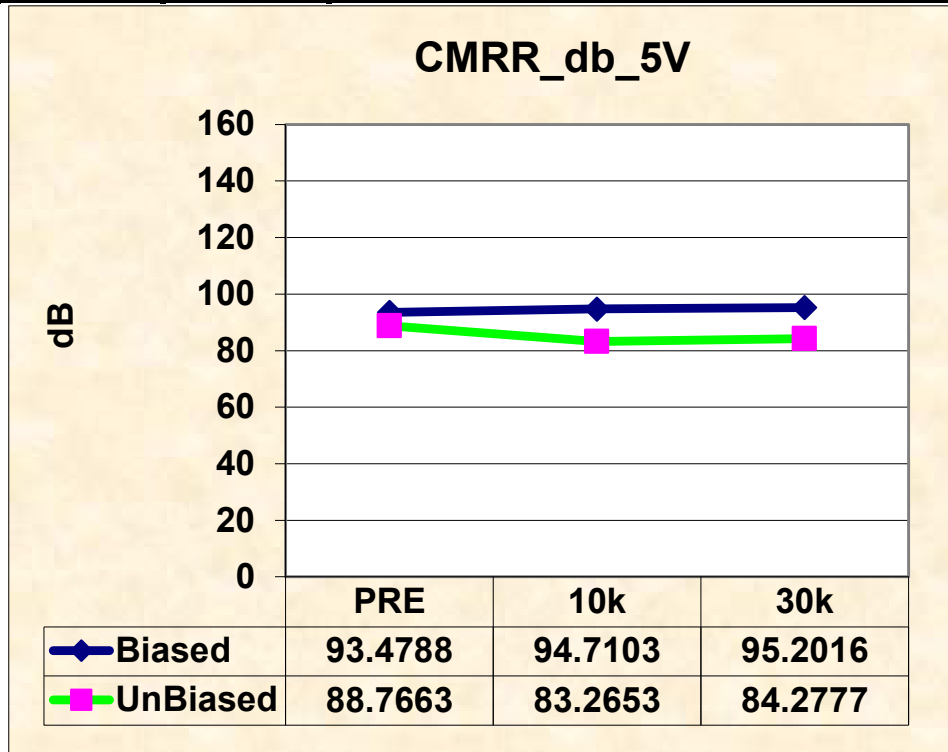
Wafer #	SN	Ibias_FB_20v (nA)		
		PRE	10k	30k
CTRL	99	-0.0120	-0.0180	-0.0079
Biased	104	-0.0100	-0.0170	-0.0123
	105	-0.0130	-0.0180	-0.0117
	112	-0.0120	-0.0200	-0.0169
	114	-0.0120	-0.0180	-0.0172
	Min	-0.0130	-0.0200	-0.0172
	Max	-0.0100	-0.0170	-0.0117
	Mean	-0.0118	-0.0183	-0.0145
UnBiased	106	-0.0080	-0.0160	-0.0115
	107	-0.0100	-0.0160	-0.0104
	120	-0.0130	-0.0170	-0.0175
	122	-0.0120	-0.0210	-0.0178
	Min	-0.0130	-0.0210	-0.0178
	Max	-0.0080	-0.0160	-0.0104
	Mean	-0.0108	-0.0175	-0.0143



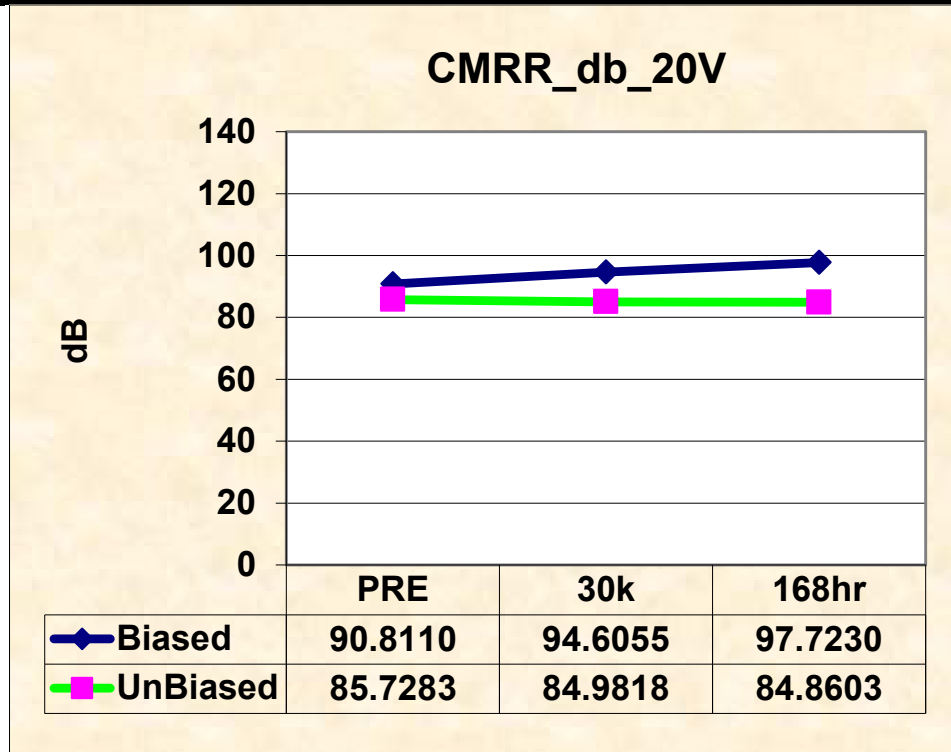
Wafer #	SN	CMRR_db_3V (dB)		
		PRE	10k	30k
CTRL	99	86.6960	80.9010	81.3583
Biased	104	94.8480	86.5670	84.8183
	105	80.7440	101.1500	108.4641
	112	87.9880	84.3000	84.0776
	114	104.8930	101.1050	96.1063
	Min	80.7440	84.3000	84.0776
	Max	104.8930	101.1500	108.4641
	Mean	92.1183	93.2805	93.3666
UnBiased	106	121.4700	91.4230	92.9985
	107	76.3330	83.2530	84.1883
	120	81.7470	80.2070	80.4916
	122	80.0740	79.0670	79.9361
	Min	76.3330	79.0670	79.9361
	Max	121.4700	91.4230	92.9985
	Mean	89.9060	83.4875	84.4036



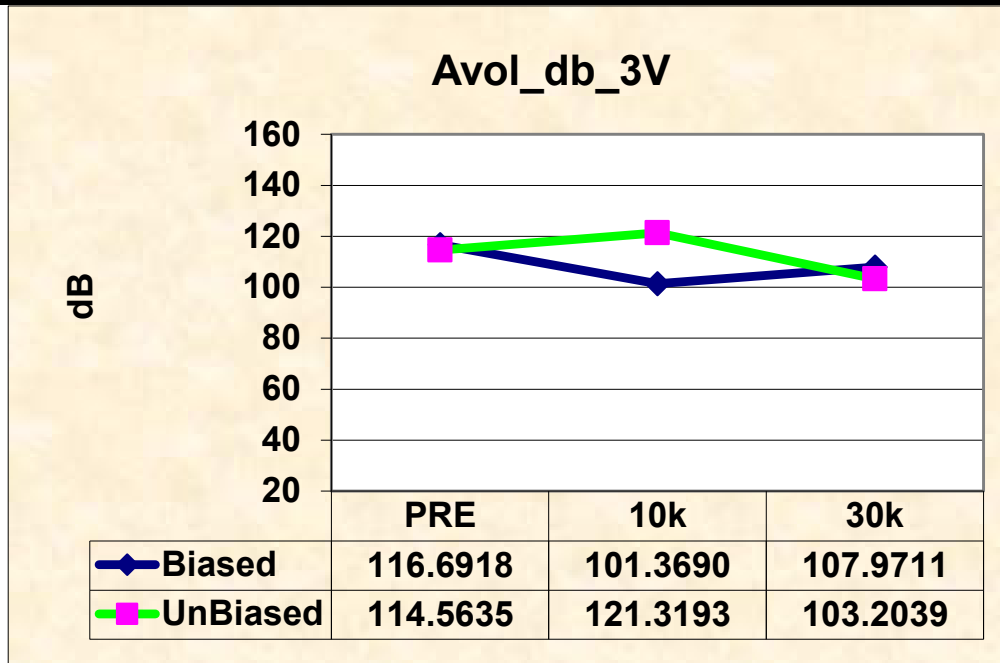
Wafer #	SN	CMRR_db_5V (dB)		
		PRE	10k	30k
CTRL	99	87.1230	80.3450	80.8842
Biased	104	106.5430	88.4240	86.4825
	105	80.5360	103.1170	102.3628
	112	86.5130	85.9000	84.7023
	114	100.3230	101.4000	107.2587
	Min	80.5360	85.9000	84.7023
	Max	106.5430	103.1170	107.2587
	Mean	93.4788	94.7103	95.2016
UnBiased	106	118.2210	89.3620	88.4732
	107	76.3650	84.0960	86.7102
	120	80.5420	80.1200	82.0570
	122	79.9370	79.4830	79.8704
	Min	76.3650	79.4830	79.8704
	Max	118.2210	89.3620	88.4732
	Mean	88.7663	83.2653	84.2777



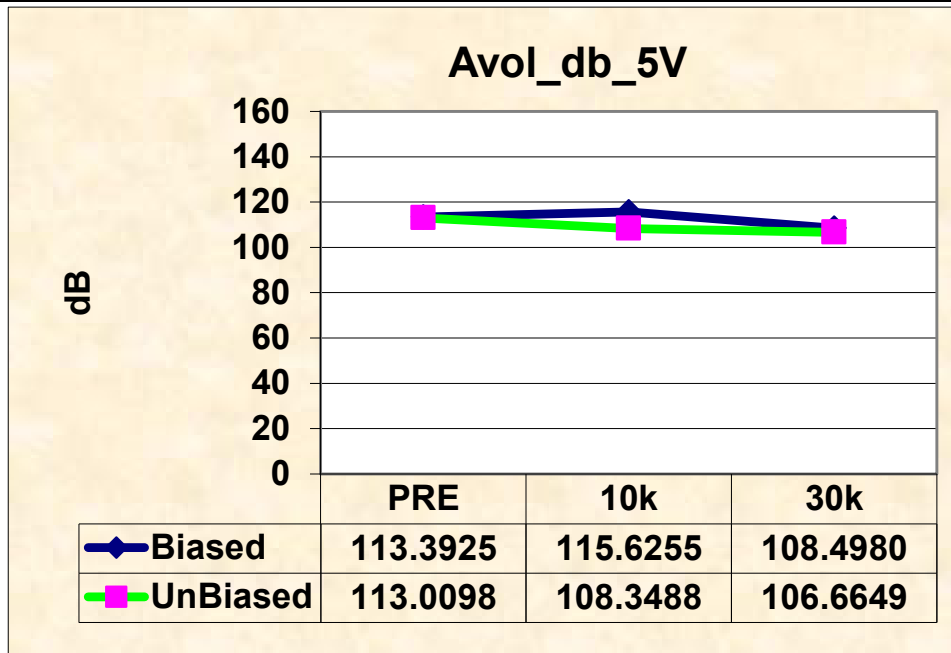
Wafer #	SN	CMRR_db_20V (dB)		
		PRE	10k	30k
CTRL	99	84.3140	80.7160	82.3609
Biased	104	92.5900	87.2450	86.7966
	105	81.4640	93.8360	99.0244
	112	87.2780	86.7060	86.1774
	114	101.9120	110.6350	118.8934
	Min	81.4640	86.7060	86.1774
	Max	101.9120	110.6350	118.8934
	Mean	90.8110	94.6055	97.7230
UnBiased	106	105.4840	91.5620	92.3135
	107	76.6550	87.2900	85.1452
	120	80.8710	81.0310	81.6072
	122	79.9030	80.0440	80.3754
	Min	76.6550	80.0440	80.3754
	Max	105.4840	91.5620	92.3135
	Mean	85.7283	84.9818	84.8603



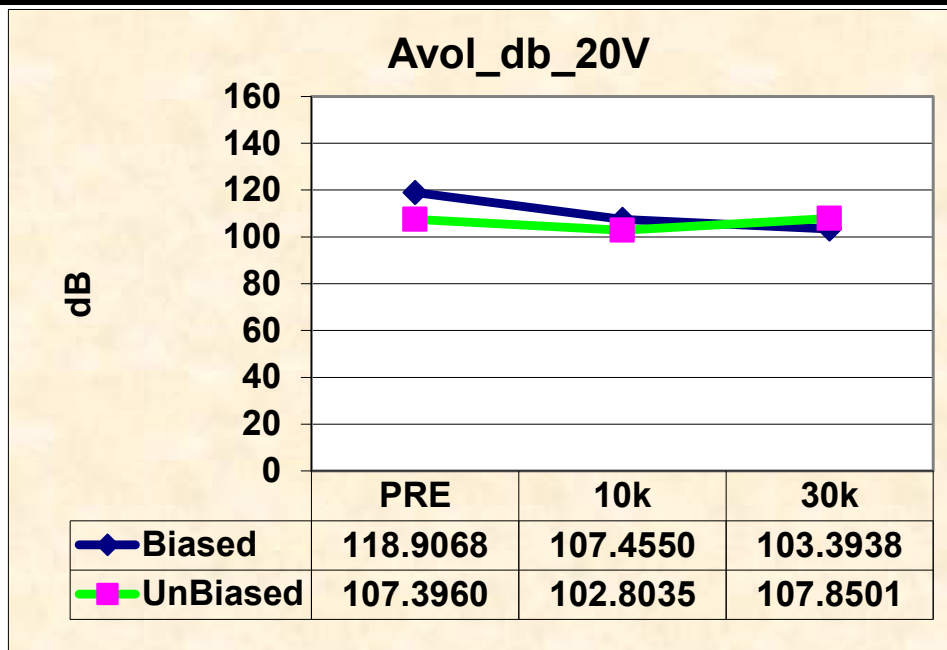
		Avol_3V (dB)		
Wafer #	SN	PRE	10k	30k
CTRL	99	100.1260	125.8210	99.6364
Biased	104	117.1770	96.1980	102.4467
	105	104.6870	98.2330	99.0486
	112	115.5560	98.8120	115.8446
	114	129.3470	112.2330	114.5443
	Min	104.6870	96.1980	99.0486
	Max	129.3470	112.2330	115.8446
	Mean	116.6918	101.3690	107.9711
UnBiased	106	101.0290	107.5910	100.8034
	107	131.1050	136.3640	104.0085
	120	108.0740	111.8280	109.1360
	122	118.0460	129.4940	98.8678
	Min	101.0290	107.5910	98.8678
	Max	131.1050	136.3640	109.1360
	Mean	114.5635	121.3193	103.2039



		Avol_5V (dB)		
Wafer #	SN	PRE	10k	30k
CTRL	99	109.9600	104.3990	107.7136
Biased	104	119.4430	107.9280	102.0886
	105	120.5130	103.2330	104.9872
	112	100.9440	127.1910	100.0948
	114	112.6700	124.1500	126.8214
	Min	100.9440	103.2330	100.0948
	Max	120.5130	127.1910	126.8214
	Mean	113.3925	115.6255	108.4980
UnBiased	106	112.8390	109.0490	103.3931
	107	110.1100	110.5750	102.0976
	120	117.3660	109.5220	118.2633
	122	111.7240	104.2490	102.9054
	Min	110.1100	104.2490	102.0976
	Max	117.3660	110.5750	118.2633
	Mean	113.0098	108.3488	106.6649

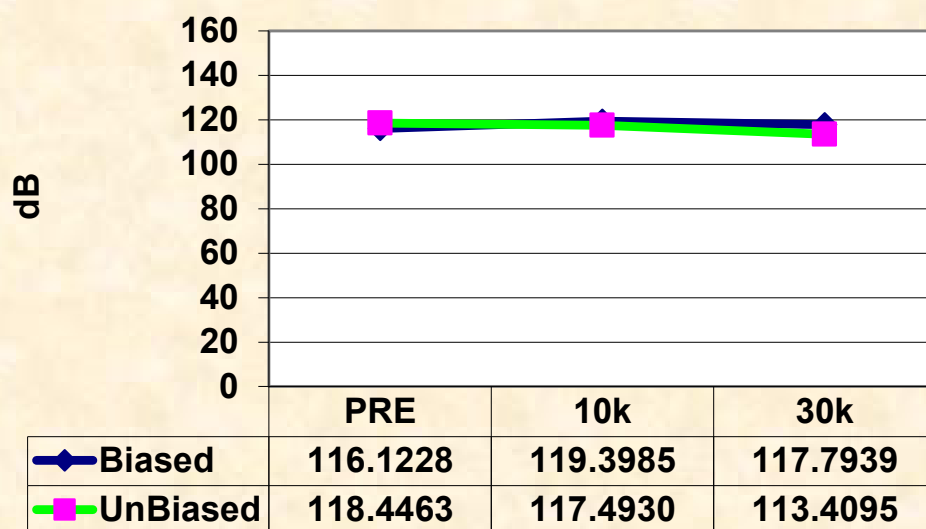


Wafer #	SN	Avol_20V (dB)		
		PRE	10k	30k
CTRL	99	96.8200	105.0780	96.2958
Biased	104	123.1210	95.9750	100.9022
	105	108.8720	101.5520	98.2373
	112	106.2940	109.0620	108.3309
	114	137.3400	123.2310	106.1047
	Min	106.2940	95.9750	98.2373
	Max	137.3400	123.2310	108.3309
	Mean	118.9068	107.4550	103.3938
UnBiased	106	103.7850	109.6430	108.4237
	107	109.6000	97.2360	100.4227
	120	102.5130	102.8020	117.6397
	122	113.6860	101.5330	104.9143
	Min	102.5130	97.2360	100.4227
	Max	113.6860	109.6430	117.6397
	Mean	107.3960	102.8035	107.8501

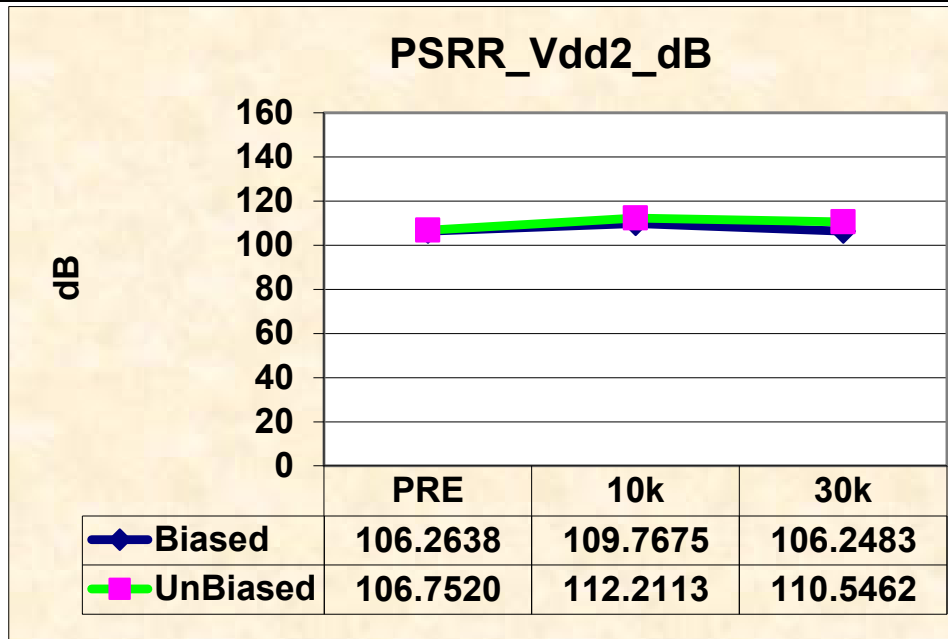


Wafer #	SN	PSRR_Vdd1		
		PRE	10k	30k
CTRL	99	108.6800	120.0000	119.1447
Biased	104	120.0000	112.4290	128.6871
	105	107.8520	120.0000	119.1447
	112	120.0000	125.1650	103.3437
	114	116.6390	120.0000	120.0000
	Min	107.8520	112.4290	103.3437
	Max	120.0000	125.1650	128.6871
	Mean	116.1228	119.3985	117.7939
UnBiased	106	115.6160	111.1860	120.0000
	107	125.1580	120.0000	120.0000
	120	113.8730	128.6870	111.1859
	122	119.1380	110.0990	102.4521
	Min	113.8730	110.0990	102.4521
	Max	125.1580	128.6870	120.0000
	Mean	118.4463	117.4930	113.4095

PSRR_Vdd1_dB

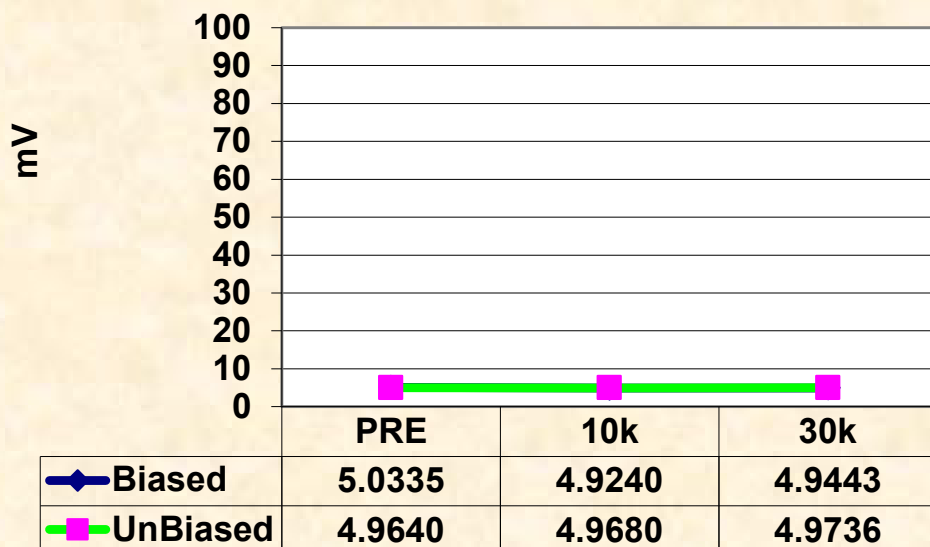


Wafer #	SN	PSRR_Vdd2		
		PRE	10k	30k
CTRL	99	106.0730	120.0000	122.6665
Biased	104	98.9940	120.0000	109.6023
	105	111.7780	113.1240	110.6253
	112	105.1580	108.2630	105.7646
	114	109.1250	97.6830	99.0011
	Min	98.9940	97.6830	99.0011
	Max	111.7780	120.0000	110.6253
	Mean	106.2638	109.7675	106.2483
UnBiased	106	103.5750	120.7280	106.7489
	107	106.0730	111.7850	106.7489
	120	114.7010	102.4520	120.0000
	122	102.6590	113.8800	108.6871
	Min	102.6590	102.4520	106.7489
	Max	114.7010	120.7280	120.0000
	Mean	106.7520	112.2113	110.5462

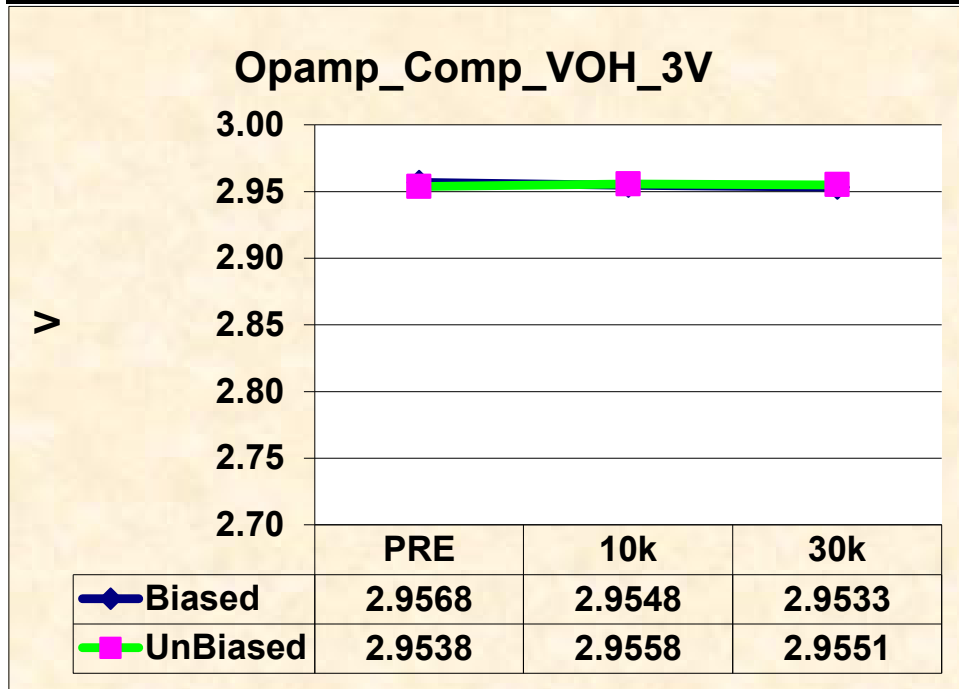


		VOL_Opamp_3V (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.1130	4.9840	4.9764
Biased	104	5.1930	4.9580	4.9842
	105	5.0850	5.0920	5.1001
	112	4.9850	4.8680	4.8997
	114	4.8710	4.7780	4.7932
	Min	4.8710	4.7780	4.7932
	Max	5.1930	5.0920	5.1001
	Mean	5.0335	4.9240	4.9443
UnBiased	106	4.8880	5.1280	5.1298
	107	5.0410	4.9840	4.9967
	120	4.9890	4.8890	4.8981
	122	4.9380	4.8710	4.8699
	Min	4.8880	4.8710	4.8699
	Max	5.0410	5.1280	5.1298
	Mean	4.9640	4.9680	4.9736

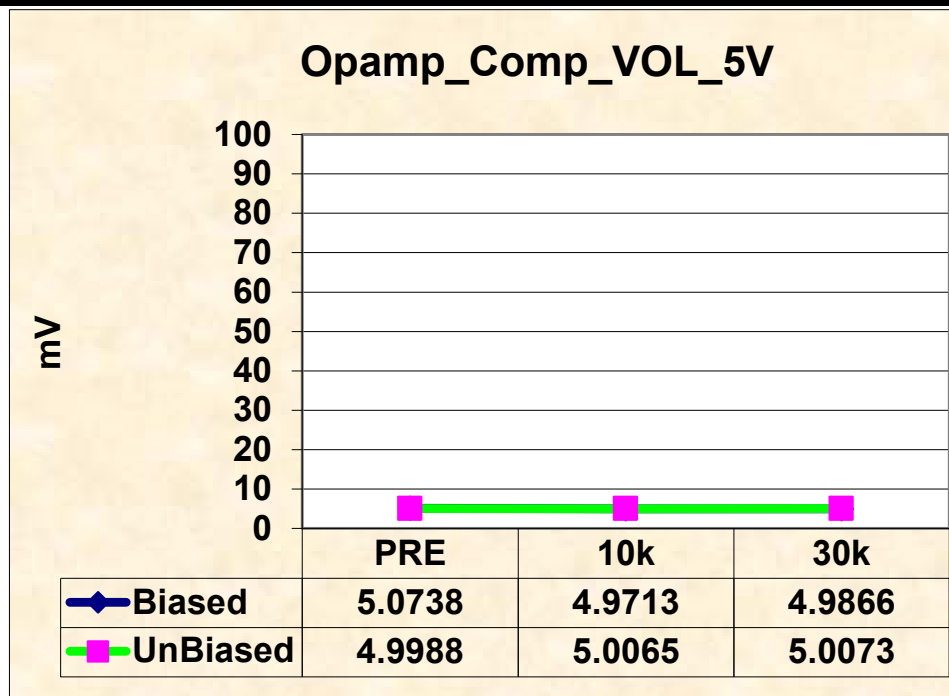
Opamp_Comp_VOL_3V



		VOH_Opamp_3V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.9600	2.9540	2.9541
Biased	104	2.9580	2.9550	2.9535
	105	2.9530	2.9520	2.9515
	112	2.9580	2.9560	2.9540
	114	2.9580	2.9560	2.9543
	Min	2.9530	2.9520	2.9515
	Max	2.9580	2.9560	2.9543
	Mean	2.9568	2.9548	2.9533
UnBiased	106	2.9510	2.9580	2.9576
	107	2.9560	2.9590	2.9585
	120	2.9590	2.9580	2.9570
	122	2.9490	2.9480	2.9472
	Min	2.9490	2.9480	2.9472
	Max	2.9590	2.9590	2.9585
	Mean	2.9538	2.9558	2.9551

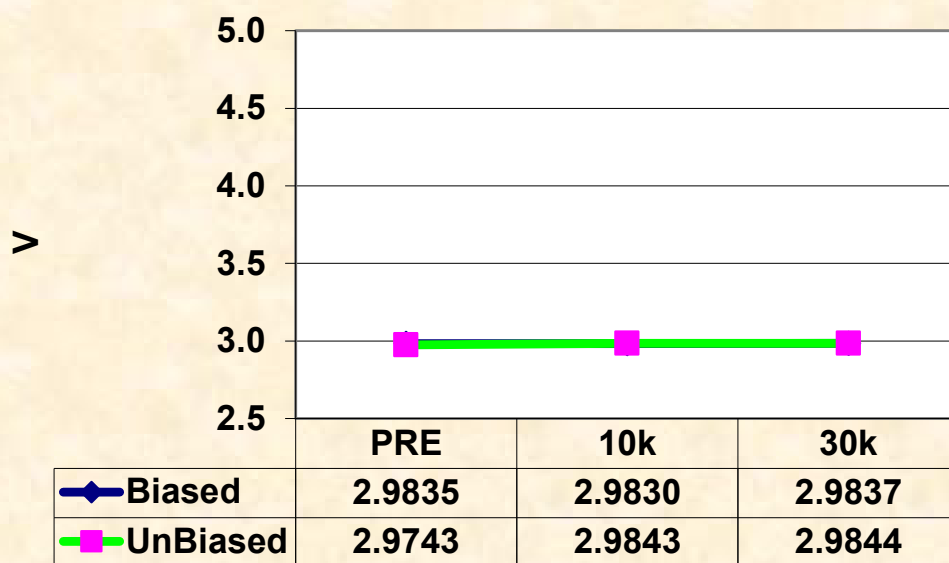


Wafer #	SN	VOL_Opamp_5V (mV)		
		PRE	10k	30k
CTRL	99	5.1510	5.0000	4.9999
Biased	104	5.2430	5.0170	5.0218
	105	5.1180	5.1410	5.1502
	112	5.0290	4.9120	4.9372
	114	4.9050	4.8150	4.8371
	Min	4.9050	4.8150	4.8371
	Max	5.2430	5.1410	5.1502
	Mean	5.0738	4.9713	4.9866
UnBiased	106	4.9220	5.1640	5.1893
	107	5.0800	5.0450	5.0296
	120	5.0440	4.9390	4.9388
	122	4.9490	4.8780	4.8715
	Min	4.9220	4.8780	4.8715
	Max	5.0800	5.1640	5.1893
	Mean	4.9988	5.0065	5.0073



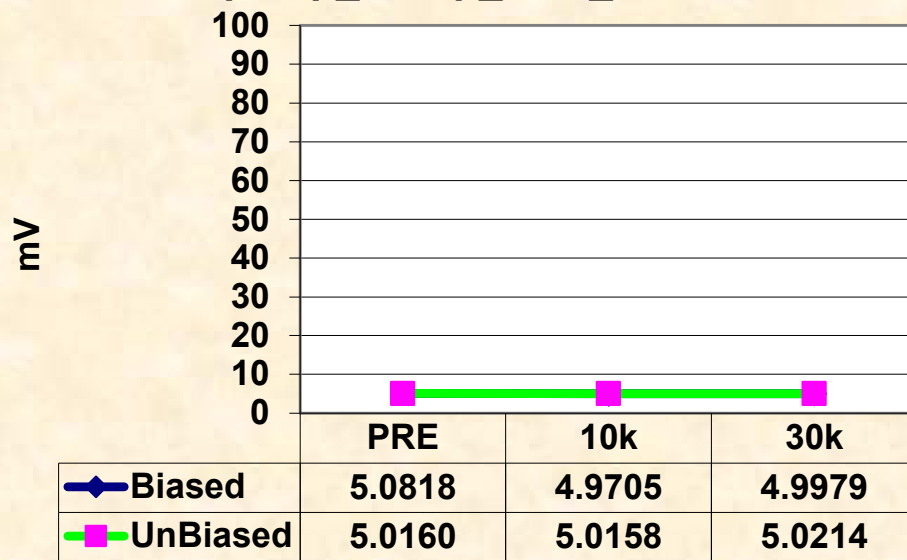
		VOH_Opamp_5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.9990	2.9740	2.9742
Biased	104	2.9880	2.9830	2.9827
	105	2.9740	2.9750	2.9776
	112	2.9860	2.9880	2.9879
	114	2.9860	2.9860	2.9867
	Min	2.9740	2.9750	2.9776
	Max	2.9880	2.9880	2.9879
	Mean	2.9835	2.9830	2.9837
UnBiased	106	2.9650	2.9890	2.9919
	107	2.9800	2.9980	2.9981
	120	2.9880	2.9870	2.9860
	122	2.9640	2.9630	2.9617
	Min	2.9640	2.9630	2.9617
	Max	2.9880	2.9980	2.9981
	Mean	2.9743	2.9843	2.9844

Opamp_Comp_VOH_5V

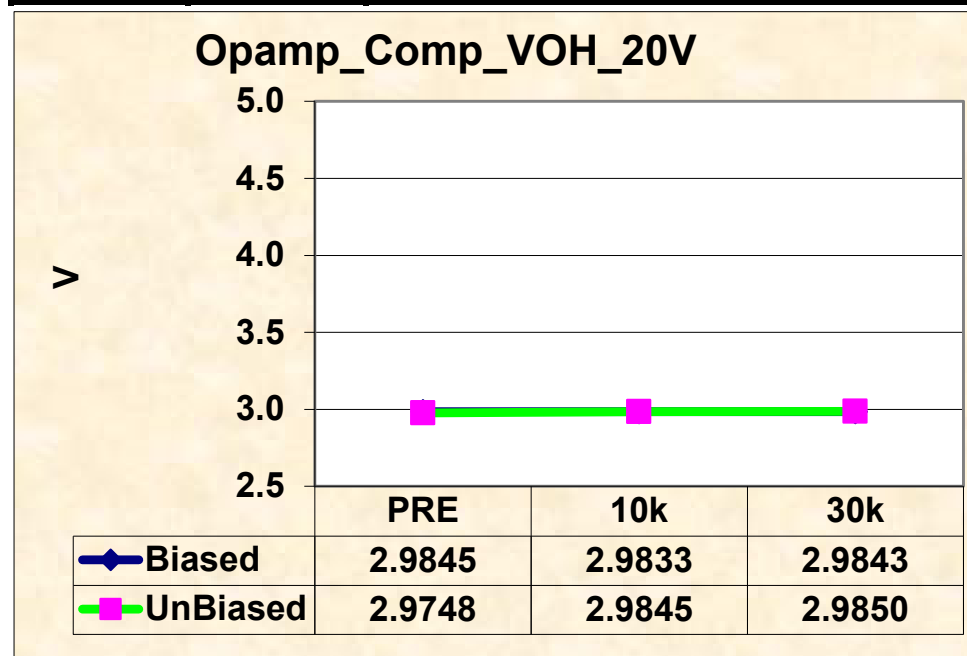


		VOL_Opamp_20V (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.1690	5.0050	5.0061
Biased	104	5.2490	5.0050	5.0249
	105	5.1300	5.1380	5.1486
	112	5.0320	4.9290	4.9623
	114	4.9160	4.8100	4.8558
	Min	4.9160	4.8100	4.8558
	Max	5.2490	5.1380	5.1486
	Mean	5.0818	4.9705	4.9979
UnBiased	106	4.9320	5.1770	5.2002
	107	5.0880	5.0440	5.0547
	120	5.0610	4.9390	4.9513
	122	4.9830	4.9030	4.8793
	Min	4.9320	4.9030	4.8793
	Max	5.0880	5.1770	5.2002
	Mean	5.0160	5.0158	5.0214

Opamp_Comp_VOL_20V

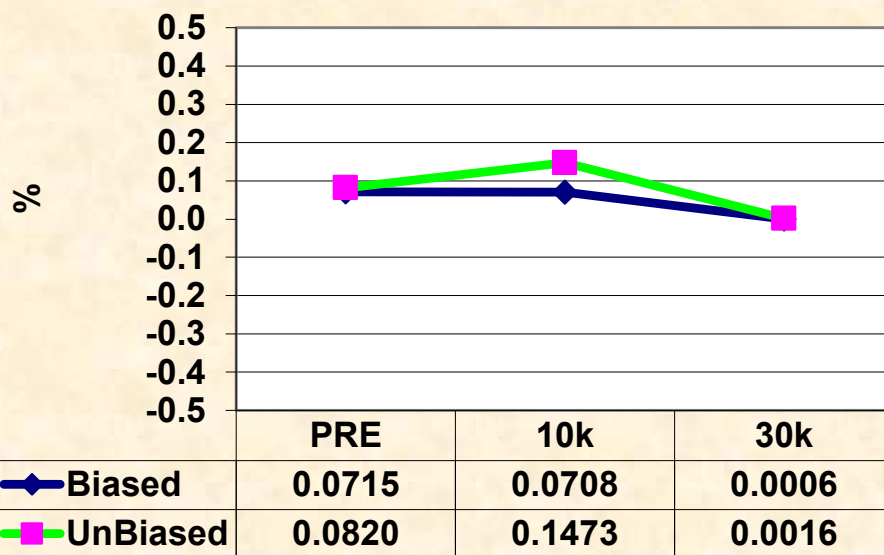


Wafer #	SN	VOH_Opamp_20V (V)		
		PRE	10k	30k
CTRL	99	3.0000	2.9750	2.9749
Biased	104	2.9890	2.9830	2.9833
	105	2.9750	2.9760	2.9784
	112	2.9870	2.9880	2.9883
	114	2.9870	2.9860	2.9870
	Min	2.9750	2.9760	2.9784
	Max	2.9890	2.9880	2.9883
	Mean	2.9845	2.9833	2.9843
UnBiased	106	2.9660	2.9890	2.9923
	107	2.9800	2.9980	2.9987
	120	2.9880	2.9880	2.9866
	122	2.9650	2.9630	2.9622
	Min	2.9650	2.9630	2.9622
	Max	2.9880	2.9980	2.9987
	Mean	2.9748	2.9845	2.9850

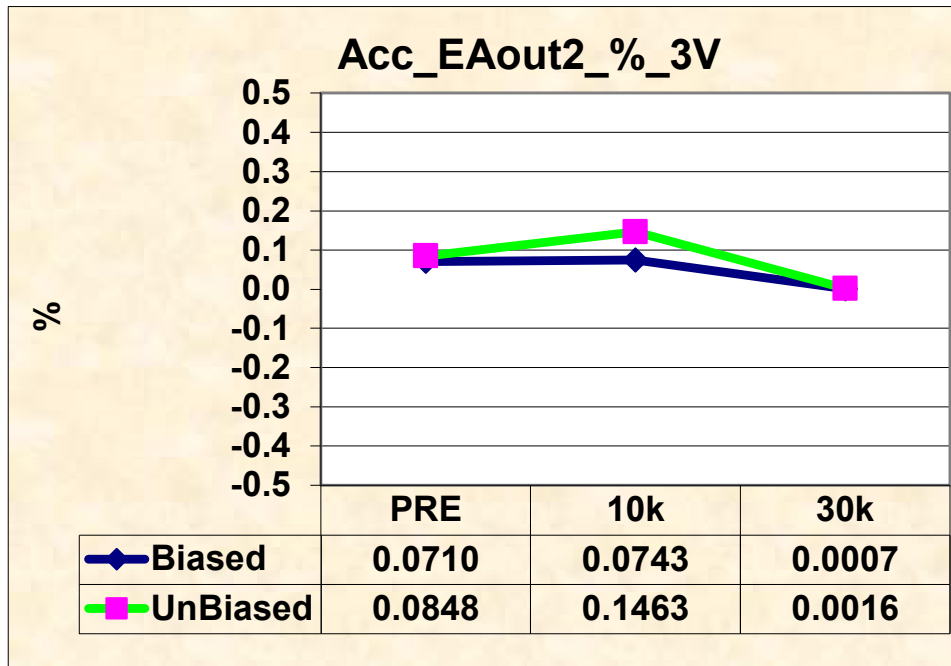


Wafer #	SN	Accuracy_EAout_%_3V (%)		
		PRE	10k	30k
CTRL	99	0.0020	0.0070	0.0002
Biased	104	0.0070	0.0100	0.0000
	105	0.0050	0.0690	0.0002
	112	0.1330	0.0800	0.0007
	114	0.1410	0.1240	0.0014
	Min	0.0050	0.0100	0.0000
	Max	0.1410	0.1240	0.0014
	Mean	0.0715	0.0708	0.0006
UnBiased	106	0.0050	0.1950	0.0029
	107	0.0390	0.0640	0.0004
	120	0.1380	0.1330	0.0007
	122	0.1460	0.1970	0.0022
	Min	0.0050	0.0640	0.0004
	Max	0.1460	0.1970	0.0029
	Mean	0.0820	0.1473	0.0016

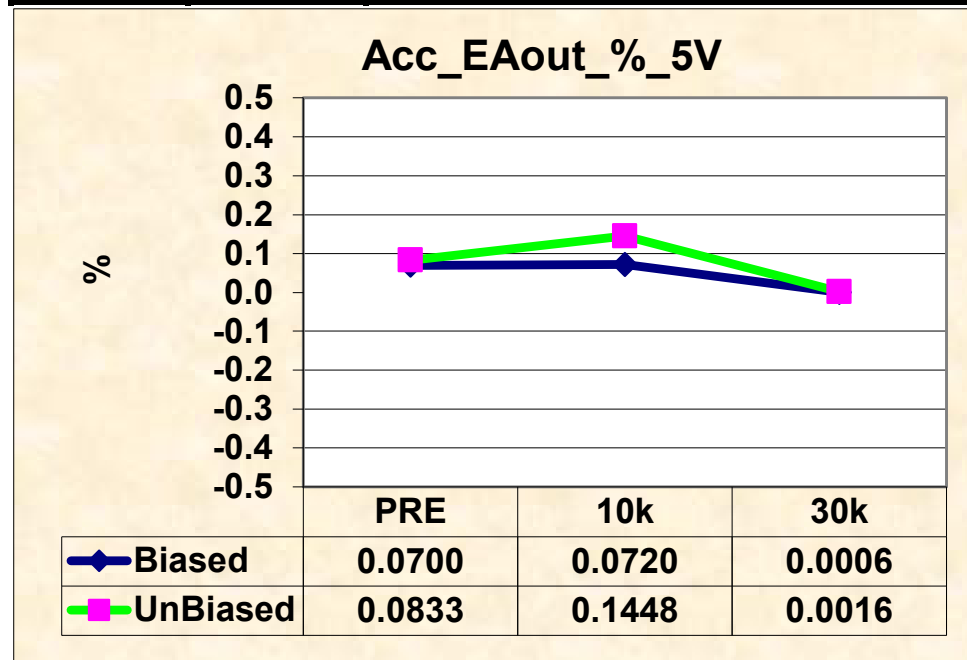
Acc_EAout_%_3V



Wafer #	SN	Accuracy_EAout2_%_3V (%)		
		PRE	10k	30k
CTRL	99	0.0220	0.0270	0.0003
Biased	104	0.0020	0.0300	0.0002
	105	0.0200	0.0620	0.0003
	112	0.1150	0.0720	0.0006
	114	0.1470	0.1330	0.0015
	Min	0.0020	0.0300	0.0002
	Max	0.1470	0.1330	0.0015
	Mean	0.0710	0.0743	0.0007
UnBiased	106	0.0200	0.2160	0.0031
	107	0.0480	0.0570	0.0002
	120	0.1460	0.1320	0.0009
	122	0.1250	0.1800	0.0021
	Min	0.0200	0.0570	0.0002
	Max	0.1460	0.2160	0.0031
	Mean	0.0848	0.1463	0.0016

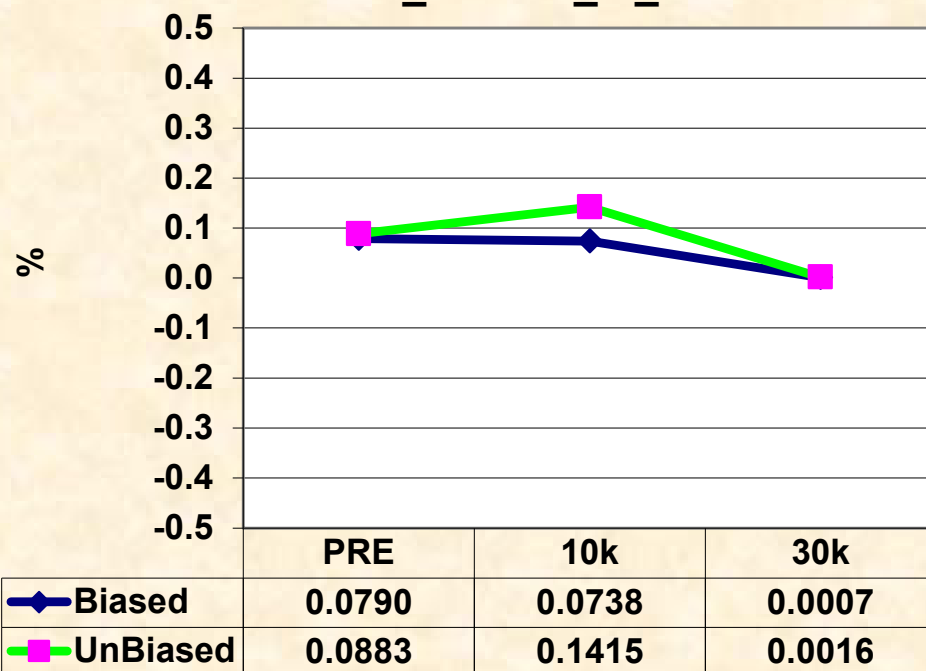


Wafer #	SN	Accuracy_EAout_%_5V (%)		
		PRE	10k	30k
CTRL	99	0.0020	0.0120	0.0002
Biased	104	0.0050	0.0190	0.0001
	105	0.0060	0.0650	0.0002
	112	0.1240	0.0770	0.0006
	114	0.1450	0.1270	0.0014
	Min	0.0050	0.0190	0.0001
	Max	0.1450	0.1270	0.0014
	Mean	0.0700	0.0720	0.0006
UnBiased	106	0.0040	0.2030	0.0029
	107	0.0430	0.0650	0.0004
	120	0.1450	0.1160	0.0008
	122	0.1410	0.1950	0.0023
	Min	0.0040	0.0650	0.0004
	Max	0.1450	0.2030	0.0029
	Mean	0.0833	0.1448	0.0016



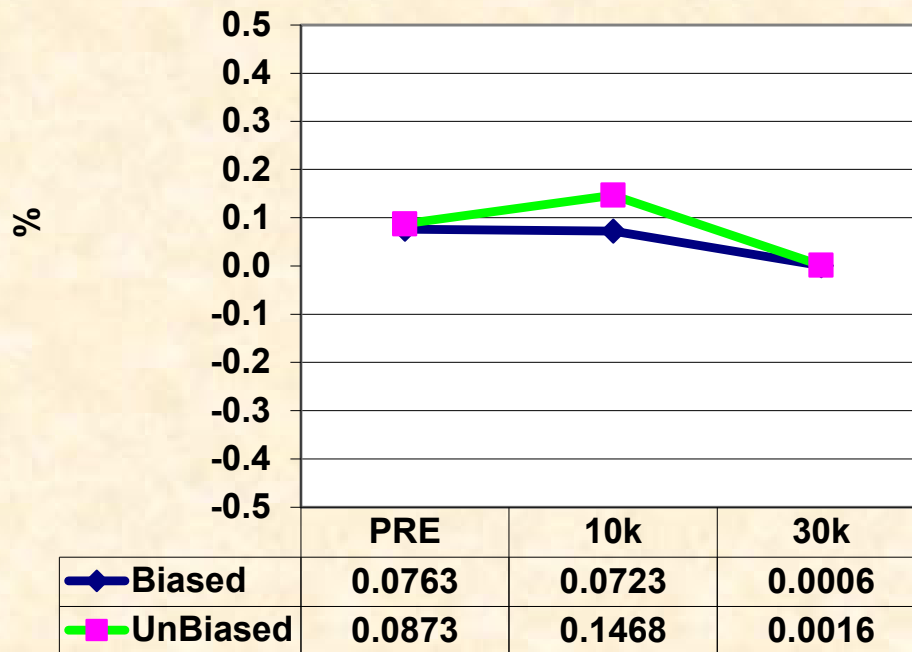
Wafer #	SN	Accuracy_EAout2_%_5V (%)		
		PRE	10k	30k
CTRL	99	0.0230	0.0340	0.0004
Biased	104	0.0230	0.0320	0.0003
	105	0.0260	0.0480	0.0003
	112	0.1090	0.0700	0.0005
	114	0.1580	0.1450	0.0016
	Min	0.0230	0.0320	0.0003
	Max	0.1580	0.1450	0.0016
	Mean	0.0790	0.0738	0.0007
UnBiased	106	0.0180	0.2170	0.0031
	107	0.0600	0.0480	0.0002
	120	0.1520	0.1330	0.0009
	122	0.1230	0.1680	0.0021
	Min	0.0180	0.0480	0.0002
	Max	0.1520	0.2170	0.0031
	Mean	0.0883	0.1415	0.0016

Acc_EAout2_%_5V

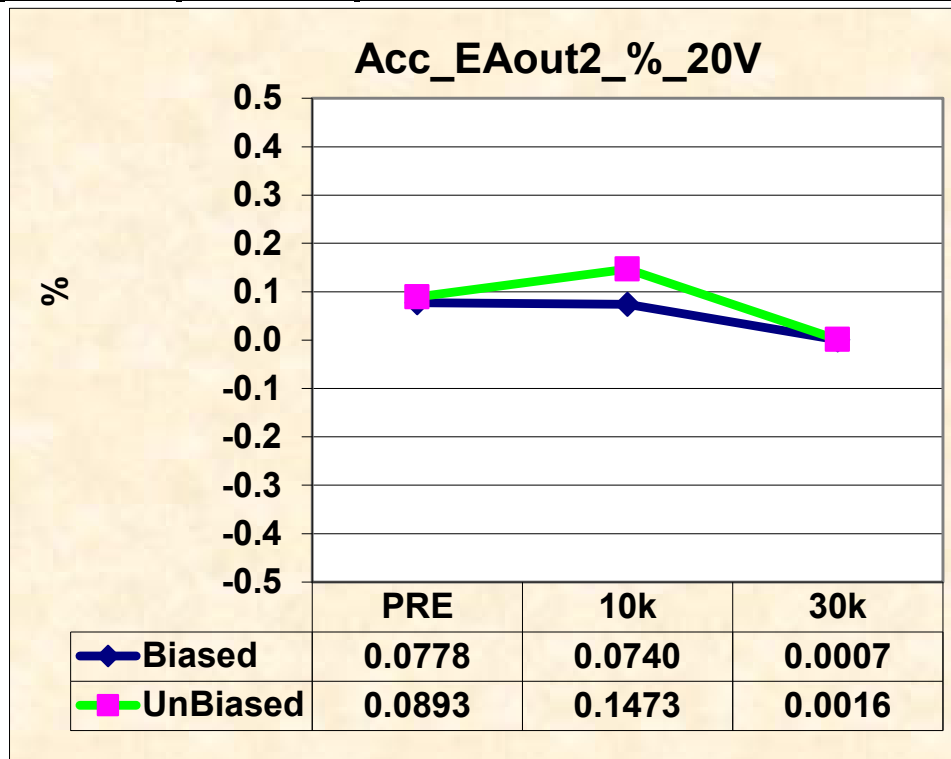


Wafer #	SN	Acc_EAout_%_20V (%)		
		PRE	10k	30k
CTRL	99	0.0080	0.0110	0.0003
Biased	104	0.0190	0.0210	0.0001
	105	0.0170	0.0570	0.0002
	112	0.1200	0.0780	0.0006
	114	0.1490	0.1330	0.0014
	Min	0.0170	0.0210	0.0001
	Max	0.1490	0.1330	0.0014
	Mean	0.0763	0.0723	0.0006
UnBiased	106	0.0130	0.2080	0.0030
	107	0.0500	0.0580	0.0003
	120	0.1450	0.1350	0.0009
	122	0.1410	0.1860	0.0022
	Min	0.0130	0.0580	0.0003
	Max	0.1450	0.2080	0.0030
	Mean	0.0873	0.1468	0.0016

Acc_EAout_%_20V

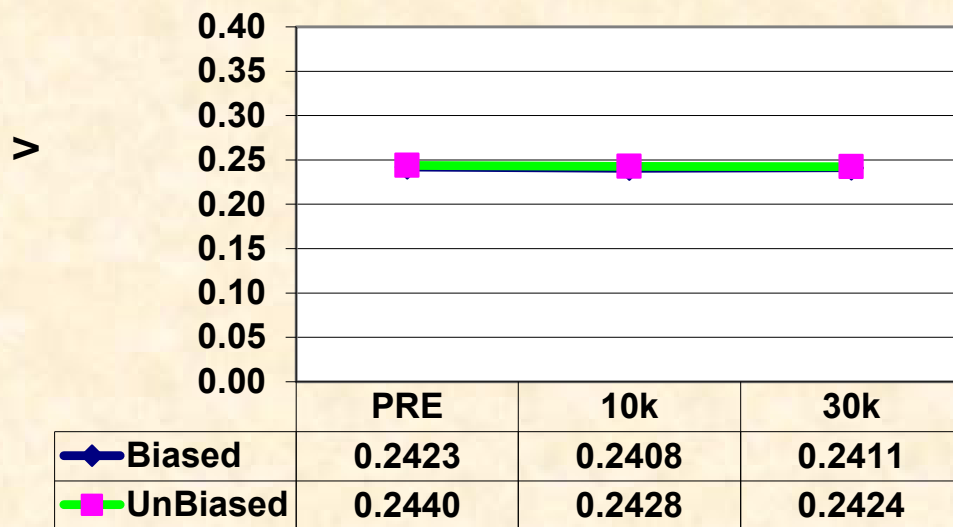


Wafer #	SN	Acc_EAout2_%_20V (%)		
		PRE	10k	30k
CTRL	99	0.0080	0.0270	0.0004
Biased	104	0.0220	0.0390	0.0002
	105	0.0300	0.0460	0.0004
	112	0.1120	0.0600	0.0005
	114	0.1470	0.1510	0.0016
	Min	0.0220	0.0390	0.0002
	Max	0.1470	0.1510	0.0016
	Mean	0.0778	0.0740	0.0007
UnBiased	106	0.0220	0.2250	0.0031
	107	0.0500	0.0470	0.0001
	120	0.1500	0.1410	0.0010
	122	0.1350	0.1760	0.0021
	Min	0.0220	0.0470	0.0001
	Max	0.1500	0.2250	0.0031
	Mean	0.0893	0.1473	0.0016

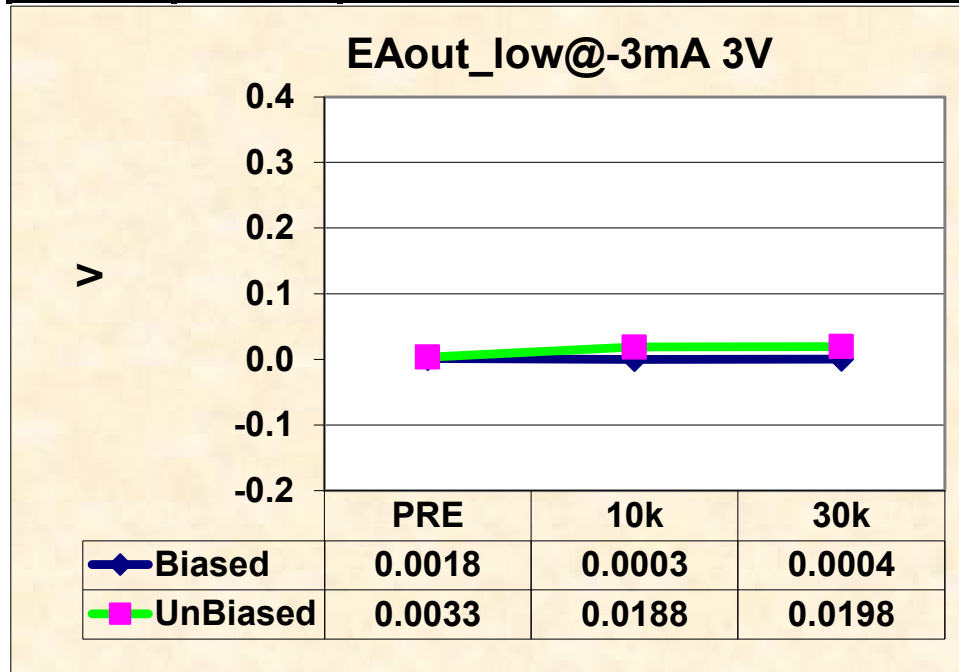


		VOL_EAout@ 3mA 3V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.2410	0.2380	0.2376
Biased	104	0.2410	0.2400	0.2411
	105	0.2390	0.2360	0.2363
	112	0.2440	0.2430	0.2433
	114	0.2450	0.2440	0.2437
	Min	0.2390	0.2360	0.2363
	Max	0.2450	0.2440	0.2437
	Mean	0.2423	0.2408	0.2411
UnBiased	106	0.2410	0.2440	0.2429
	107	0.2450	0.2390	0.2390
	120	0.2450	0.2440	0.2440
	122	0.2450	0.2440	0.2438
	Min	0.2410	0.2390	0.2390
	Max	0.2450	0.2440	0.2440
	Mean	0.2440	0.2428	0.2424

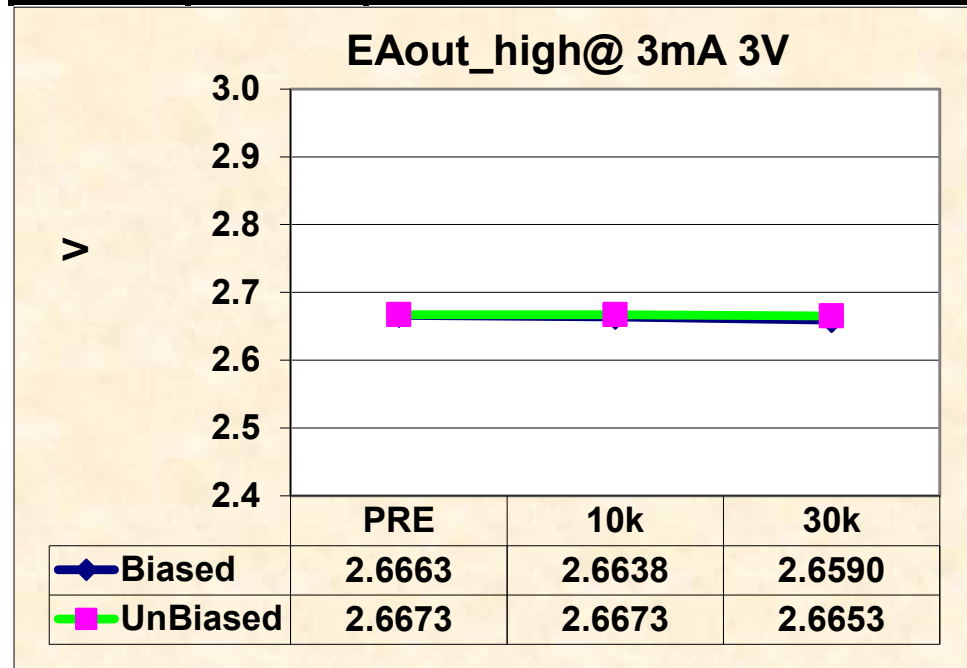
EAout_low@ 3mA 3V



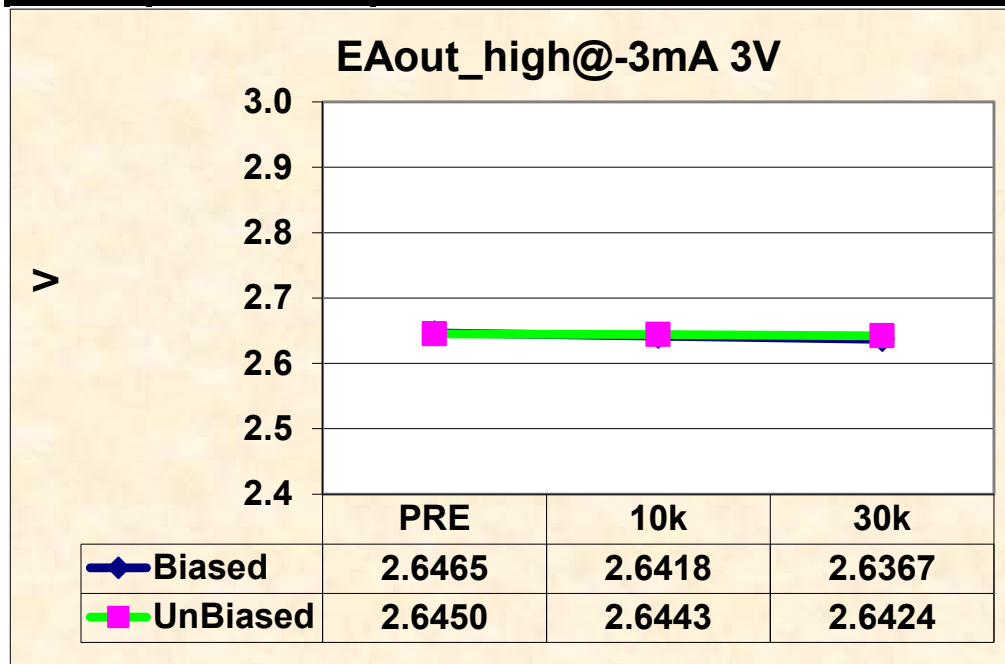
		VOL_EAout@-3mA 3V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.0010	0.0001	0.0014
Biased	104	-0.0030	-0.0030	-0.0026
	105	0.0010	-0.0060	-0.0061
	112	0.0030	0.0030	0.0036
	114	0.0060	0.0070	0.0067
	Min	-0.0030	-0.0060	-0.0061
	Max	0.0060	0.0070	0.0067
	Mean	0.0018	0.0003	0.0004
UnBiased	106	-0.0050	0.0520	0.0527
	107	-0.0060	0.0020	0.0016
	120	0.0210	0.0180	0.0206
	122	0.0030	0.0030	0.0041
	Min	-0.0060	0.0020	0.0016
	Max	0.0210	0.0520	0.0527
	Mean	0.0033	0.0188	0.0198



Wafer #	SN	VOH_EAout@ 3mA 3V (V)		
		PRE	10k	30k
CTRL	99	2.6760	2.6690	2.6695
Biased	104	2.6700	2.6680	2.6636
	105	2.6670	2.6670	2.6627
	112	2.6670	2.6630	2.6578
	114	2.6610	2.6570	2.6520
	Min	2.6610	2.6570	2.6520
	Max	2.6700	2.6680	2.6636
	Mean	2.6663	2.6638	2.6590
UnBiased	106	2.6700	2.6610	2.6593
	107	2.6680	2.6770	2.6749
	120	2.6640	2.6640	2.6617
	122	2.6670	2.6670	2.6654
	Min	2.6640	2.6610	2.6593
	Max	2.6700	2.6770	2.6749
	Mean	2.6673	2.6673	2.6653

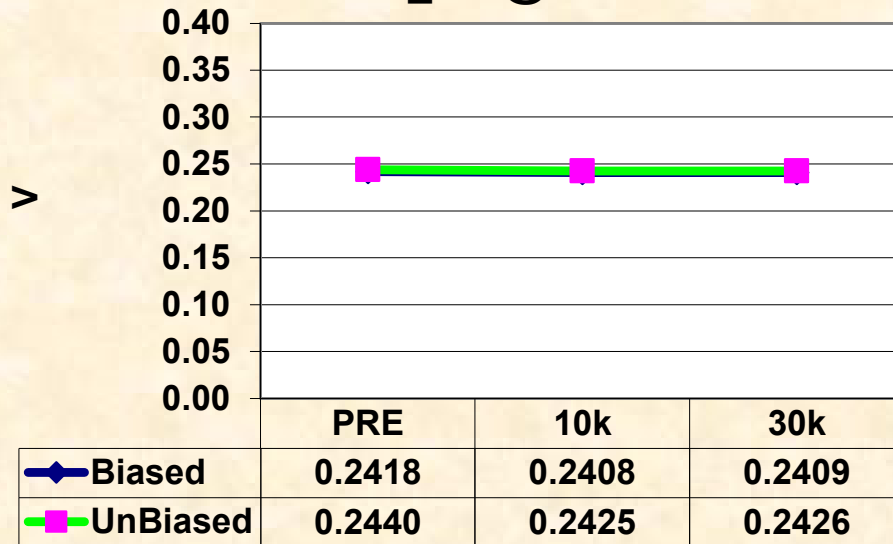


		VOH_EAout@-3mA 3V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.6480	2.6510	2.6514
Biased	104	2.6480	2.6420	2.6374
	105	2.6490	2.6450	2.6399
	112	2.6450	2.6410	2.6352
	114	2.6440	2.6390	2.6341
	Min	2.6440	2.6390	2.6341
	Max	2.6490	2.6450	2.6399
	Mean	2.6465	2.6418	2.6367
UnBiased	106	2.6480	2.6390	2.6371
	107	2.6430	2.6490	2.6469
	120	2.6450	2.6450	2.6436
	122	2.6440	2.6440	2.6420
	Min	2.6430	2.6390	2.6371
	Max	2.6480	2.6490	2.6469
	Mean	2.6450	2.6443	2.6424

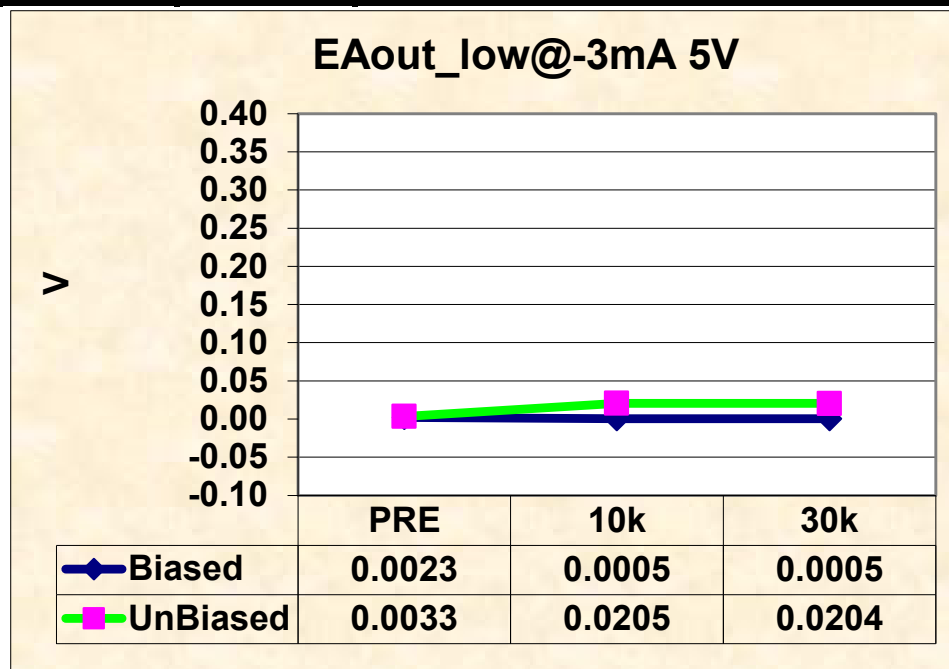


		VOL_EAout@ 3mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.2400	0.2380	0.2378
Biased	104	0.2400	0.2400	0.2405
	105	0.2390	0.2370	0.2369
	112	0.2440	0.2430	0.2431
	114	0.2440	0.2430	0.2431
	Min	0.2390	0.2370	0.2369
	Max	0.2440	0.2430	0.2431
	Mean	0.2418	0.2408	0.2409
UnBiased	106	0.2410	0.2430	0.2438
	107	0.2450	0.2390	0.2387
	120	0.2450	0.2440	0.2439
	122	0.2450	0.2440	0.2439
	Min	0.2410	0.2390	0.2387
	Max	0.2450	0.2440	0.2439
	Mean	0.2440	0.2425	0.2426

EAout_low@ 3mA 5V

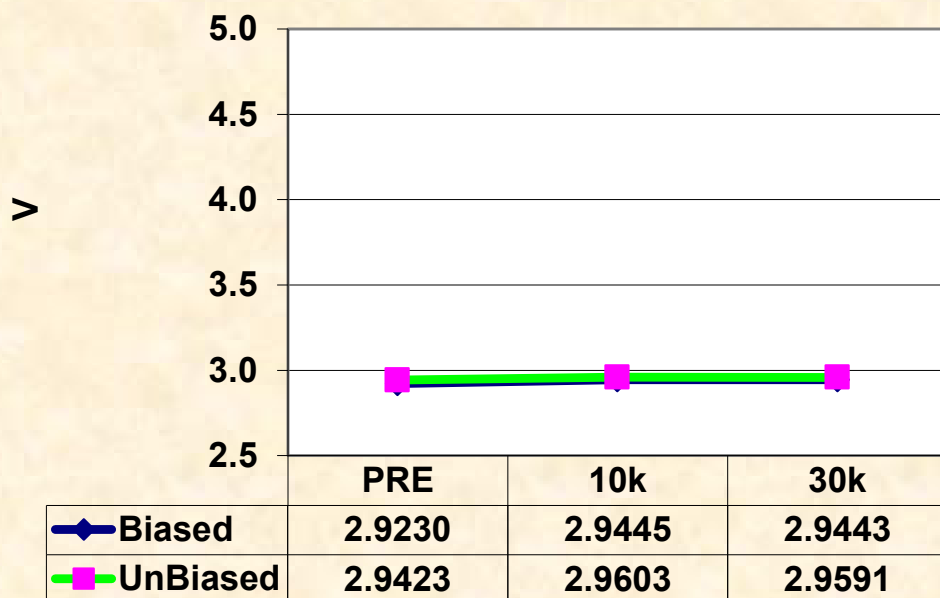


Wafer #	SN	VOL_EAout@-3mA 5V (V)		
		PRE	10k	30k
CTRL	99	0.0010	0.0030	0.0044
Biased	104	-0.0030	-0.0030	-0.0031
	105	0.0030	-0.0050	-0.0054
	112	0.0030	0.0030	0.0038
	114	0.0060	0.0070	0.0065
	Min	-0.0030	-0.0050	-0.0054
	Max	0.0060	0.0070	0.0065
	Mean	0.0023	0.0005	0.0005
UnBiased	106	-0.0050	0.0560	0.0537
	107	-0.0060	0.0020	0.0015
	120	0.0200	0.0200	0.0217
	122	0.0040	0.0040	0.0045
	Min	-0.0060	0.0020	0.0015
	Max	0.0200	0.0560	0.0537
	Mean	0.0033	0.0205	0.0204

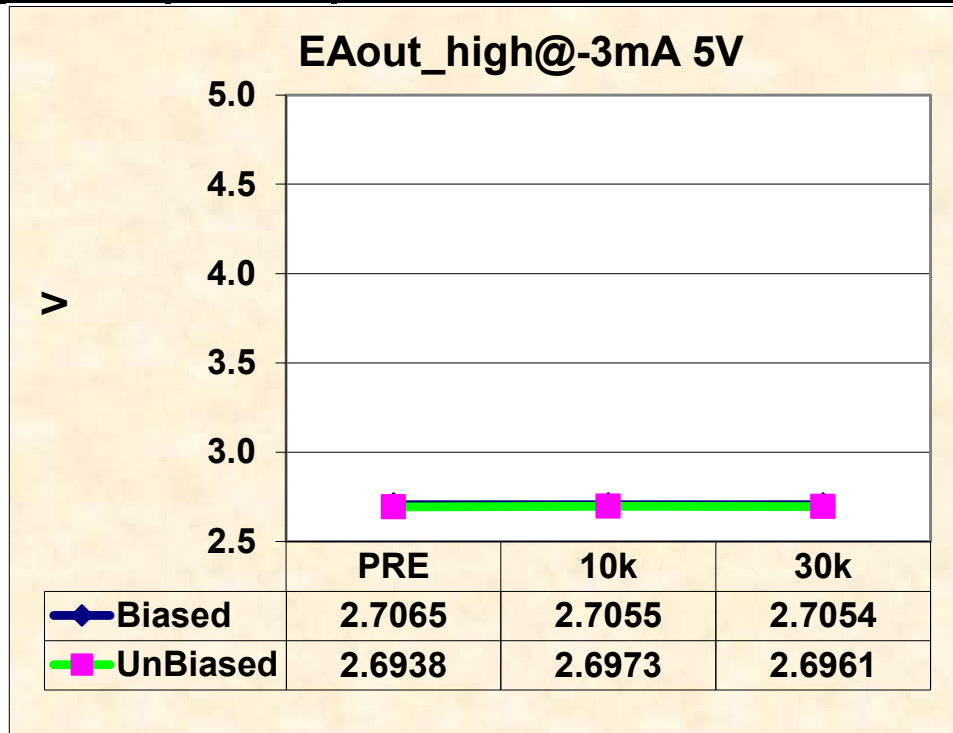


		VOH_EAout@ 3mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	3.0330	2.9050	2.9042
Biased	104	2.9440	2.9830	2.9821
	105	2.9050	2.9530	2.9535
	112	2.9480	2.9470	2.9470
	114	2.8950	2.8950	2.8946
	Min	2.8950	2.8950	2.8946
	Max	2.9480	2.9830	2.9821
	Mean	2.9230	2.9445	2.9443
UnBiased	106	2.9400	2.9460	2.9438
	107	2.9700	3.0340	3.0348
	120	2.9130	2.9140	2.9121
	122	2.9460	2.9470	2.9455
	Min	2.9130	2.9140	2.9121
	Max	2.9700	3.0340	3.0348
	Mean	2.9423	2.9603	2.9591

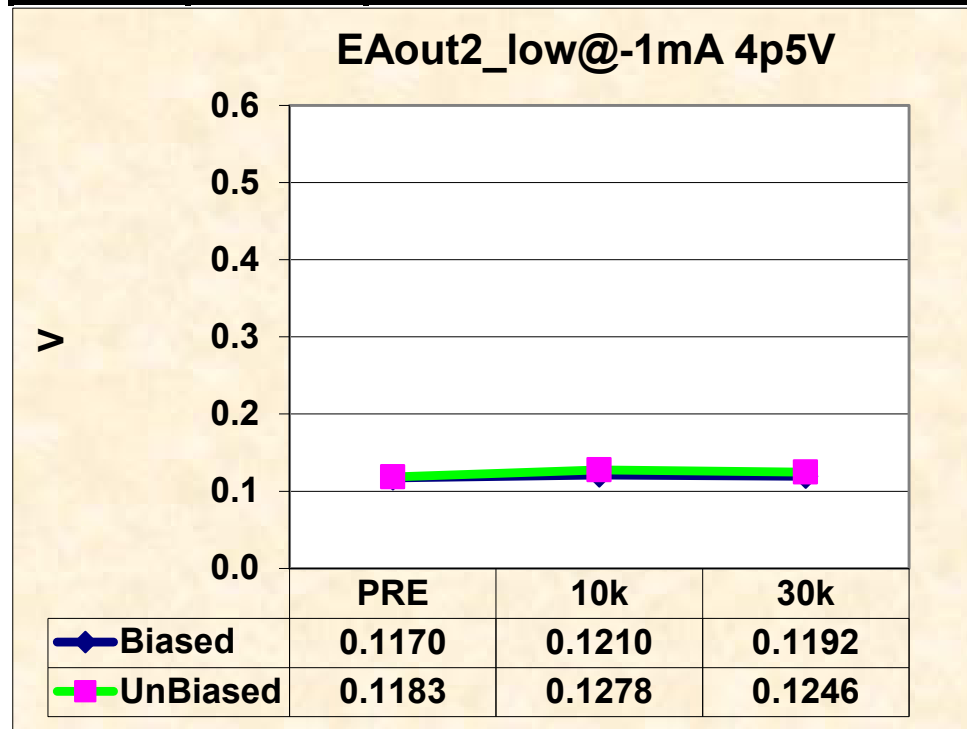
EAout_high@ 3mA 5V



		VOH_EAout@-3mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.7030	2.7230	2.7233
Biased	104	2.7020	2.7250	2.7232
	105	2.7210	2.6910	2.6917
	112	2.7020	2.7040	2.7057
	114	2.7010	2.7020	2.7010
	Min	2.7010	2.6910	2.6917
	Max	2.7210	2.7250	2.7232
	Mean	2.7065	2.7055	2.7054
UnBiased	106	2.6980	2.6850	2.6840
	107	2.6820	2.7060	2.7057
	120	2.7090	2.7120	2.7095
	122	2.6860	2.6860	2.6852
	Min	2.6820	2.6850	2.6840
	Max	2.7090	2.7120	2.7095
	Mean	2.6938	2.6973	2.6961

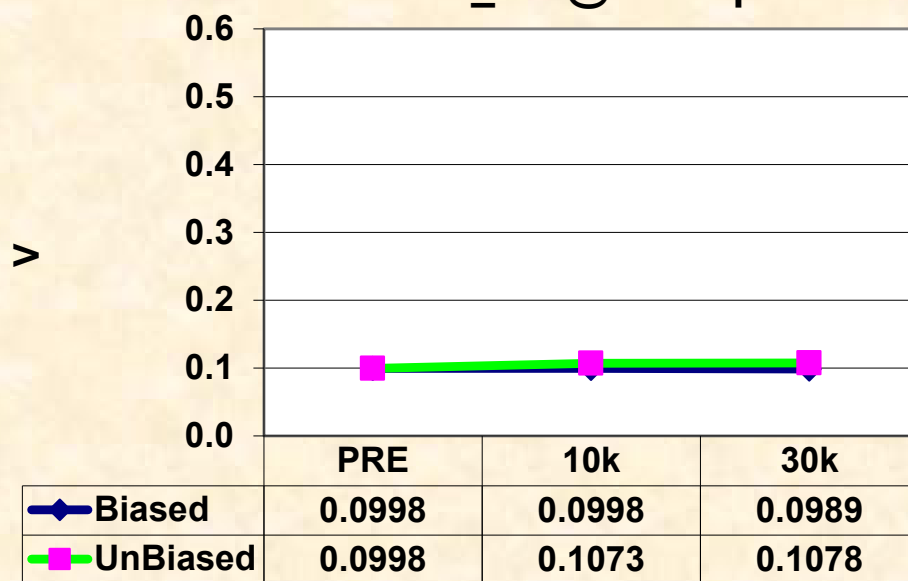


		VOL_EAout2@-1mA 4.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1170	0.1180	0.1167
Biased	104	0.1160	0.1190	0.1179
	105	0.1150	0.1180	0.1182
	112	0.1190	0.1240	0.1207
	114	0.1180	0.1230	0.1200
	Min	0.1150	0.1180	0.1179
	Max	0.1190	0.1240	0.1207
	Mean	0.1170	0.1210	0.1192
UnBiased	106	0.1160	0.1410	0.1390
	107	0.1190	0.1220	0.1161
	120	0.1190	0.1230	0.1231
	122	0.1190	0.1250	0.1203
	Min	0.1160	0.1220	0.1161
	Max	0.1190	0.1410	0.1390
	Mean	0.1183	0.1278	0.1246

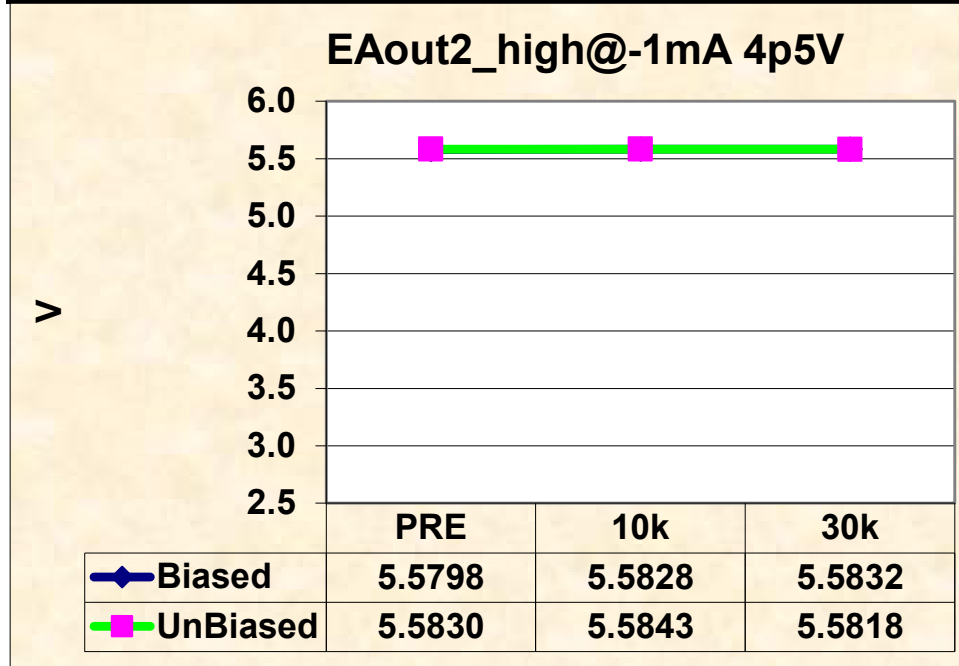


		VOL_EAout2@1mA 4.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.0980	0.0960	0.0950
Biased	104	0.0990	0.0980	0.0984
	105	0.0970	0.0980	0.0981
	112	0.1020	0.1010	0.0993
	114	0.1010	0.1020	0.0996
	Min	0.0970	0.0980	0.0981
	Max	0.1020	0.1020	0.0996
	Mean	0.0998	0.0998	0.0989
UnBiased	106	0.1000	0.1280	0.1350
	107	0.0980	0.0990	0.0974
	120	0.1010	0.1010	0.0999
	122	0.1000	0.1010	0.0987
	Min	0.0980	0.0990	0.0974
	Max	0.1010	0.1280	0.1350
	Mean	0.0998	0.1073	0.1078

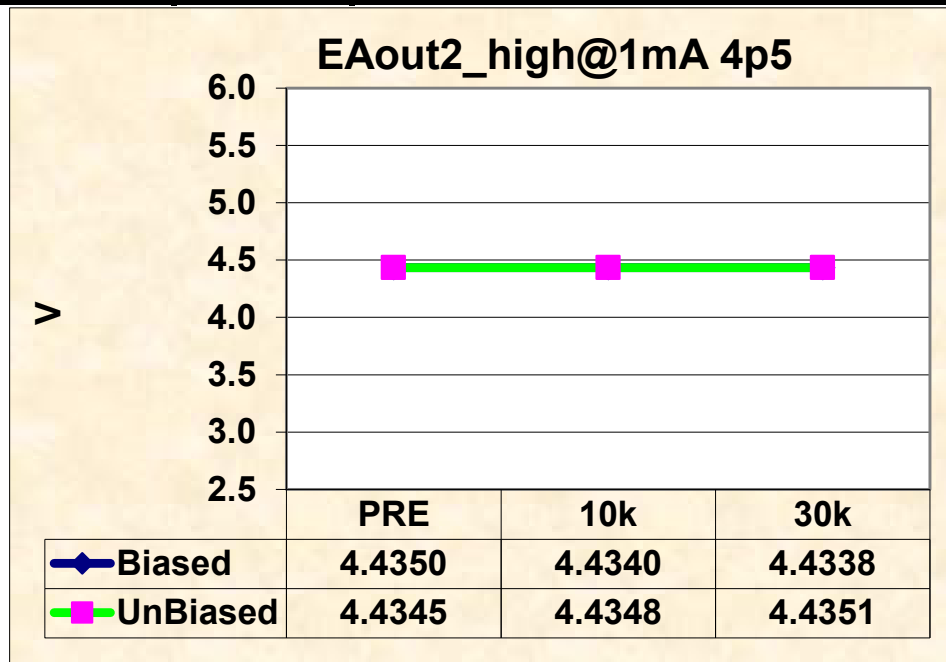
EAout2_low@1mA 4p5V



		VOH_EAout2@-1mA 4.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5860	5.5830	5.5792
Biased	104	5.5790	5.5850	5.5850
	105	5.5820	5.5910	5.5914
	112	5.5770	5.5760	5.5758
	114	5.5810	5.5790	5.5807
	Min	5.5770	5.5760	5.5758
	Max	5.5820	5.5910	5.5914
	Mean	5.5798	5.5828	5.5832
UnBiased	106	5.5880	5.5850	5.5789
	107	5.5810	5.5890	5.5856
	120	5.5790	5.5800	5.5792
	122	5.5840	5.5830	5.5834
	Min	5.5790	5.5800	5.5789
	Max	5.5880	5.5890	5.5856
	Mean	5.5830	5.5843	5.5818

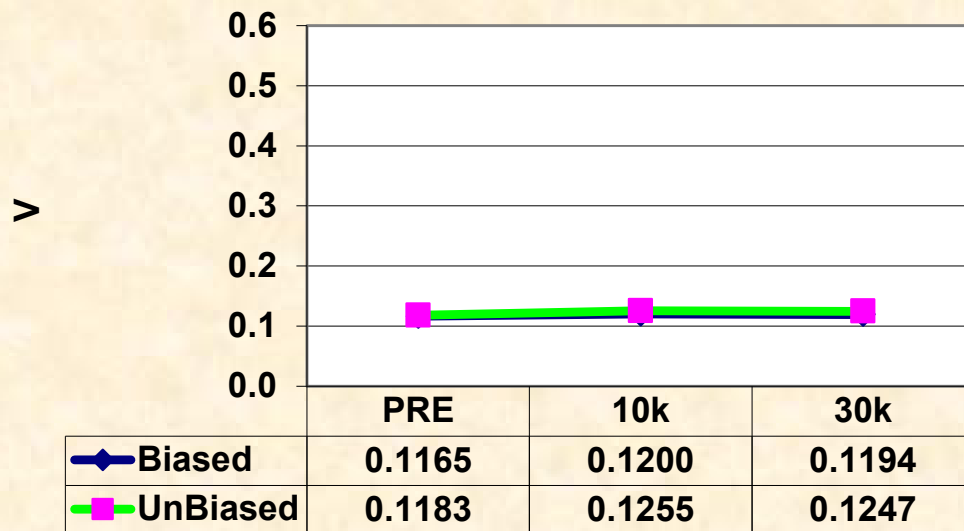


		VOH_EAout2@1mA 4.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	4.4350	4.4340	4.4346
Biased	104	4.4360	4.4360	4.4322
	105	4.4340	4.4330	4.4337
	112	4.4350	4.4320	4.4352
	114	4.4350	4.4350	4.4340
	Min	4.4340	4.4320	4.4322
	Max	4.4360	4.4360	4.4352
	Mean	4.4350	4.4340	4.4338
UnBiased	106	4.4350	4.4360	4.4352
	107	4.4320	4.4340	4.4331
	120	4.4350	4.4330	4.4361
	122	4.4360	4.4360	4.4361
	Min	4.4320	4.4330	4.4331
	Max	4.4360	4.4360	4.4361
	Mean	4.4345	4.4348	4.4351



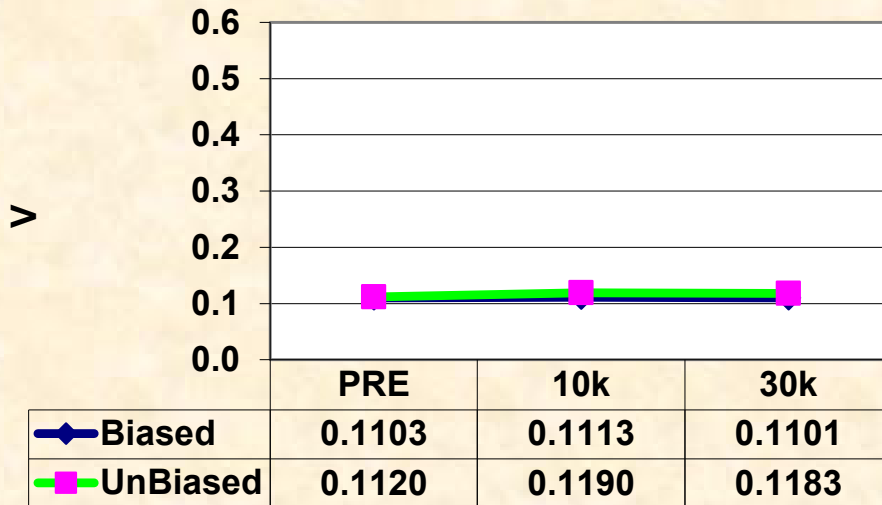
		VOL_EAout2@-1mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1170	0.1180	0.1155
Biased	104	0.1160	0.1190	0.1197
	105	0.1130	0.1160	0.1173
	112	0.1180	0.1220	0.1197
	114	0.1190	0.1230	0.1210
	Min	0.1130	0.1160	0.1173
	Max	0.1190	0.1230	0.1210
	Mean	0.1165	0.1200	0.1194
UnBiased	106	0.1160	0.1380	0.1368
	107	0.1200	0.1190	0.1194
	120	0.1190	0.1230	0.1210
	122	0.1180	0.1220	0.1216
	Min	0.1160	0.1190	0.1194
	Max	0.1200	0.1380	0.1368
	Mean	0.1183	0.1255	0.1247

EAout2_low@-1mA 5V

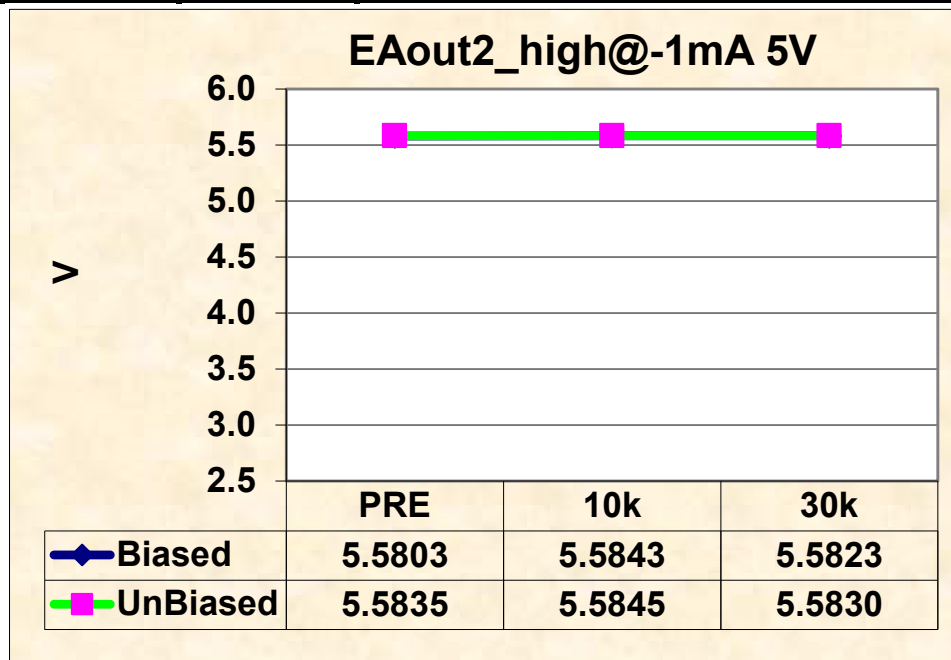


		VOL_EAout2@1mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1090	0.1080	0.1081
Biased	104	0.1090	0.1110	0.1094
	105	0.1090	0.1090	0.1060
	112	0.1110	0.1120	0.1118
	114	0.1120	0.1130	0.1130
	Min	0.1090	0.1090	0.1060
	Max	0.1120	0.1130	0.1130
	Mean	0.1103	0.1113	0.1101
UnBiased	106	0.1120	0.1400	0.1381
	107	0.1110	0.1110	0.1097
	120	0.1120	0.1120	0.1130
	122	0.1130	0.1130	0.1124
	Min	0.1110	0.1110	0.1097
	Max	0.1130	0.1400	0.1381
	Mean	0.1120	0.1190	0.1183

EAout2_low@1mA 5V

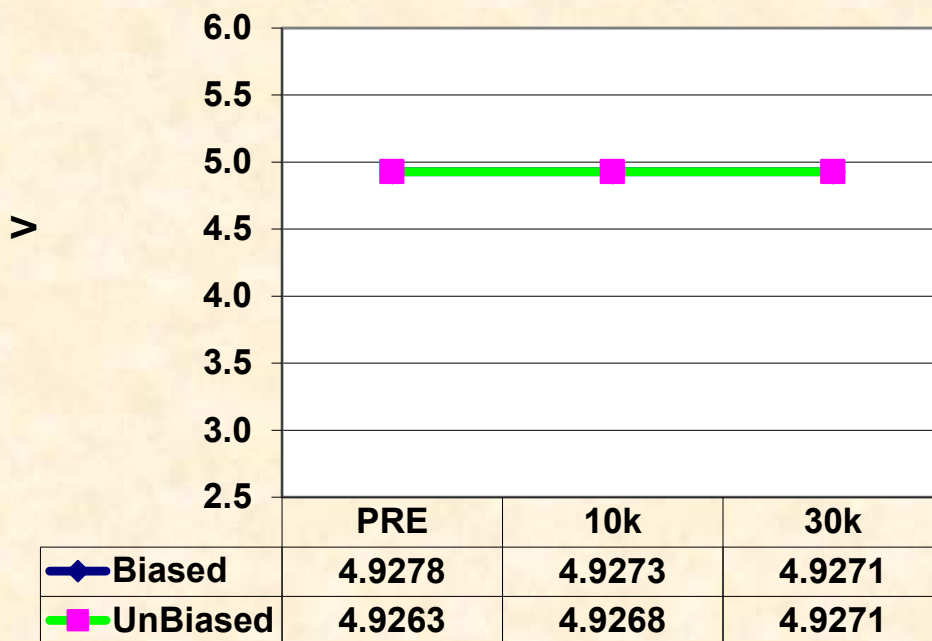


		VOH_EAout2@-1mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5880	5.5840	5.5798
Biased	104	5.5810	5.5850	5.5841
	105	5.5830	5.5920	5.5902
	112	5.5740	5.5780	5.5755
	114	5.5830	5.5820	5.5795
	Min	5.5740	5.5780	5.5755
	Max	5.5830	5.5920	5.5902
	Mean	5.5803	5.5843	5.5823
UnBiased	106	5.5890	5.5830	5.5822
	107	5.5800	5.5880	5.5841
	120	5.5800	5.5810	5.5798
	122	5.5850	5.5860	5.5859
	Min	5.5800	5.5810	5.5798
	Max	5.5890	5.5880	5.5859
	Mean	5.5835	5.5845	5.5830

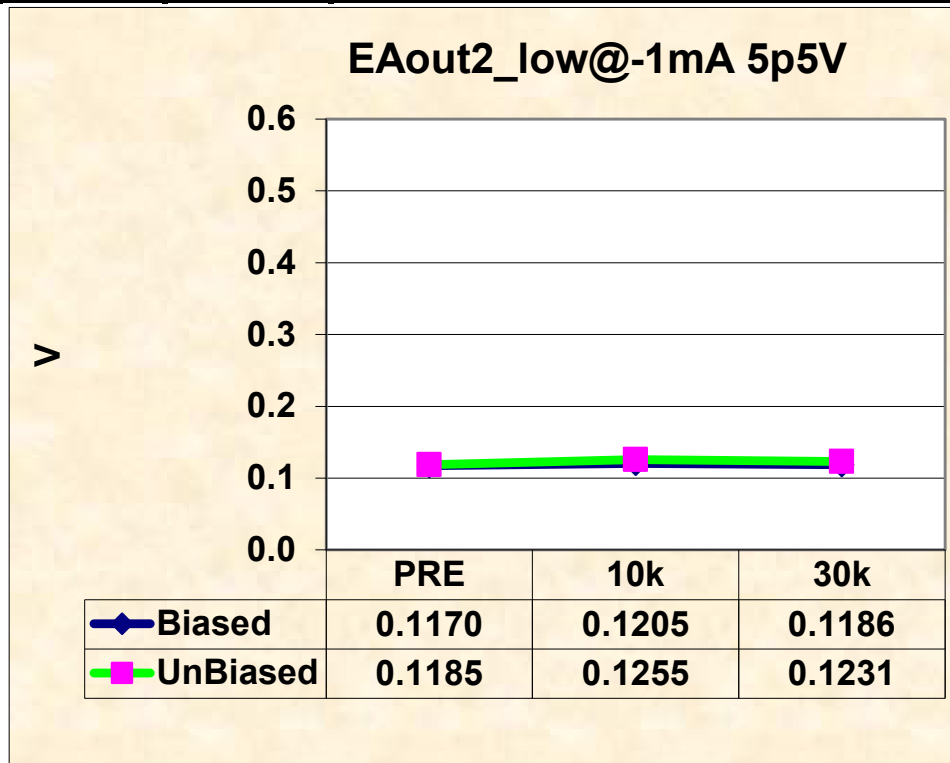


		VOH_EAout2@1mA 5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	4.9280	4.9250	4.9249
Biased	104	4.9290	4.9300	4.9307
	105	4.9270	4.9240	4.9255
	112	4.9270	4.9270	4.9261
	114	4.9280	4.9280	4.9261
	Min	4.9270	4.9240	4.9255
	Max	4.9290	4.9300	4.9307
	Mean	4.9278	4.9273	4.9271
UnBiased	106	4.9260	4.9260	4.9267
	107	4.9270	4.9280	4.9276
	120	4.9260	4.9270	4.9258
	122	4.9260	4.9260	4.9282
	Min	4.9260	4.9260	4.9258
	Max	4.9270	4.9280	4.9282
	Mean	4.9263	4.9268	4.9271

EAout2_high@1mA 5V

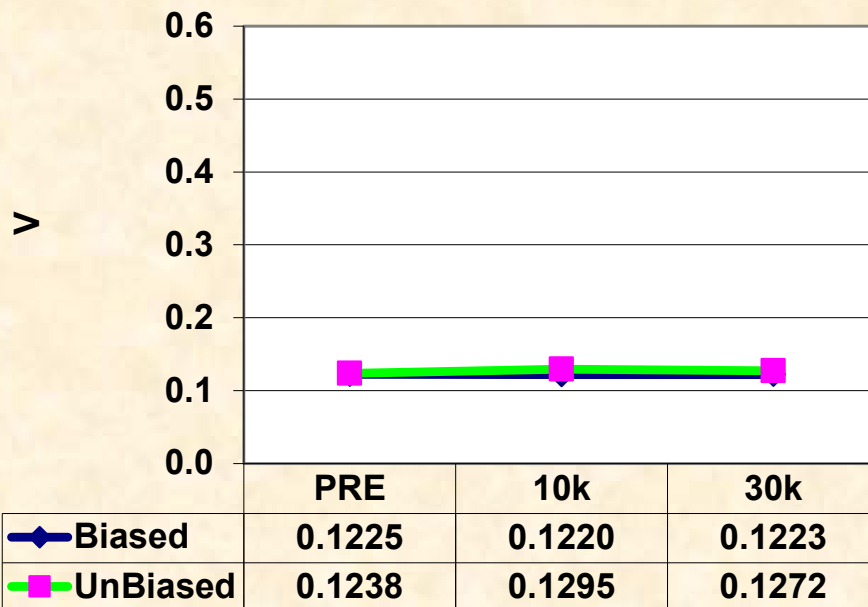


		VOL_EAout2@-1mA 5.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1180	0.1180	0.1158
Biased	104	0.1160	0.1200	0.1176
	105	0.1140	0.1160	0.1167
	112	0.1190	0.1240	0.1194
	114	0.1190	0.1220	0.1207
	Min	0.1140	0.1160	0.1167
	Max	0.1190	0.1240	0.1207
	Mean	0.1170	0.1205	0.1186
UnBiased	106	0.1160	0.1350	0.1320
	107	0.1180	0.1210	0.1176
	120	0.1200	0.1230	0.1216
	122	0.1200	0.1230	0.1213
	Min	0.1160	0.1210	0.1176
	Max	0.1200	0.1350	0.1320
	Mean	0.1185	0.1255	0.1231



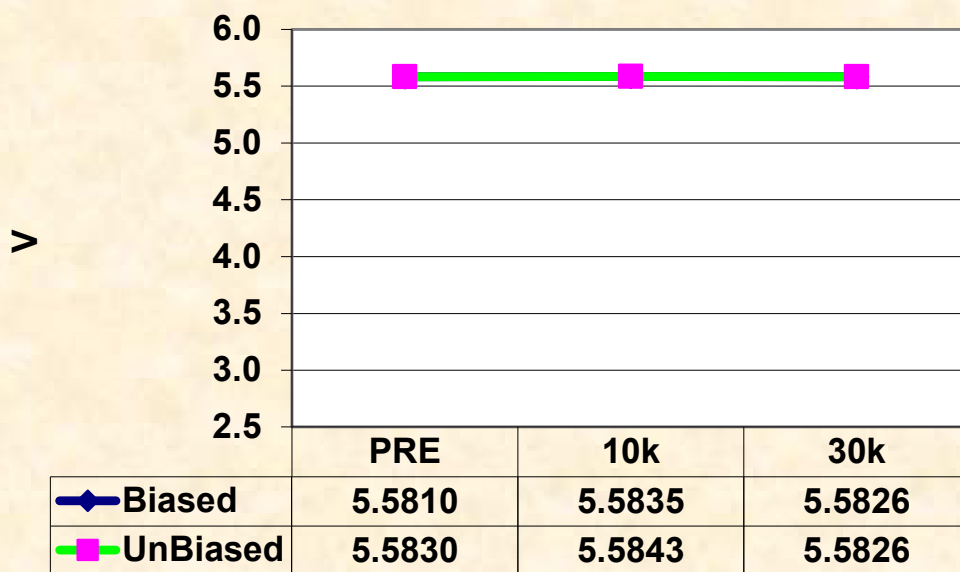
		VOL_EAout2@1mA 5.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1220	0.1170	0.1188
Biased	104	0.1220	0.1240	0.1213
	105	0.1210	0.1190	0.1203
	112	0.1220	0.1220	0.1237
	114	0.1250	0.1230	0.1237
	Min	0.1210	0.1190	0.1203
	Max	0.1250	0.1240	0.1237
	Mean	0.1225	0.1220	0.1223
UnBiased	106	0.1240	0.1450	0.1393
	107	0.1240	0.1230	0.1222
	120	0.1250	0.1240	0.1249
	122	0.1220	0.1260	0.1225
	Min	0.1220	0.1230	0.1222
	Max	0.1250	0.1450	0.1393
	Mean	0.1238	0.1295	0.1272

EAout2_low@1mA 5p5V

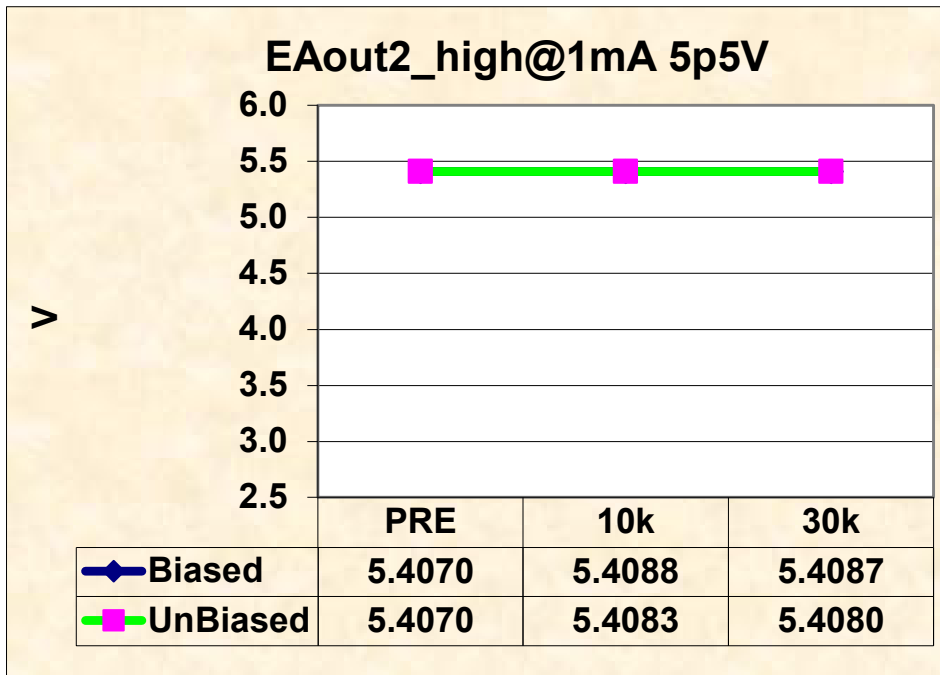


		VOH_EAout2@-1mA 5.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5860	5.5810	5.5789
Biased	104	5.5820	5.5860	5.5847
	105	5.5840	5.5910	5.5908
	112	5.5760	5.5760	5.5740
	114	5.5820	5.5810	5.5807
	Min	5.5760	5.5760	5.5740
	Max	5.5840	5.5910	5.5908
	Mean	5.5810	5.5835	5.5826
UnBiased	106	5.5880	5.5850	5.5819
	107	5.5810	5.5870	5.5828
	120	5.5780	5.5810	5.5807
	122	5.5850	5.5840	5.5850
	Min	5.5780	5.5810	5.5807
	Max	5.5880	5.5870	5.5850
	Mean	5.5830	5.5843	5.5826

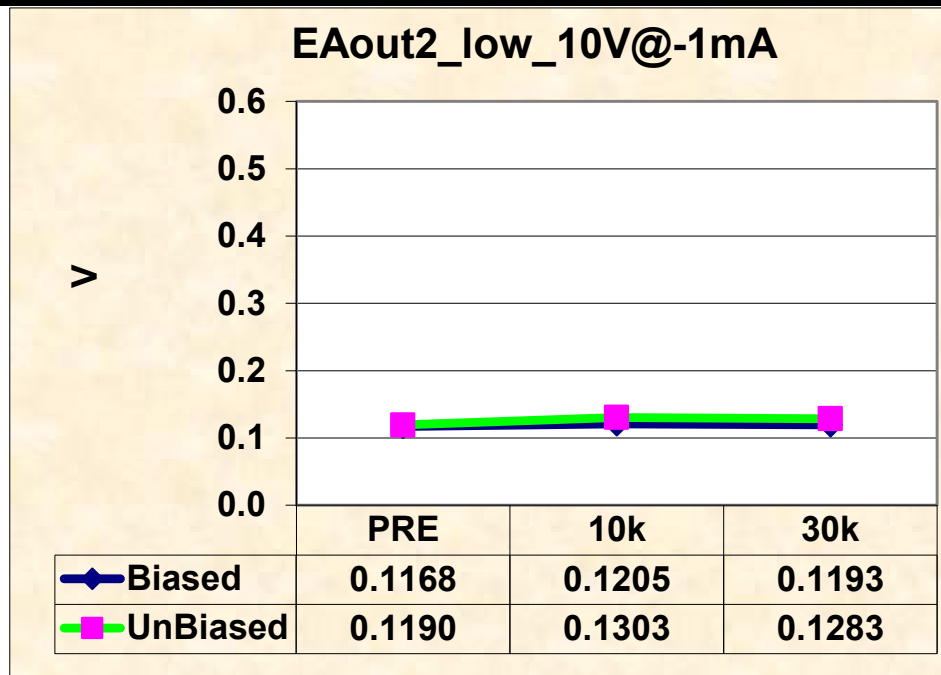
EAout2_high@-1mA 5p5V



		VOH_EAout2@1mA 5.5V (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.4110	5.4090	5.4084
Biased	104	5.4080	5.4100	5.4096
	105	5.4060	5.4110	5.4093
	112	5.4060	5.4070	5.4078
	114	5.4080	5.4070	5.4081
	Min	5.4060	5.4070	5.4078
	Max	5.4080	5.4110	5.4096
	Mean	5.4070	5.4088	5.4087
UnBiased	106	5.4070	5.4040	5.4044
	107	5.4040	5.4110	5.4112
	120	5.4070	5.4080	5.4078
	122	5.4100	5.4100	5.4084
	Min	5.4040	5.4040	5.4044
	Max	5.4100	5.4110	5.4112
	Mean	5.4070	5.4083	5.4080

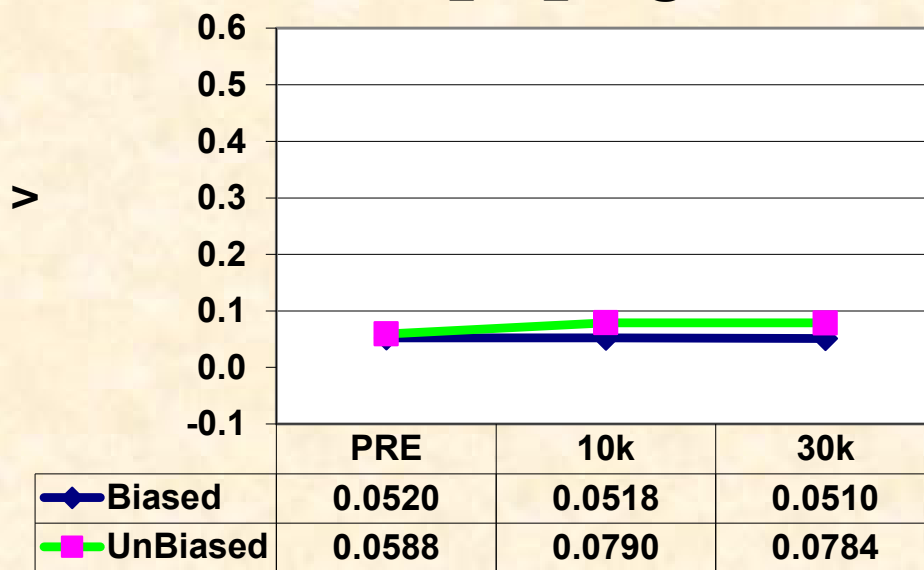


Wafer #	SN	VOL_EAout2_10V -1mA (V)		
		PRE	10k	30k
CTRL	99	0.1160	0.1160	0.1164
Biased	104	0.1160	0.1190	0.1207
	105	0.1150	0.1170	0.1170
	112	0.1180	0.1230	0.1203
	114	0.1180	0.1230	0.1191
	Min	0.1150	0.1170	0.1170
	Max	0.1180	0.1230	0.1207
	Mean	0.1168	0.1205	0.1193
UnBiased	106	0.1170	0.1520	0.1506
	107	0.1200	0.1210	0.1179
	120	0.1190	0.1250	0.1234
	122	0.1200	0.1230	0.1213
	Min	0.1170	0.1210	0.1179
	Max	0.1200	0.1520	0.1506
	Mean	0.1190	0.1303	0.1283

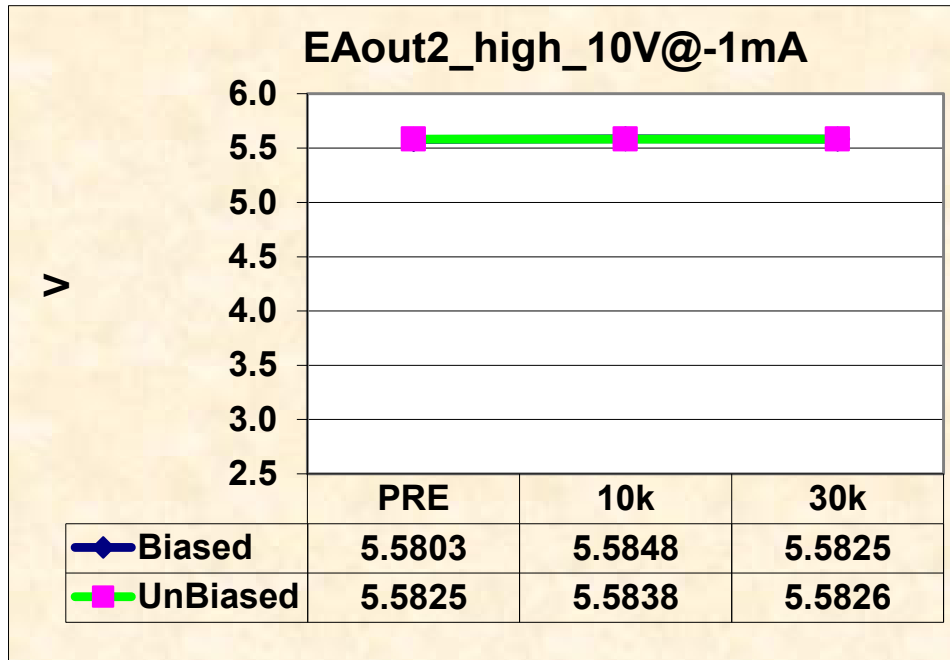


Wafer #	SN	VOL_EAout2_10V 1mA (V)		
		PRE	10k	30k
CTRL	99	0.0500	0.0510	0.0483
Biased	104	0.0520	0.0510	0.0498
	105	0.0490	0.0510	0.0510
	112	0.0540	0.0520	0.0516
	114	0.0530	0.0530	0.0516
	Min	0.0490	0.0510	0.0498
	Max	0.0540	0.0530	0.0516
	Mean	0.0520	0.0518	0.0510
UnBiased	106	0.0530	0.1480	0.1347
	107	0.0530	0.0510	0.0516
	120	0.0770	0.0660	0.0736
	122	0.0520	0.0510	0.0538
	Min	0.0520	0.0510	0.0516
	Max	0.0770	0.1480	0.1347
	Mean	0.0588	0.0790	0.0784

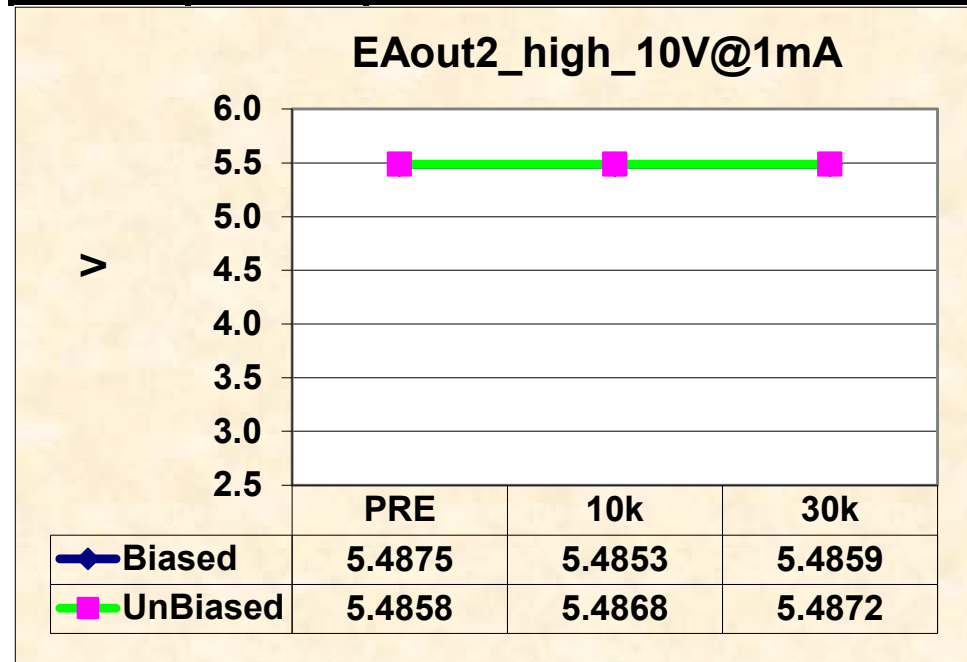
EAout2_low_10V@1mA



		VOH_EAout2_10V -1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5850	5.5840	5.5789
Biased	104	5.5820	5.5880	5.5853
	105	5.5830	5.5920	5.5908
	112	5.5770	5.5770	5.5743
	114	5.5790	5.5820	5.5795
	Min	5.5770	5.5770	5.5743
	Max	5.5830	5.5920	5.5908
	Mean	5.5803	5.5848	5.5825
UnBiased	106	5.5870	5.5830	5.5804
	107	5.5790	5.5860	5.5844
	120	5.5790	5.5790	5.5801
	122	5.5850	5.5870	5.5853
	Min	5.5790	5.5790	5.5801
	Max	5.5870	5.5870	5.5853
	Mean	5.5825	5.5838	5.5826

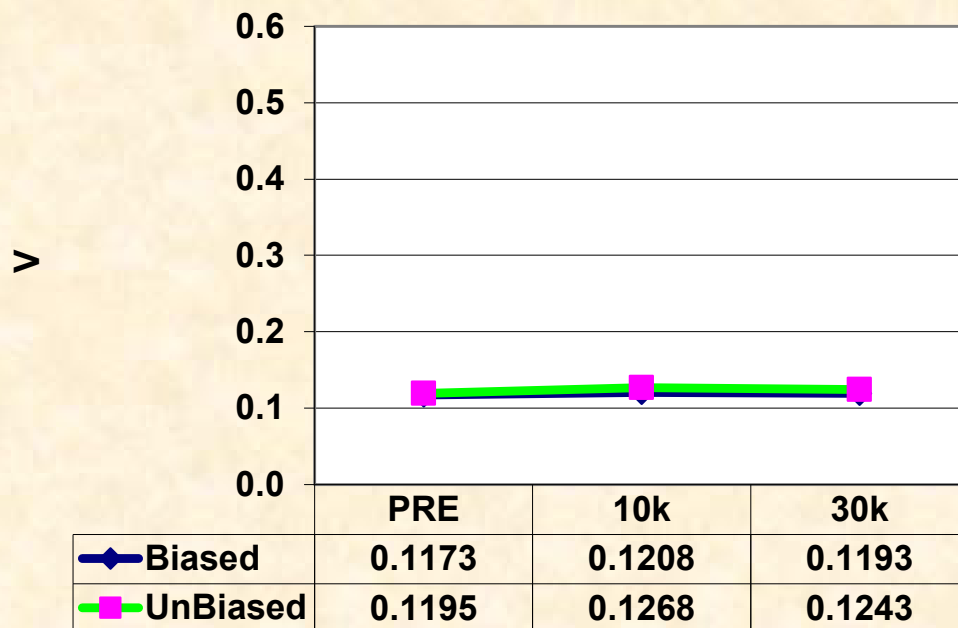


		VOH_EAout2_10V 1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.4980	5.4820	5.4811
Biased	104	5.4900	5.4920	5.4921
	105	5.4880	5.4820	5.4826
	112	5.4840	5.4810	5.4842
	114	5.4880	5.4860	5.4845
	Min	5.4840	5.4810	5.4826
	Max	5.4900	5.4920	5.4921
	Mean	5.4875	5.4853	5.4859
UnBiased	106	5.4950	5.4770	5.4799
	107	5.4670	5.4950	5.4952
	120	5.4850	5.4830	5.4823
	122	5.4960	5.4920	5.4915
	Min	5.4670	5.4770	5.4799
	Max	5.4960	5.4950	5.4952
	Mean	5.4858	5.4868	5.4872



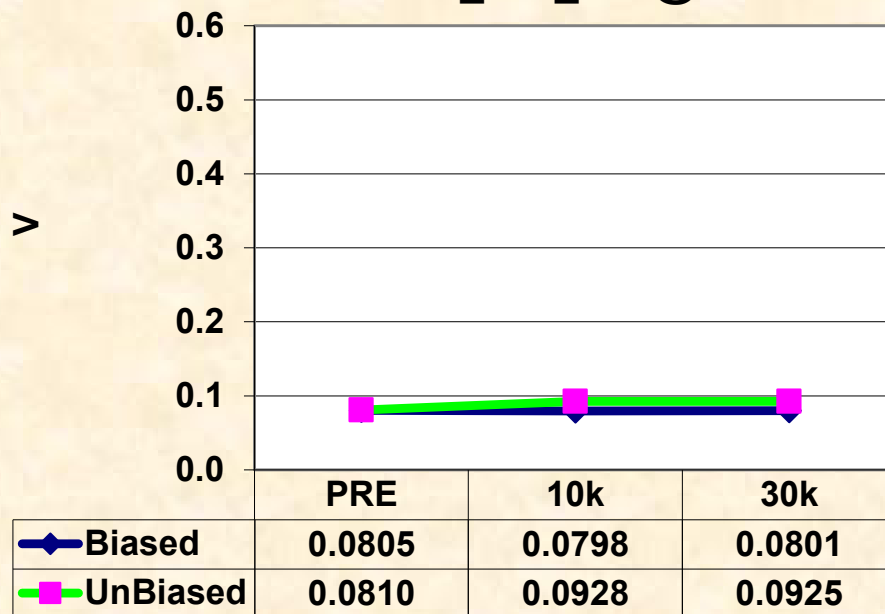
Wafer #	SN	VOL_EAout2_15V -1mA (V)		
		PRE	10k	30k
CTRL	99	0.1180	0.1170	0.1152
Biased	104	0.1170	0.1200	0.1194
	105	0.1150	0.1170	0.1167
	112	0.1190	0.1220	0.1216
	114	0.1180	0.1240	0.1194
	Min	0.1150	0.1170	0.1167
	Max	0.1190	0.1240	0.1216
	Mean	0.1173	0.1208	0.1193
UnBiased	106	0.1180	0.1400	0.1341
	107	0.1190	0.1210	0.1188
	120	0.1220	0.1240	0.1225
	122	0.1190	0.1220	0.1219
	Min	0.1180	0.1210	0.1188
	Max	0.1220	0.1400	0.1341
	Mean	0.1195	0.1268	0.1243

EAout2_low_15V@-1mA

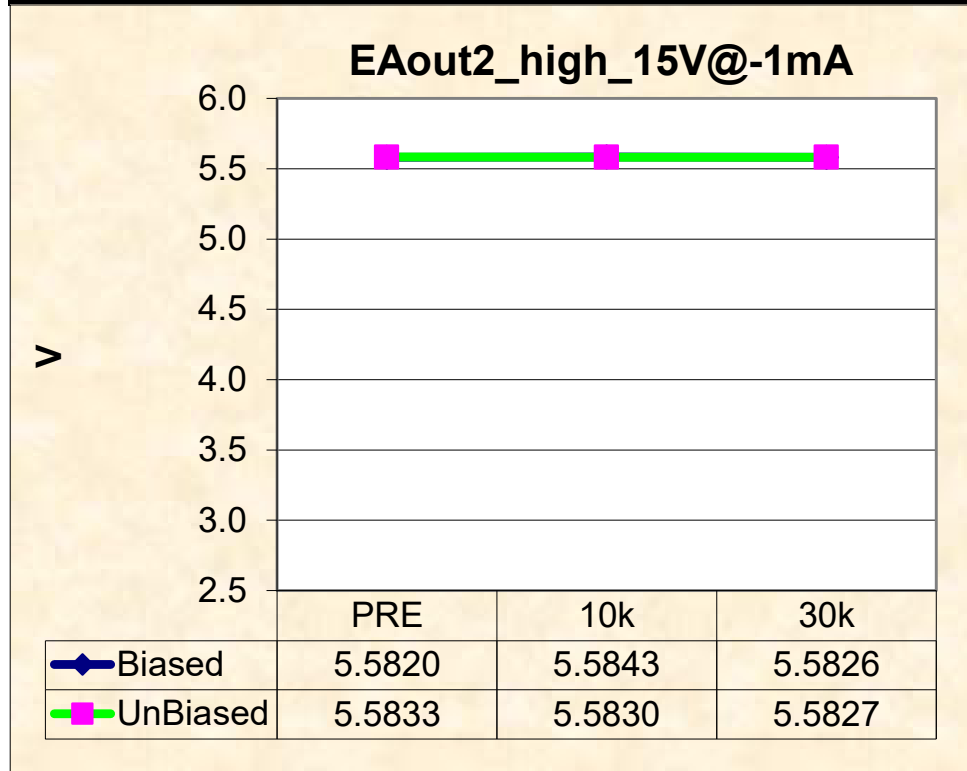


		VOL_EAout2_15V 1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.0800	0.0790	0.0788
Biased	104	0.0790	0.0800	0.0791
	105	0.0790	0.0780	0.0791
	112	0.0820	0.0810	0.0816
	114	0.0820	0.0800	0.0806
	Min	0.0790	0.0780	0.0791
	Max	0.0820	0.0810	0.0816
	Mean	0.0805	0.0798	0.0801
UnBiased	106	0.0800	0.1260	0.1252
	107	0.0820	0.0820	0.0809
	120	0.0820	0.0830	0.0822
	122	0.0800	0.0800	0.0816
	Min	0.0800	0.0800	0.0809
	Max	0.0820	0.1260	0.1252
	Mean	0.0810	0.0928	0.0925

EAout2_low_15V@1mA

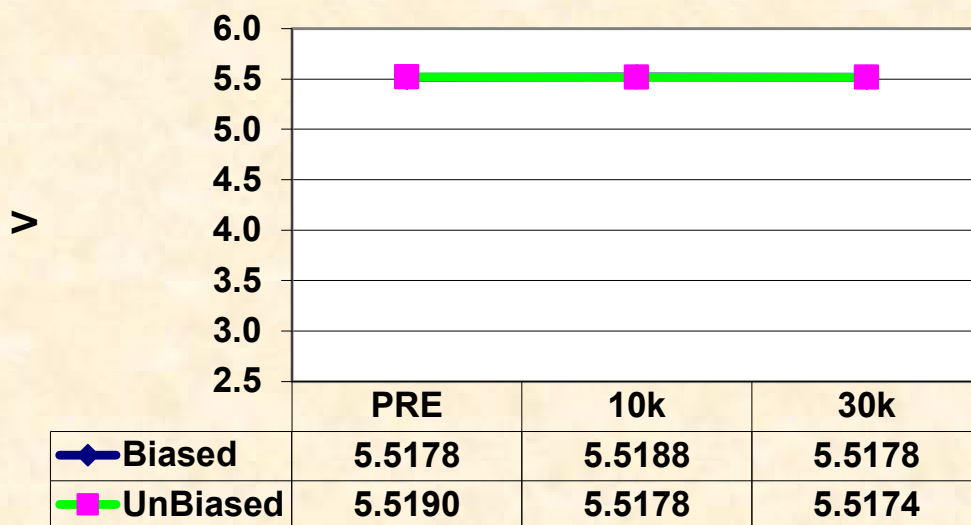


		VOH_EAout2_15V -1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5870	5.5830	5.5807
Biased	104	5.5820	5.5850	5.5871
	105	5.5850	5.5910	5.5911
	112	5.5780	5.5780	5.5731
	114	5.5830	5.5830	5.5789
	Min	5.5780	5.5780	5.5731
	Max	5.5850	5.5910	5.5911
	Mean	5.5820	5.5843	5.5826
UnBiased	106	5.5870	5.5830	5.5801
	107	5.5810	5.5860	5.5868
	120	5.5790	5.5780	5.5792
	122	5.5860	5.5850	5.5847
	Min	5.5790	5.5780	5.5792
	Max	5.5870	5.5860	5.5868
	Mean	5.5833	5.5830	5.5827

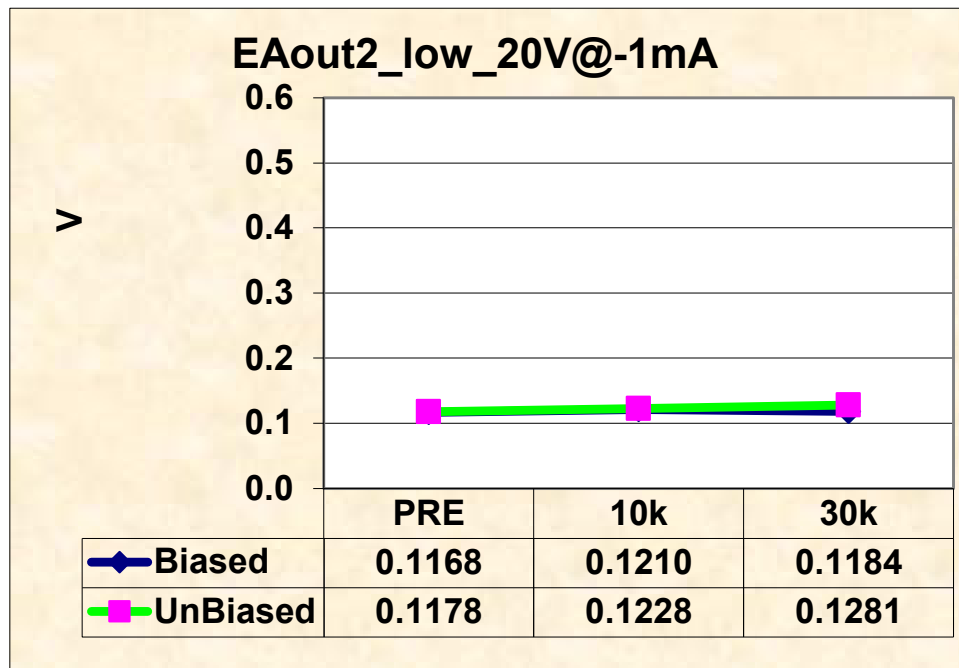


		VOH_EAout2_15V 1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5260	5.5170	5.5147
Biased	104	5.5190	5.5220	5.5214
	105	5.5210	5.5270	5.5242
	112	5.5140	5.5100	5.5107
	114	5.5170	5.5160	5.5147
	Min	5.5140	5.5100	5.5107
	Max	5.5210	5.5270	5.5242
	Mean	5.5178	5.5188	5.5178
UnBiased	106	5.5270	5.5150	5.5165
	107	5.5110	5.5230	5.5214
	120	5.5170	5.5140	5.5147
	122	5.5210	5.5190	5.5168
	Min	5.5110	5.5140	5.5147
	Max	5.5270	5.5230	5.5214
	Mean	5.5190	5.5178	5.5174

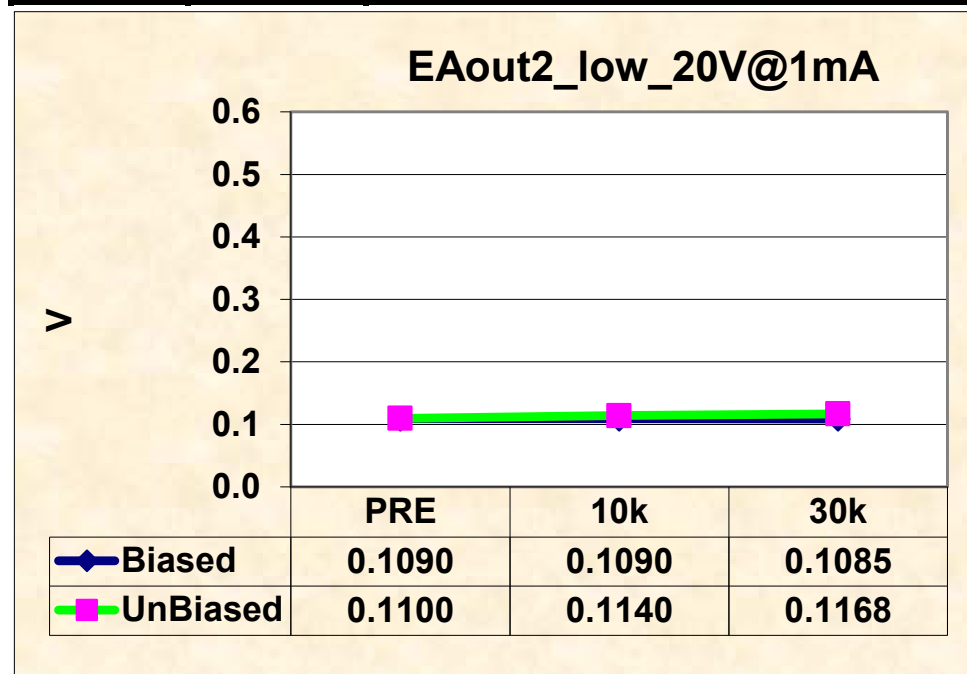
EAout2_high_15V@1mA



		VOL_EAout2_20V -1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1180	0.1180	0.1164
Biased	104	0.1150	0.1220	0.1188
	105	0.1140	0.1180	0.1164
	112	0.1200	0.1220	0.1194
	114	0.1180	0.1220	0.1188
	Min	0.1140	0.1180	0.1164
	Max	0.1200	0.1220	0.1194
	Mean	0.1168	0.1210	0.1184
UnBiased	106	0.1150	0.1300	0.1515
	107	0.1190	0.1180	0.1182
	120	0.1200	0.1220	0.1210
	122	0.1170	0.1210	0.1216
	Min	0.1150	0.1180	0.1182
	Max	0.1200	0.1300	0.1515
	Mean	0.1178	0.1228	0.1281

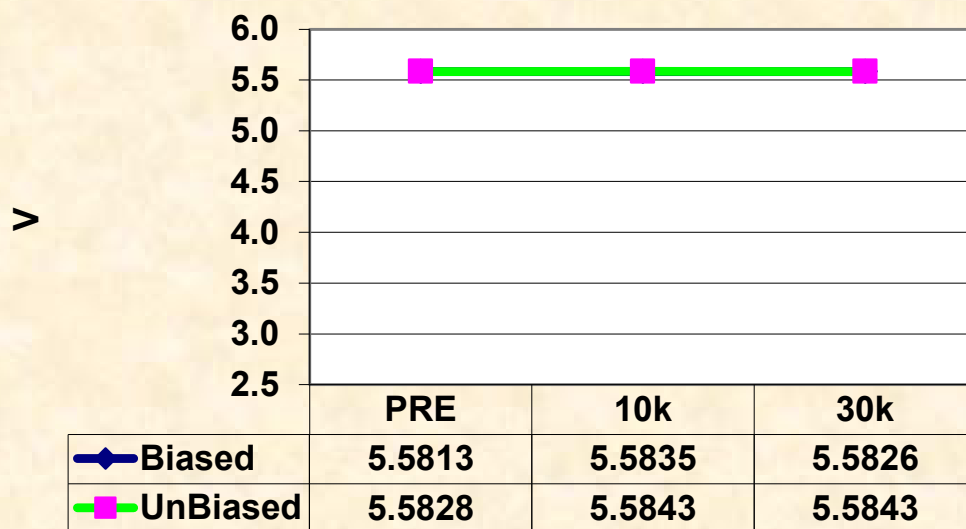


Wafer #	SN	VOL_EAout2_20V 1mA (V)		
		PRE	10k	30k
CTRL	99	0.1090	0.1070	0.1075
Biased	104	0.1070	0.1080	0.1069
	105	0.1070	0.1070	0.1066
	112	0.1110	0.1100	0.1094
	114	0.1110	0.1110	0.1112
	Min	0.1070	0.1070	0.1066
	Max	0.1110	0.1110	0.1112
	Mean	0.1090	0.1090	0.1085
UnBiased	106	0.1100	0.1280	0.1399
	107	0.1100	0.1070	0.1087
	120	0.1110	0.1120	0.1087
	122	0.1090	0.1090	0.1100
	Min	0.1090	0.1070	0.1087
	Max	0.1110	0.1280	0.1399
	Mean	0.1100	0.1140	0.1168



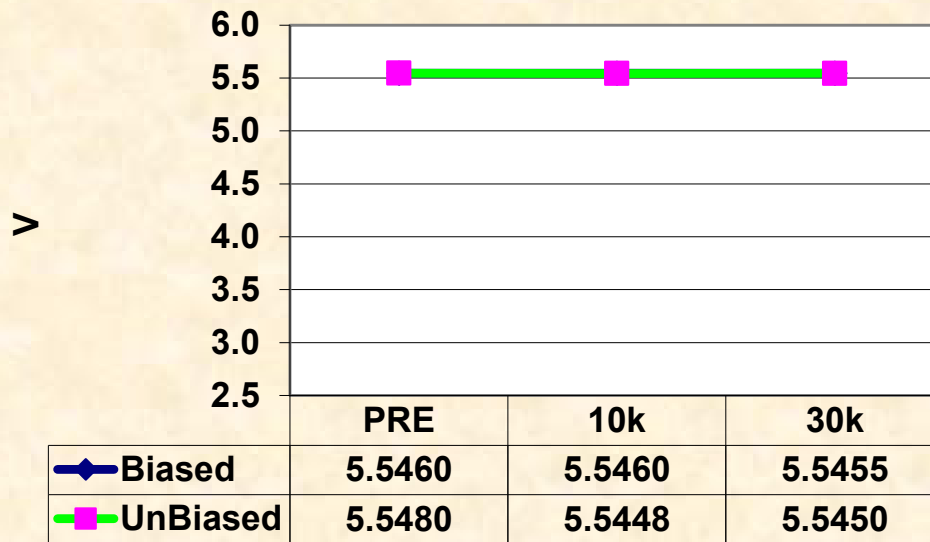
		VOH_EAout2_20V -1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5860	5.5840	5.5770
Biased	104	5.5810	5.5850	5.5859
	105	5.5860	5.5910	5.5895
	112	5.5780	5.5790	5.5752
	114	5.5800	5.5790	5.5798
	Min	5.5780	5.5790	5.5752
	Max	5.5860	5.5910	5.5895
	Mean	5.5813	5.5835	5.5826
UnBiased	106	5.5860	5.5830	5.5831
	107	5.5810	5.5870	5.5868
	120	5.5800	5.5810	5.5813
	122	5.5840	5.5860	5.5859
	Min	5.5800	5.5810	5.5813
	Max	5.5860	5.5870	5.5868
	Mean	5.5828	5.5843	5.5843

EAout2_high_20V@-1mA

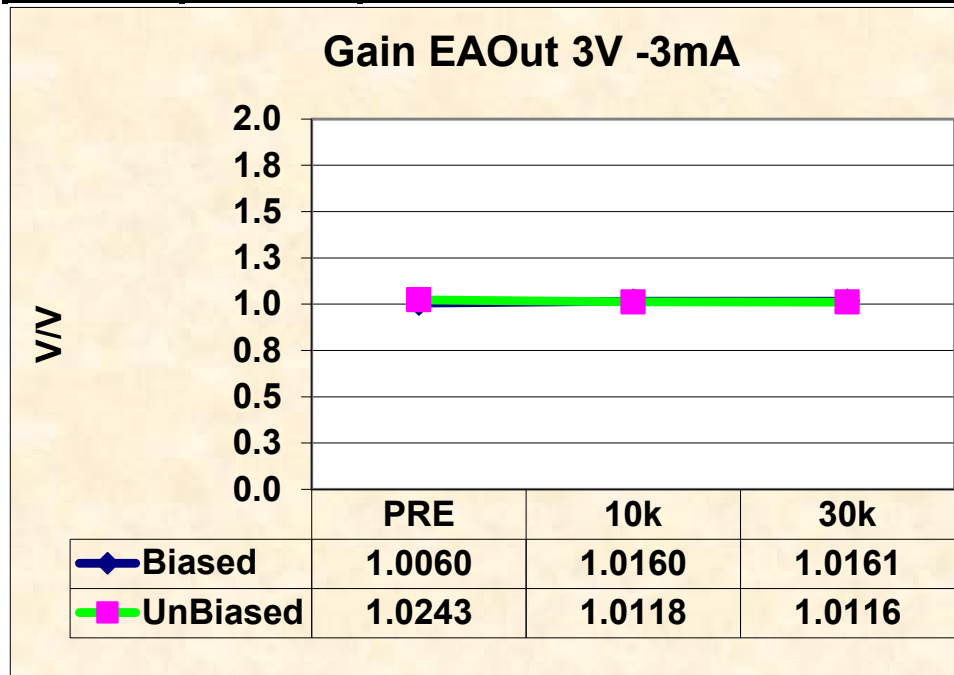


		VOH_EAout2_20V 1mA (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	5.5510	5.5430	5.5437
Biased	104	5.5470	5.5490	5.5474
	105	5.5480	5.5540	5.5529
	112	5.5430	5.5390	5.5404
	114	5.5460	5.5420	5.5413
	Min	5.5430	5.5390	5.5404
	Max	5.5480	5.5540	5.5529
	Mean	5.5460	5.5460	5.5455
UnBiased	106	5.5530	5.5420	5.5425
	107	5.5440	5.5480	5.5477
	120	5.5450	5.5410	5.5419
	122	5.5500	5.5480	5.5480
	Min	5.5440	5.5410	5.5419
	Max	5.5530	5.5480	5.5480
	Mean	5.5480	5.5448	5.5450

EAout2_high_20V@1mA

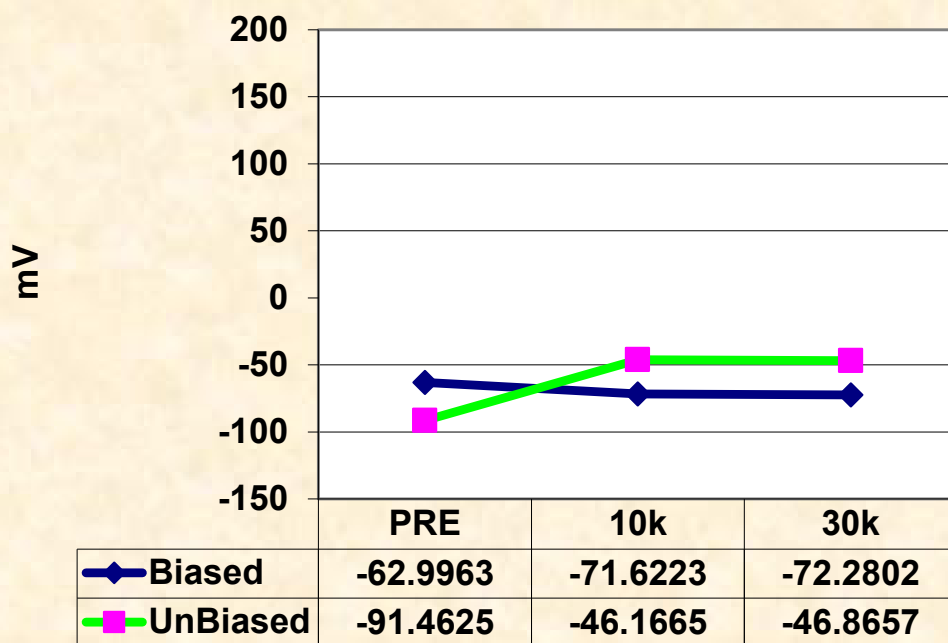


Wafer #	SN	Gain_EAout_3V -3mA (V/V)		
		PRE	10k	30k
CTRL	99	1.0488	0.9861	0.9867
Biased	104	1.0154	1.0358	1.0357
	105	0.9868	1.0061	1.0046
	112	1.0277	1.0271	1.0279
	114	0.9943	0.9951	0.9961
	Min	0.9868	0.9951	0.9961
	Max	1.0277	1.0358	1.0357
	Mean	1.0060	1.0160	1.0161
UnBiased	106	1.0322	0.9776	0.9769
	107	1.0454	1.0500	1.0489
	120	0.9753	0.9751	0.9758
	122	1.0442	1.0444	1.0447
	Min	0.9753	0.9751	0.9758
	Max	1.0454	1.0500	1.0489
	Mean	1.0243	1.0118	1.0116

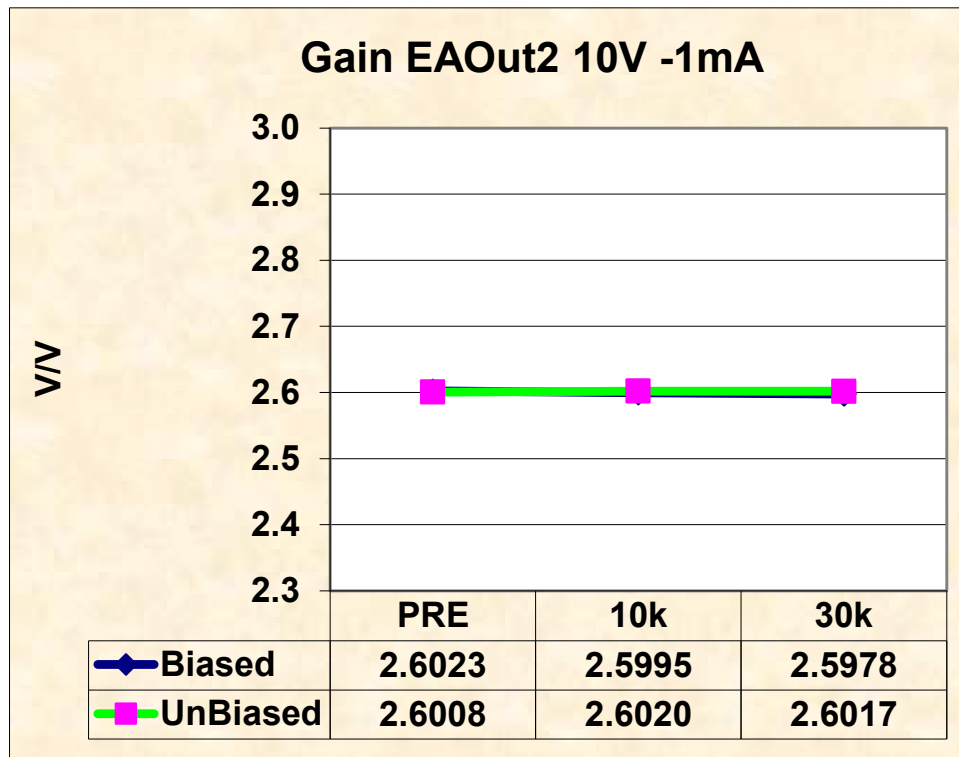


		Offset_EAout_3V_-3mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-94.1330	-10.0210	-10.5377
Biased	104	-73.7090	-92.2180	-92.2010
	105	-11.0830	-24.9080	-25.2847
	112	-104.3250	-105.1320	-105.3936
	114	-62.8680	-64.2310	-66.2414
	Min	-104.3250	-105.1320	-105.3936
	Max	-11.0830	-24.9080	-25.2847
	Mean	-62.9963	-71.6223	-72.2802
UnBiased	106	-110.4930	36.7960	35.9807
	107	-128.4520	-94.5170	-94.0444
	120	5.5490	5.6310	4.7297
	122	-132.4540	-132.5760	-134.1288
	Min	-132.4540	-132.5760	-134.1288
	Max	5.5490	36.7960	35.9807
	Mean	-91.4625	-46.1665	-46.8657

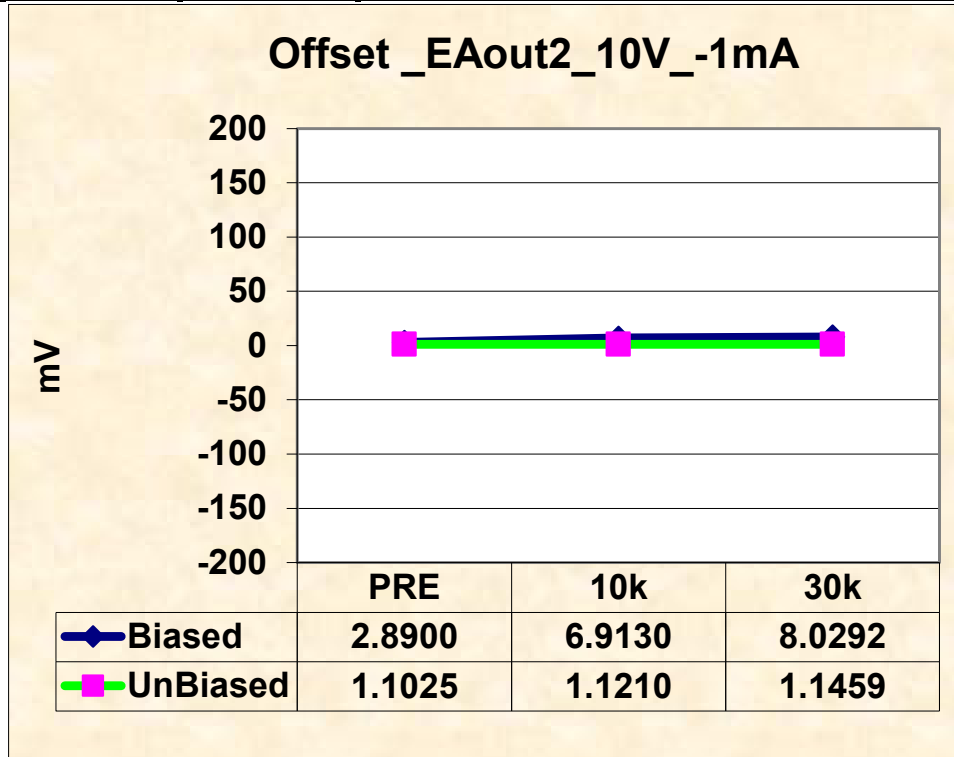
Offset_EAout_3V_-3mA



		Gain_EAout2_10V -1mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.5980	2.6050	2.6036
Biased	104	2.6020	2.5980	2.5969
	105	2.6050	2.5990	2.6005
	112	2.6030	2.6030	2.5992
	114	2.5990	2.5980	2.5945
	Min	2.5990	2.5980	2.5945
	Max	2.6050	2.6030	2.6005
	Mean	2.6023	2.5995	2.5978
UnBiased	106	2.6050	2.6040	2.6015
	107	2.5930	2.5990	2.6015
	120	2.6080	2.6080	2.6074
	122	2.5970	2.5970	2.5965
	Min	2.5930	2.5970	2.5965
	Max	2.6080	2.6080	2.6074
	Mean	2.6008	2.6020	2.6017

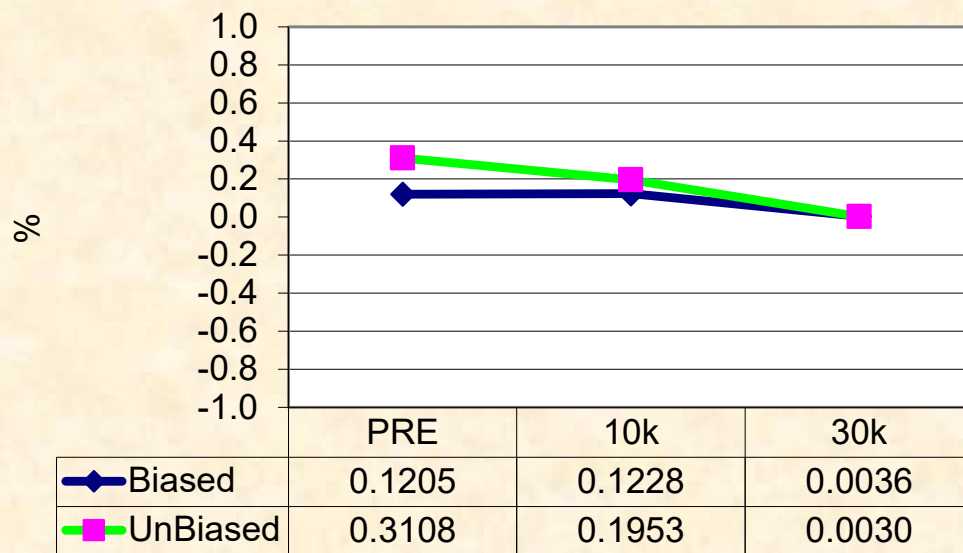


		Offset_EAout2_10V_-1mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	6.4570	-1.2640	-312.5961
Biased	104	3.4830	6.1480	6.9696
	105	0.4870	12.4230	11.9108
	112	9.2480	10.8810	12.2207
	114	-1.6580	-1.8000	1.0155
	Min	-1.6580	-1.8000	1.0155
	Max	9.2480	12.4230	12.2207
	Mean	2.8900	6.9130	8.0292
UnBiased	106	6.8510	-9.3330	-6.6439
	107	-11.4920	6.3200	1.7927
	120	7.3450	5.9990	7.5750
	122	1.7060	1.4980	1.8598
	Min	-11.4920	-9.3330	-6.6439
	Max	7.3450	6.3200	7.5750
	Mean	1.1025	1.1210	1.1459



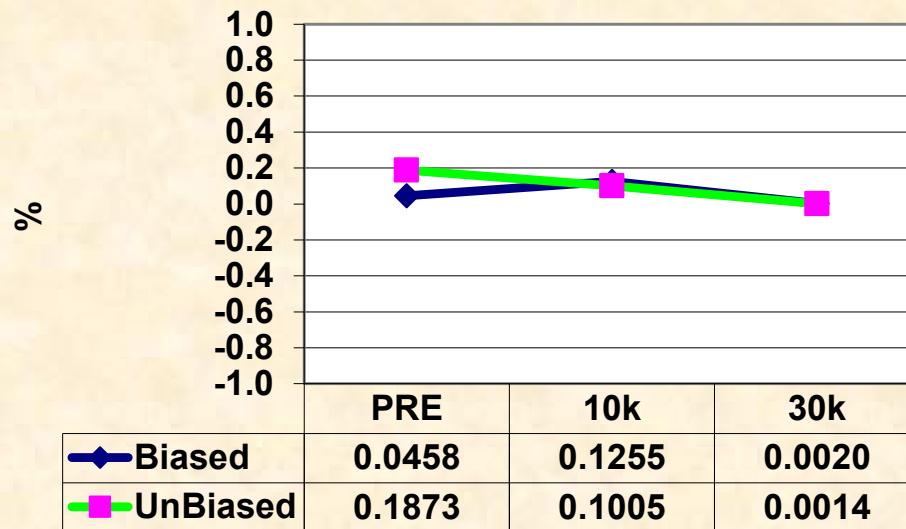
Wafer #	SN	Lin_error_EAout 3V -3mA (%)		
		PRE	10k	30k
CTRL	99	0.2470	0.3300	0.0017
Biased	104	0.1380	0.2490	0.0036
	105	0.0790	0.0920	0.0034
	112	0.0980	0.0940	0.0044
	114	0.1670	0.0560	0.0029
	Min	0.0790	0.0560	0.0029
	Max	0.1670	0.2490	0.0044
	Mean	0.1205	0.1228	0.0036
UnBiased	106	0.4560	0.1850	0.0046
	107	0.3890	0.1220	0.0024
	120	0.0810	0.2920	0.0020
	122	0.3170	0.1820	0.0029
	Min	0.0810	0.1220	0.0020
	Max	0.4560	0.2920	0.0046
	Mean	0.3108	0.1953	0.0030

Lin_error_EAout_3V_-3mA

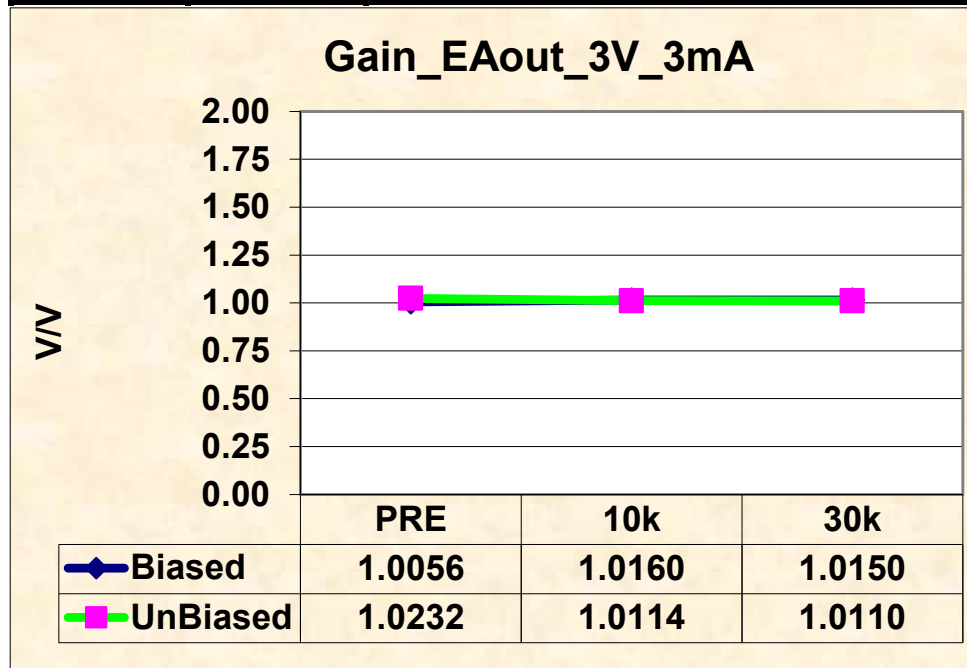


Wafer #	SN	Lin_error_EAout2 10V -1mA (%)		
		PRE	10k	30k
CTRL	99	0.1190	0.1210	0.0007
Biased	104	0.0410	0.1110	0.0017
	105	0.0840	0.1720	0.0018
	112	0.0310	0.0490	0.0038
	114	0.0270	0.1700	0.0008
	Min	0.0270	0.0490	0.0008
	Max	0.0840	0.1720	0.0038
	Mean	0.0458	0.1255	0.0020
UnBiased	106	0.3520	0.1090	0.0025
	107	0.1840	0.0490	0.0008
	120	0.1330	0.1050	0.0008
	122	0.0800	0.1390	0.0013
	Min	0.0800	0.0490	0.0008
	Max	0.3520	0.1390	0.0025
	Mean	0.1873	0.1005	0.0014

Lin_error_EAout2_10V_-1mA

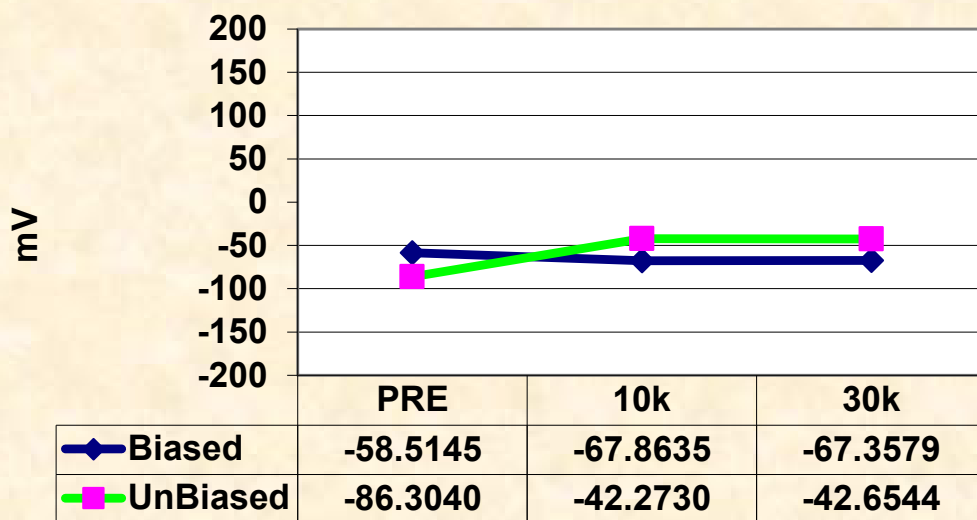


		Gain_EAout_3V_3mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	1.0481	0.9854	0.9857
Biased	104	1.0143	1.0360	1.0352
	105	0.9857	1.0065	1.0048
	112	1.0274	1.0275	1.0264
	114	0.9950	0.9943	0.9938
	Min	0.9857	0.9943	0.9938
	Max	1.0274	1.0360	1.0352
	Mean	1.0056	1.0160	1.0150
UnBiased	106	1.0308	0.9772	0.9758
	107	1.0449	1.0501	1.0491
	120	0.9736	0.9751	0.9745
	122	1.0436	1.0432	1.0448
	Min	0.9736	0.9751	0.9745
	Max	1.0449	1.0501	1.0491
	Mean	1.0232	1.0114	1.0110

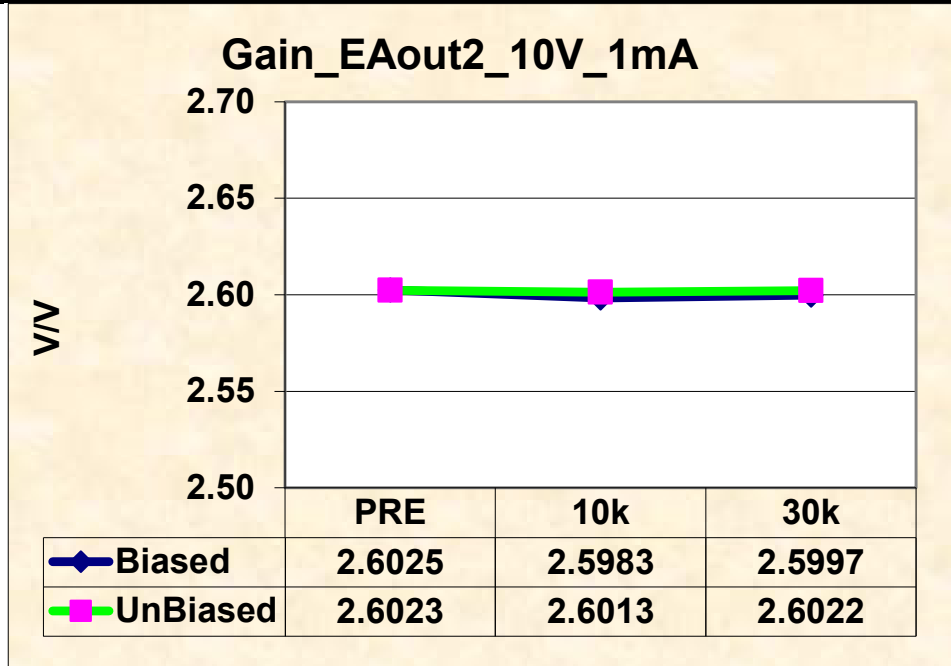


		Offset_EAout_3V 3mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-89.8040	-5.2330	-5.9814
Biased	104	-68.7560	-88.3970	-87.6846
	105	-5.4360	-21.8440	-21.8821
	112	-100.4890	-101.0370	-100.2874
	114	-59.3770	-60.1760	-59.5775
	Min	-100.4890	-101.0370	-100.2874
	Max	-5.4360	-21.8440	-21.8821
	Mean	-58.5145	-67.8635	-67.3579
UnBiased	106	-104.4780	40.7770	40.9635
	107	-124.0380	-91.1520	-90.8676
	120	10.8080	8.9060	9.5894
	122	-127.5080	-127.6230	-130.3027
	Min	-127.5080	-127.6230	-130.3027
	Max	10.8080	40.7770	40.9635
	Mean	-86.3040	-42.2730	-42.6544

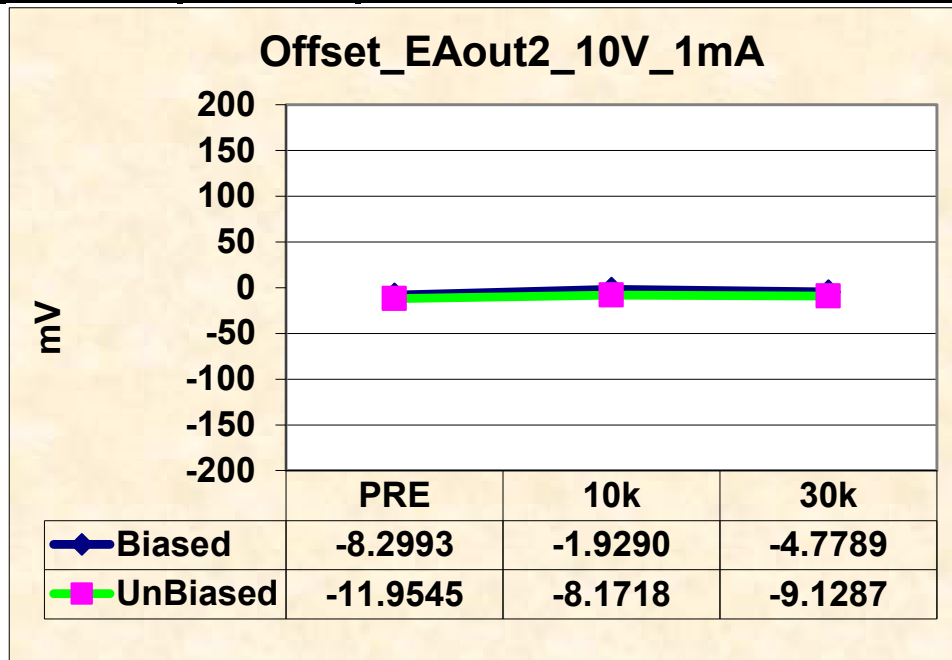
Offset_EAout_3V_3mA



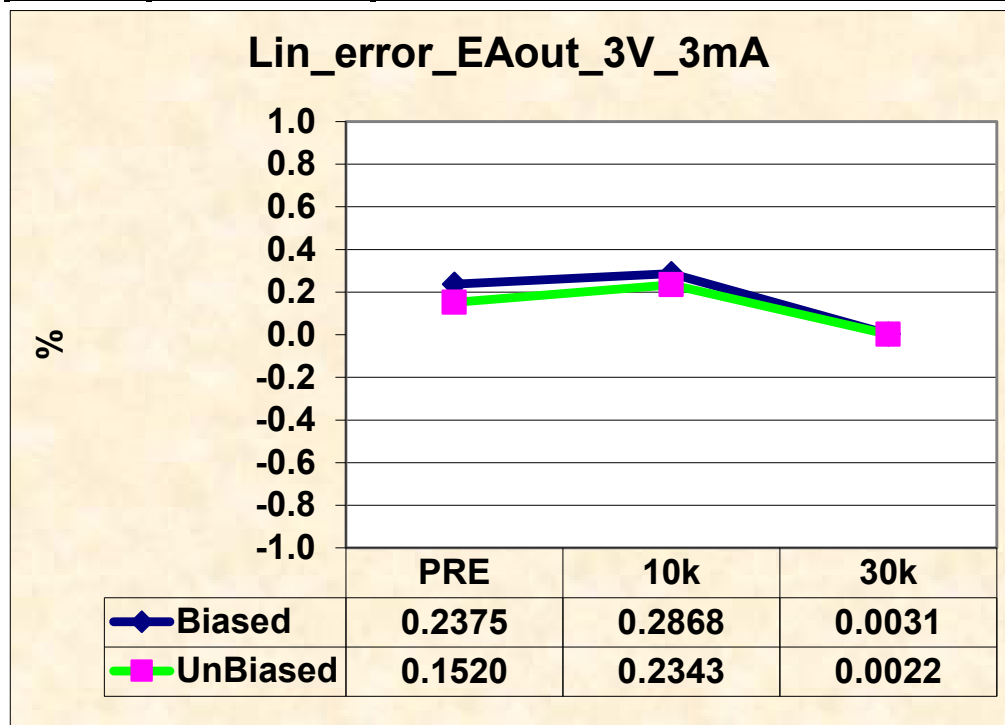
		Gain_EAout2_10V 1mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.5990	2.6070	2.6060
Biased	104	2.6050	2.5960	2.5974
	105	2.6070	2.5970	2.5983
	112	2.6010	2.6000	2.6034
	114	2.5970	2.6000	2.5998
	Min	2.5970	2.5960	2.5974
	Max	2.6070	2.6000	2.6034
	Mean	2.6025	2.5983	2.5997
UnBiased	106	2.6070	2.6030	2.6036
	107	2.5940	2.5980	2.5997
	120	2.6110	2.6070	2.6092
	122	2.5970	2.5970	2.5961
	Min	2.5940	2.5970	2.5961
	Max	2.6110	2.6070	2.6092
	Mean	2.6023	2.6013	2.6022



		Offset_EAout2_10V 1mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-3.3340	-14.4830	-13.3467
Biased	104	-10.1510	-2.5290	-4.5640
	105	-14.9500	5.4600	3.0560
	112	1.9560	1.6090	-1.5749
	114	-10.0520	-12.2560	-16.0328
	Min	-14.9500	-12.2560	-16.0328
	Max	1.9560	5.4600	3.0560
	Mean	-8.2993	-1.9290	-4.7789
UnBiased	106	-8.2490	-18.4170	-18.1658
	107	-22.9530	-3.4510	-6.4506
	120	-6.6500	-1.7100	-3.9743
	122	-9.9660	-9.1090	-7.9241
	Min	-22.9530	-18.4170	-18.1658
	Max	-6.6500	-1.7100	-3.9743
	Mean	-11.9545	-8.1718	-9.1287

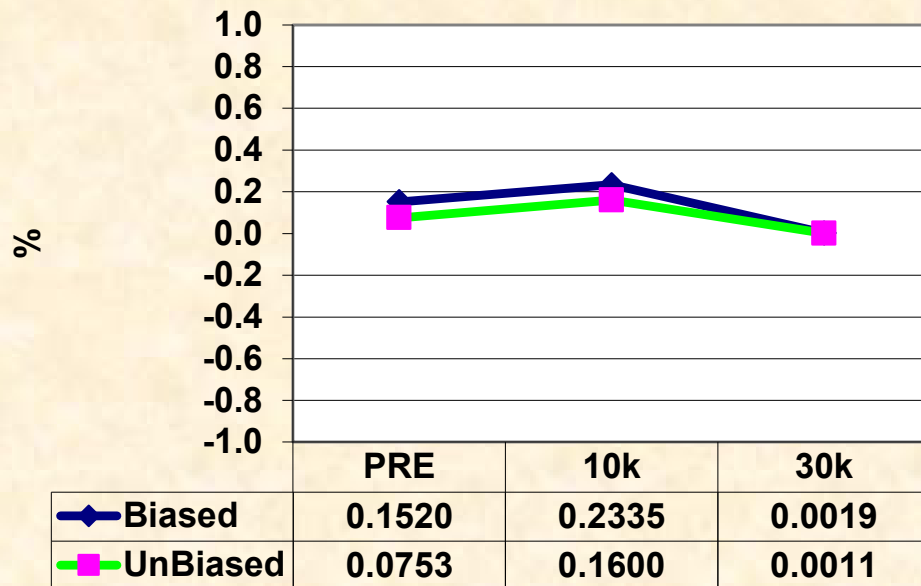


Wafer #	SN	Lin_error_EAout 3V 3mA (%)		
		PRE	10k	30k
CTRL	99	0.3680	0.1270	0.0007
Biased	104	0.1170	0.6580	0.0026
	105	0.2850	0.0790	0.0048
	112	0.1300	0.2540	0.0027
	114	0.4180	0.1560	0.0024
	Min	0.1170	0.0790	0.0024
	Max	0.4180	0.6580	0.0048
	Mean	0.2375	0.2868	0.0031
UnBiased	106	0.1680	0.2960	0.0019
	107	0.1350	0.3640	0.0042
	120	0.1660	0.1640	0.0017
	122	0.1390	0.1130	0.0009
	Min	0.1350	0.1130	0.0009
	Max	0.1680	0.3640	0.0042
	Mean	0.1520	0.2343	0.0022

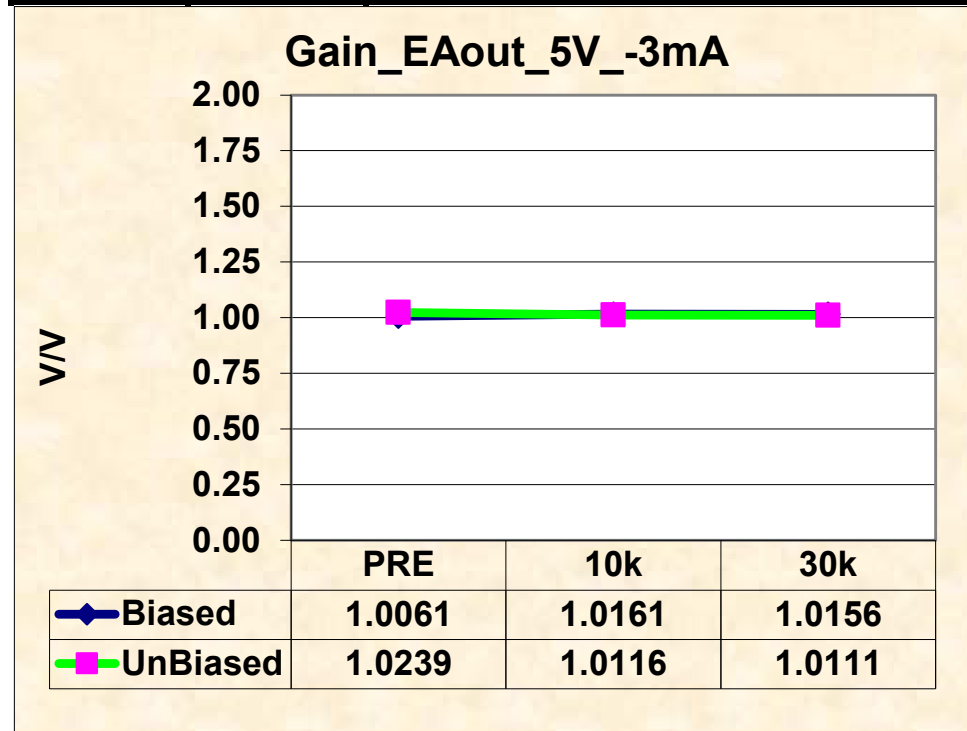


		Lin_error_EAout2 10V 1mA (%)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.2170	0.2200	0.0010
Biased	104	0.0940	0.5130	0.0013
	105	0.1180	0.1150	0.0026
	112	0.1570	0.1890	0.0025
	114	0.2390	0.1170	0.0010
	Min	0.0940	0.1150	0.0010
	Max	0.2390	0.5130	0.0026
	Mean	0.1520	0.2335	0.0019
UnBiased	106	0.0670	0.1630	0.0004
	107	0.1310	0.2490	0.0027
	120	0.0470	0.1880	0.0005
	122	0.0560	0.0400	0.0009
	Min	0.0470	0.0400	0.0004
	Max	0.1310	0.2490	0.0027
	Mean	0.0753	0.1600	0.0011

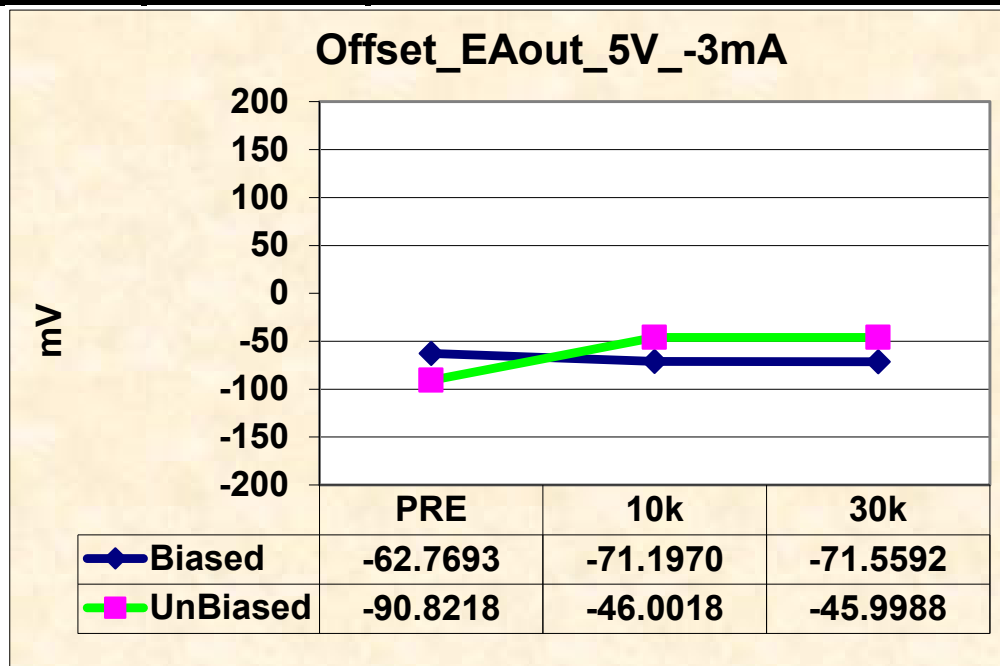
Lin_error_EAout2_10V_1mA



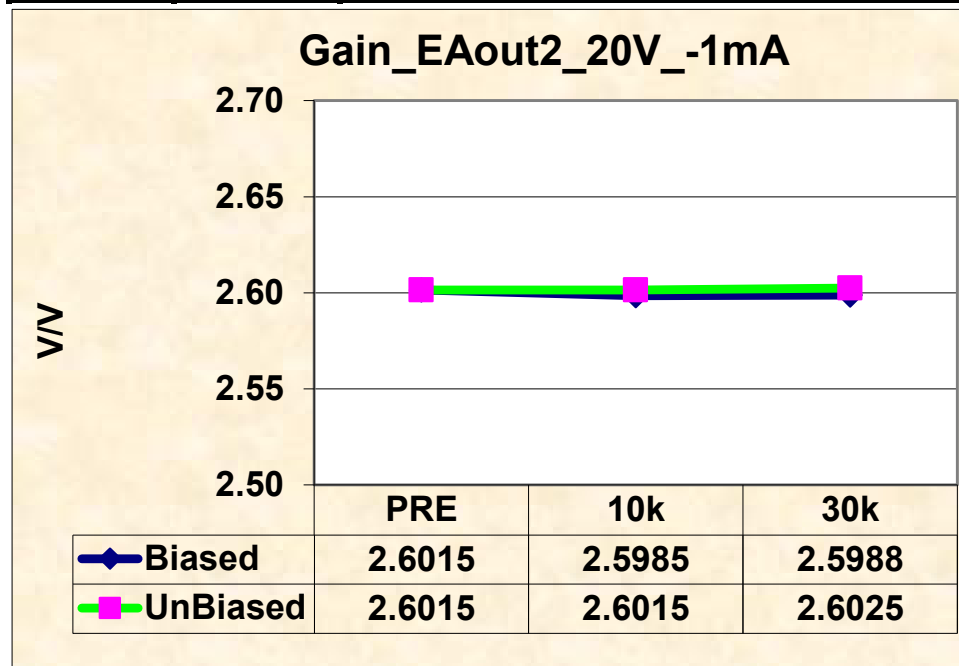
		Gain_EAout_5V_-3mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	1.04881	0.98616	0.98672
Biased	104	1.01614	1.03586	1.03440
	105	0.98584	1.00627	1.00530
	112	1.02810	1.02724	1.02780
	114	0.99442	0.99513	0.99472
	Min	0.98584	0.99513	0.99472
	Max	1.02810	1.03586	1.03440
	Mean	1.0061	1.0161	1.0156
UnBiased	106	1.03208	0.97714	0.97560
	107	1.04541	1.05057	1.04920
	120	0.97411	0.97506	0.97512
	122	1.04385	1.04372	1.04450
	Min	0.97411	0.97506	0.97512
	Max	1.04541	1.05057	1.04920
	Mean	1.0239	1.0116	1.0111



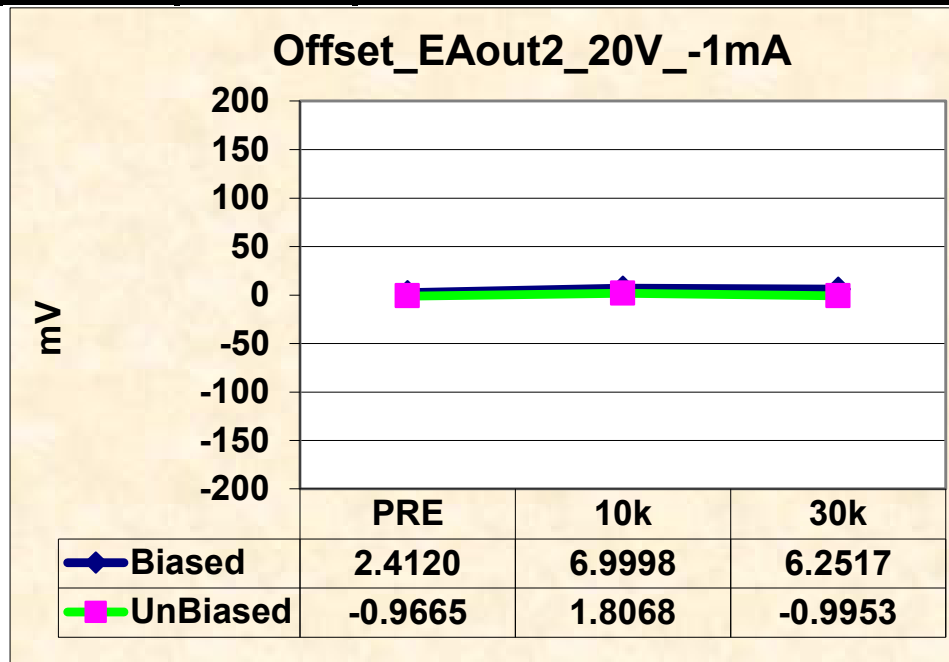
		Offset_EAout_5V_-3mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-94.5380	-9.6940	-10.2906
Biased	104	-75.0250	-91.6350	-89.9283
	105	-9.3150	-25.2650	-25.7830
	112	-104.6470	-104.5200	-105.5560
	114	-62.0900	-63.3680	-64.9695
	Min	-104.6470	-104.5200	-105.5560
	Max	-9.3150	-25.2650	-25.7830
	Mean	-62.7693	-71.1970	-71.5592
UnBiased	106	-109.5940	37.6230	37.6761
	107	-128.1180	-95.4370	-94.3371
	120	6.3590	5.8250	6.4477
	122	-131.9340	-132.0180	-133.7820
	Min	-131.9340	-132.0180	-133.7820
	Max	6.3590	37.6230	37.6761
	Mean	-90.8218	-46.0018	-45.9988



		Gain_EAout2_20V -1mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.5970	2.6050	2.6030
Biased	104	2.6010	2.5980	2.5996
	105	2.6070	2.5970	2.5968
	112	2.6000	2.6020	2.6000
	114	2.5980	2.5970	2.5986
	Min	2.5980	2.5970	2.5968
	Max	2.6070	2.6020	2.6000
	Mean	2.6015	2.5985	2.5988
UnBiased	106	2.6050	2.6040	2.6040
	107	2.5930	2.5970	2.6010
	120	2.6100	2.6080	2.6084
	122	2.5980	2.5970	2.5967
	Min	2.5930	2.5970	2.5967
	Max	2.6100	2.6080	2.6084
	Mean	2.6015	2.6015	2.6025

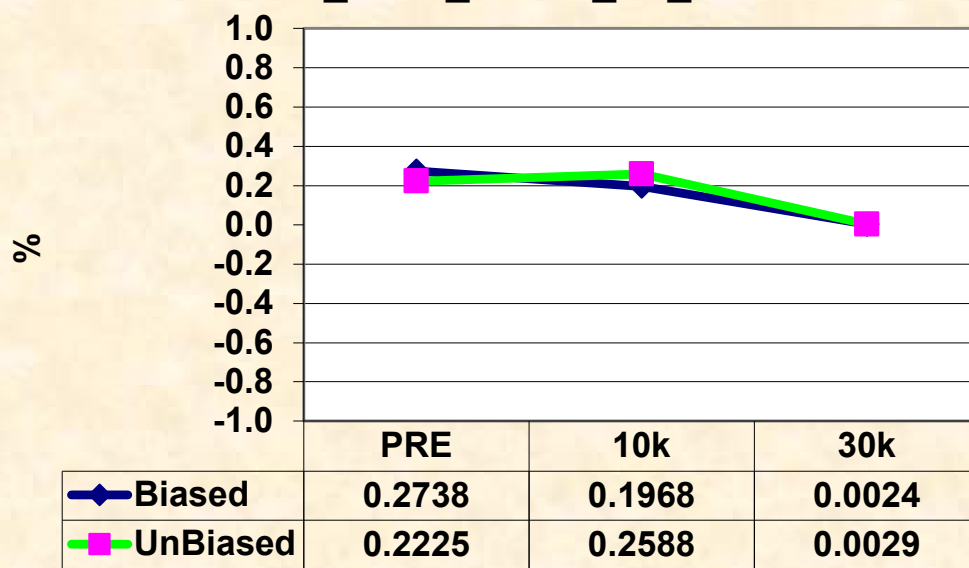


Wafer #	SN	Offset_EAout2_20V_-1mA (mV)		
		PRE	10k	30k
CTRL	99	7.9100	-2.2420	-1.6344
Biased	104	4.4780	5.1200	1.0390
	105	-4.7510	15.6770	14.6752
	112	12.1440	9.6000	11.7993
	114	-2.2230	-2.3980	-2.5068
	Min	-4.7510	-2.3980	-2.5068
	Max	12.1440	15.6770	14.6752
	Mean	2.4120	6.9998	6.2517
UnBiased	106	4.3030	-9.7320	-10.3026
	107	-12.4270	8.5490	1.3599
	120	3.9630	6.7030	3.4283
	122	0.2950	1.7070	1.5334
	Min	-12.4270	-9.7320	-10.3026
	Max	4.3030	8.5490	3.4283
	Mean	-0.9665	1.8068	-0.9953



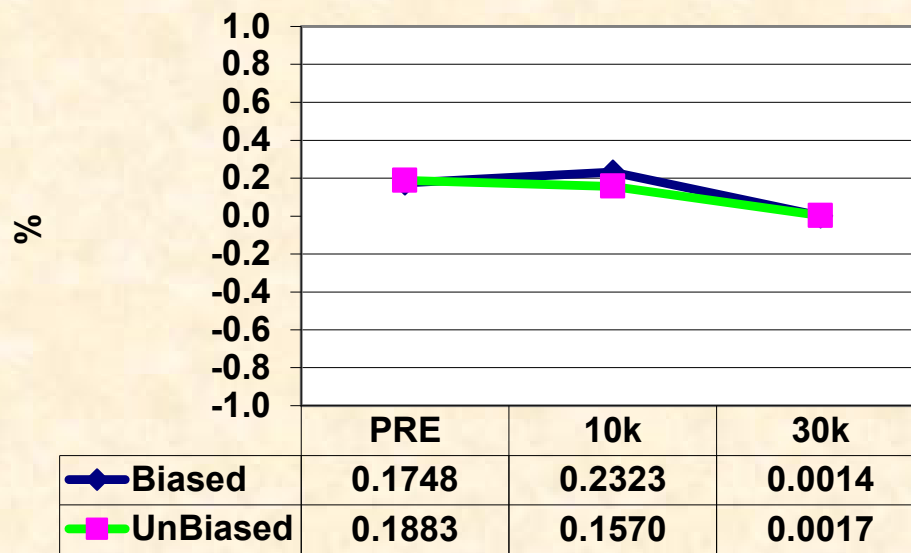
Wafer #	SN	Lin_error_EAout 5V -3mA (%)		
		PRE	10k	30k
CTRL	99	0.2680	0.1920	0.0029
Biased	104	0.2400	0.1780	0.0030
	105	0.2570	0.0590	0.0013
	112	0.0440	0.4600	0.0025
	114	0.5540	0.0900	0.0029
	Min	0.0440	0.0590	0.0013
	Max	0.5540	0.4600	0.0030
	Mean	0.2738	0.1968	0.0024
UnBiased	106	0.1710	0.3480	0.0037
	107	0.1020	0.1410	0.0019
	120	0.4350	0.1560	0.0016
	122	0.1820	0.3900	0.0042
	Min	0.1020	0.1410	0.0016
	Max	0.4350	0.3900	0.0042
	Mean	0.2225	0.2588	0.0029

Lin_error_EAout_5V_-3mA

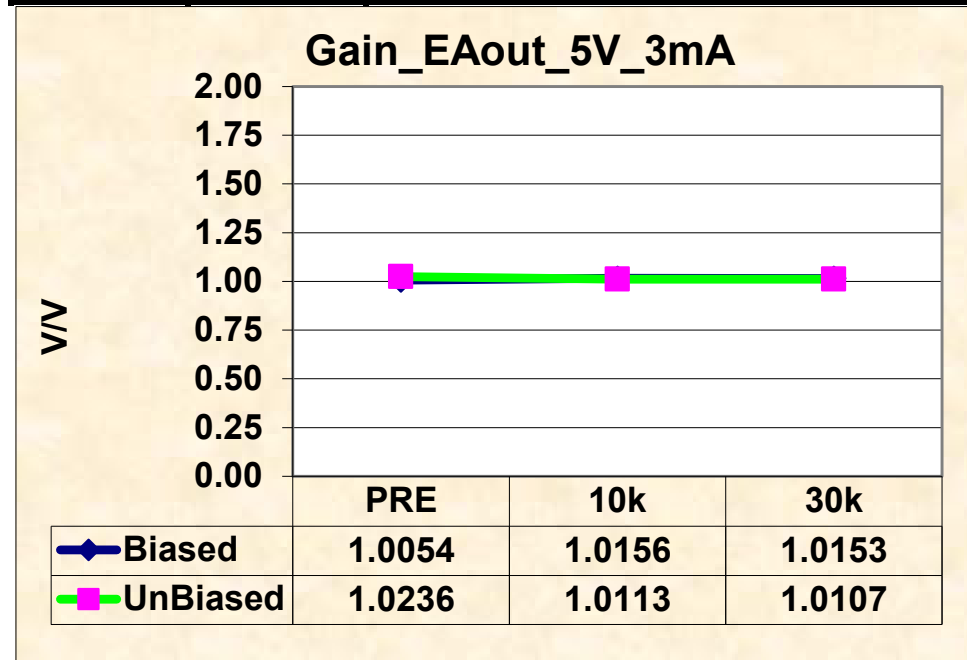


		Lin_error_EAout2 20V -1mA (%)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.1530	0.06600	0.0015
Biased	104	0.0630	0.11800	0.0015
	105	0.0970	0.20600	0.0011
	112	0.1020	0.50400	0.0017
	114	0.4370	0.10100	0.0012
	Min	0.0630	0.10100	0.0011
	Max	0.4370	0.50400	0.0017
	Mean	0.1748	0.2323	0.0014
UnBiased	106	0.0820	0.20500	0.0014
	107	0.2040	0.04600	0.0004
	120	0.2730	0.04800	0.0013
	122	0.1940	0.32900	0.0035
	Min	0.0820	0.04600	0.0004
	Max	0.2730	0.32900	0.0035
	Mean	0.1883	0.1570	0.0017

Lin_error_EAout2_20V_-1mA

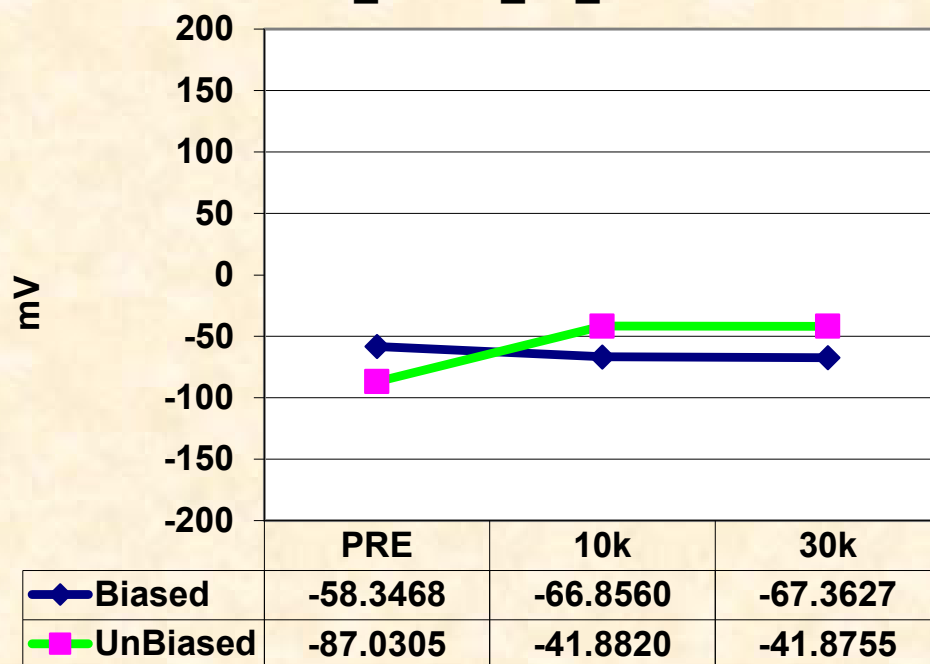


Wafer #	SN	Gain_EAout 5V 3mA (V/V)		
		PRE	10k	30k
CTRL	99	1.04784	0.98566	0.98601
Biased	104	1.01496	1.03472	1.03490
	105	0.98593	1.00708	1.00490
	112	1.02693	1.02674	1.02710
	114	0.99359	0.99405	0.99441
	Min	0.98593	0.99405	0.99441
	Max	1.02693	1.03472	1.03490
	Mean	1.0054	1.0156	1.0153
UnBiased	106	1.03143	0.97730	0.97511
	107	1.04523	1.04951	1.04880
	120	0.97422	0.97415	0.97443
	122	1.04369	1.04432	1.04440
	Min	0.97422	0.97415	0.97443
	Max	1.04523	1.04951	1.04880
	Mean	1.0236	1.0113	1.0107



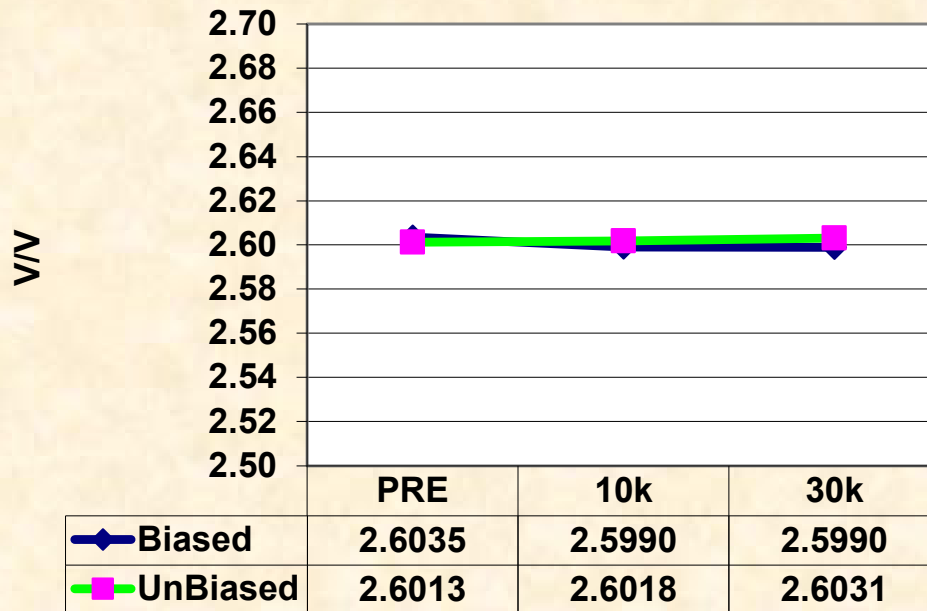
		Offset EAout 5V 3mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-89.1790	-5.3340	-6.5961
Biased	104	-69.6970	-86.5680	-87.3309
	105	-5.9820	-21.4990	-21.3454
	112	-99.7260	-100.0590	-100.5062
	114	-57.9820	-59.2980	-60.2681
	Min	-99.7260	-100.0590	-100.5062
	Max	-5.9820	-21.4990	-21.3454
	Mean	-58.3468	-66.8560	-67.3627
UnBiased	106	-105.2890	40.8440	42.3762
	107	-124.8940	-90.0450	-90.4667
	120	10.2050	10.7640	10.1277
	122	-128.1440	-129.0910	-129.5393
	Min	-128.1440	-129.0910	-129.5393
	Max	10.2050	40.8440	42.3762
	Mean	-87.0305	-41.8820	-41.8755

Offset_EAout_5V_3mA



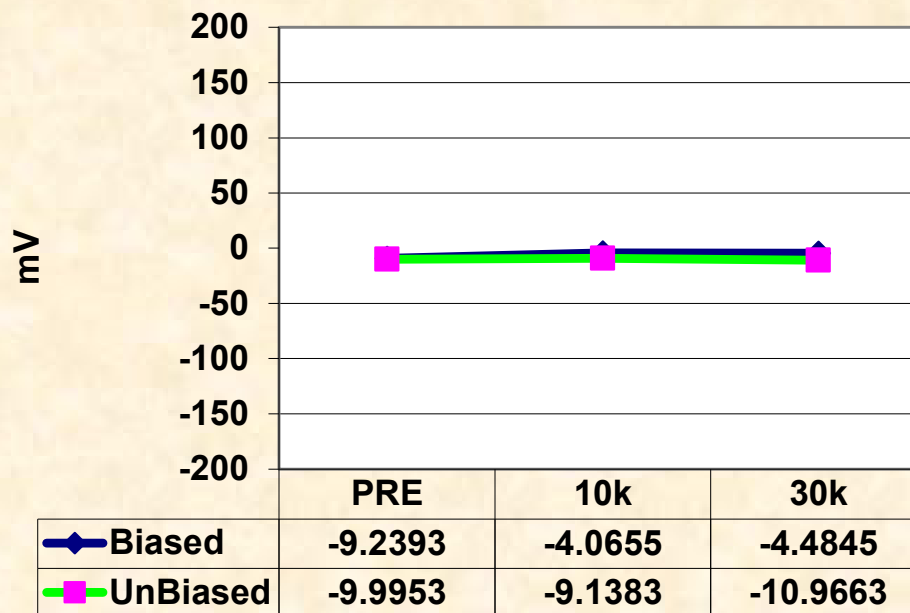
		Gain EAout2 20V 1mA (V/V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.6000	2.6050	2.6050
Biased	104	2.6030	2.5990	2.5977
	105	2.6070	2.5950	2.5984
	112	2.6030	2.6020	2.6016
	114	2.6010	2.6000	2.5981
	Min	2.6010	2.5950	2.5977
	Max	2.6070	2.6020	2.6016
	Mean	2.6035	2.5990	2.5990
UnBiased	106	2.6050	2.6040	2.6058
	107	2.5930	2.5990	2.5999
	120	2.6090	2.6090	2.6102
	122	2.5980	2.5950	2.5964
	Min	2.5930	2.5950	2.5964
	Max	2.6090	2.6090	2.6102
	Mean	2.6013	2.6018	2.6031

Gain_EAout2_20V_1mA



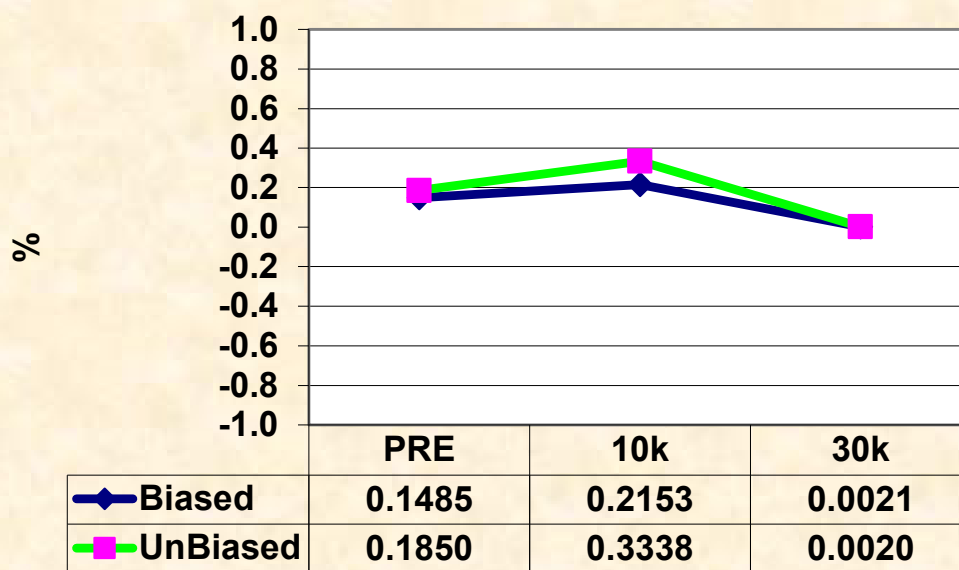
		Offset EAout2 20V 1mA (mV)		
Wafer #	SN	PRE	10k	30k
CTRL	99	-6.0310	-12.6790	-11.5655
Biased	104	-7.9110	-6.4880	-5.3545
	105	-14.2800	4.6890	2.9892
	112	0.0280	-0.4250	-1.1892
	114	-14.7940	-14.0380	-14.3835
	Min	-14.7940	-14.0380	-14.3835
	Max	0.0280	4.6890	2.9892
	Mean	-9.2393	-4.0655	-4.4845
UnBiased	106	-6.3400	-18.8630	-22.3399
	107	-20.6440	-5.6940	-5.7004
	120	-4.3150	-5.5410	-6.6471
	122	-8.6820	-6.4550	-9.1778
	Min	-20.6440	-18.8630	-22.3399
	Max	-4.3150	-5.5410	-5.7004
	Mean	-9.9953	-9.1383	-10.9663

Offset_EAout2_20V_1mA



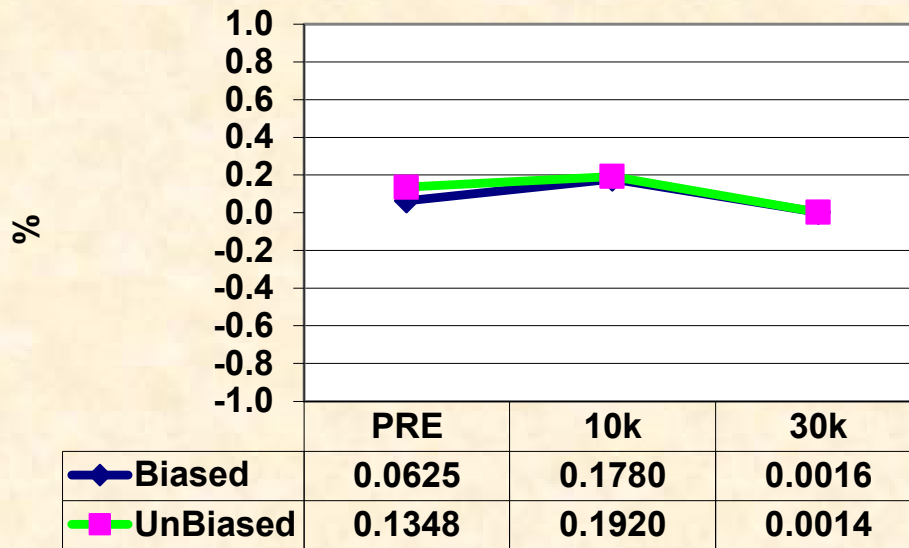
		Lin Error EAout 5V 3mA (%)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.2110	0.2240	0.0009
Biased	104	0.1310	0.2410	0.0015
	105	0.1660	0.1150	0.0019
	112	0.0610	0.2170	0.0023
	114	0.2360	0.2880	0.0025
	Min	0.0610	0.1150	0.0015
	Max	0.2360	0.2880	0.0025
	Mean	0.1485	0.2153	0.0021
UnBiased	106	0.2550	0.3410	0.0016
	107	0.1250	0.4160	0.0040
	120	0.2090	0.1700	0.0013
	122	0.1510	0.4080	0.0012
	Min	0.1250	0.1700	0.0012
	Max	0.2550	0.4160	0.0040
	Mean	0.1850	0.3338	0.0020

Lin_error_EAout_5V_3mA

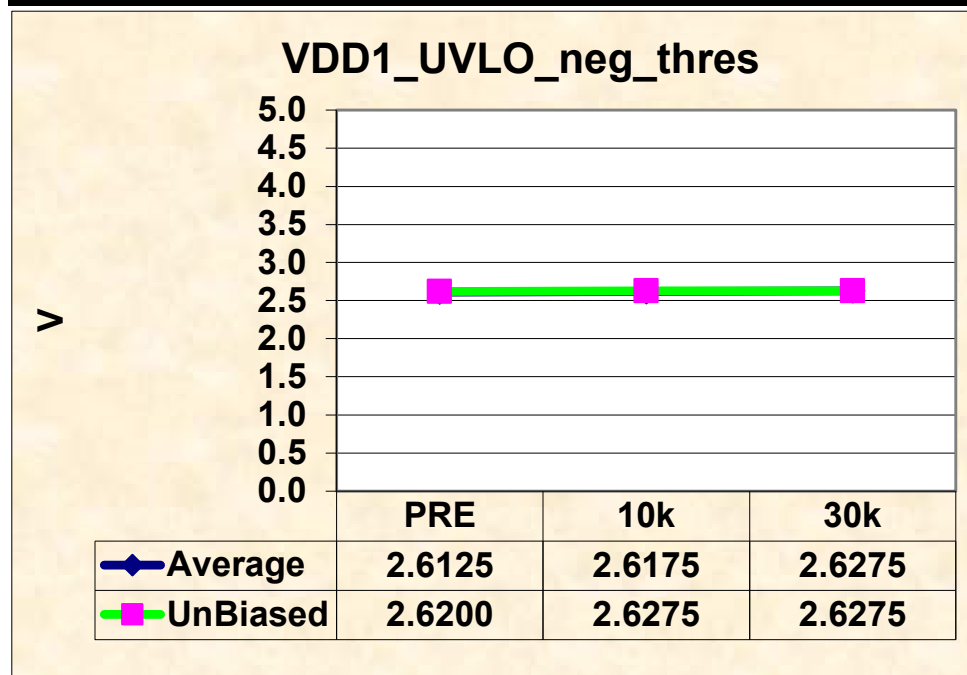


		Lin Error EAout2 20V 1mA (%)		
Wafer #	SN	PRE	10k	30k
CTRL	99	0.0530	0.13000	0.0007
Biased	104	0.0960	0.40200	0.0006
	105	0.0360	0.08700	0.0033
	112	0.0630	0.14400	0.0023
	114	0.0550	0.07900	0.0002
	Min	0.0360	0.07900	0.0002
	Max	0.0960	0.40200	0.0033
	Mean	0.0625	0.1780	0.0016
UnBiased	106	0.1250	0.17400	0.0007
	107	0.1650	0.19400	0.0022
	120	0.0900	0.10100	0.0005
	122	0.1590	0.29900	0.0022
	Min	0.0900	0.10100	0.0005
	Max	0.1650	0.29900	0.0022
	Mean	0.1348	0.1920	0.0014

Lin_error_EAout2_20V_1mA

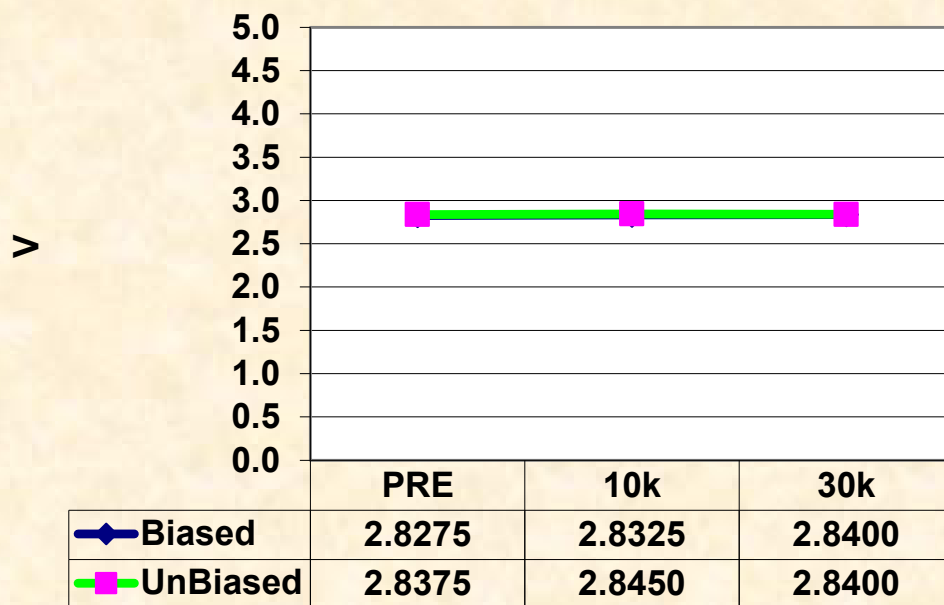


		VDD1_UVLO_neg_threshold (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.6200	2.6200	2.6200
Biased	104	2.6000	2.6100	2.6200
	105	2.6200	2.6200	2.6300
	112	2.6000	2.6100	2.6200
	114	2.6300	2.6300	2.6400
	Min	2.6000	2.6100	2.6200
	Max	2.6300	2.6300	2.6400
	Mean	2.6125	2.6175	2.6275
UnBiased	106	2.6000	2.6200	2.6200
	107	2.6100	2.6200	2.6200
	120	2.6400	2.6400	2.6400
	122	2.6300	2.6300	2.6300
	Min	2.6000	2.6200	2.6200
	Max	2.6400	2.6400	2.6400
	Mean	2.6200	2.6275	2.6275

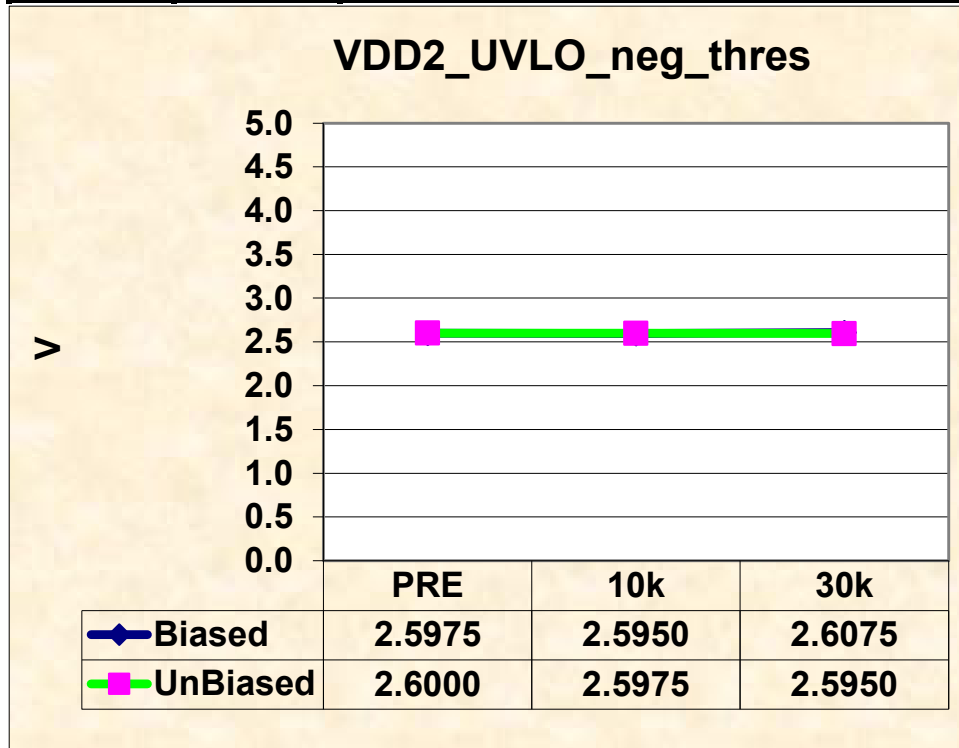


Wafer #	SN	VDD1_UVLO_pos_threshold (V)		
		PRE	10k	30k
CTRL	99	2.8400	2.8400	2.8400
Biased	104	2.8100	2.8300	2.8300
	105	2.8400	2.8400	2.8500
	112	2.8200	2.8200	2.8300
	114	2.8400	2.8400	2.8500
	Min	2.8100	2.8200	2.8300
	Max	2.8400	2.8400	2.8500
	Mean	2.8275	2.8325	2.8400
UnBiased	106	2.8100	2.8400	2.8300
	107	2.8300	2.8400	2.8400
	120	2.8600	2.8600	2.8500
	122	2.8500	2.8400	2.8400
	Min	2.8100	2.8400	2.8300
	Max	2.8600	2.8600	2.8500
	Mean	2.8375	2.8450	2.8400

VDD1_UVLO_pos_thres



		VDD2_UVLO_neg_threshold (V)		
Wafer #	SN	PRE	10k	30k
CTRL	99	2.5900	2.6000	2.6000
Biased	104	2.6200	2.6000	2.6100
	105	2.6000	2.6100	2.6300
	112	2.5700	2.5700	2.5800
	114	2.6000	2.6000	2.6100
	Min	2.5700	2.5700	2.5800
	Max	2.6200	2.6100	2.6300
	Mean	2.5975	2.5950	2.6075
UnBiased	106	2.5900	2.6100	2.6100
	107	2.6100	2.5900	2.5900
	120	2.6100	2.6100	2.6000
	122	2.5900	2.5800	2.5800
	Min	2.5900	2.5800	2.5800
	Max	2.6100	2.6100	2.6100
	Mean	2.6000	2.5975	2.5950



Wafer #	SN	VDD2_UVLO_pos_threshold (V)		
		PRE	10k	30k
CTRL	99	2.8100	2.8100	2.8100
Biased	104	2.8400	2.8200	2.8300
	105	2.8100	2.8300	2.8400
	112	2.7800	2.7900	2.8000
	114	2.8200	2.8200	2.8300
	Min	2.7800	2.7900	2.8000
	Max	2.8400	2.8300	2.8400
	Mean	2.8125	2.8150	2.8250
UnBiased	106	2.8100	2.8200	2.8200
	107	2.8200	2.8000	2.8100
	120	2.8300	2.8200	2.8200
	122	2.8000	2.8000	2.8000
	Min	2.8000	2.8000	2.8000
	Max	2.8300	2.8200	2.8200
	Mean	2.8150	2.8100	2.8125

VDD2_UVLO_pos_thres

