SINGLE EVENT LATCH-UP TEST REPORT

PRODUCT: AD8346S
DIE TYPE: AD8346 R1
DATE CODE: 0927
CASE TEMPERATURE: 125°C
EFFECTIVE LET: 83MeV-cm²/mg
MINIMUM FLUENCE: 1E7 ion/cm²
FLUX: ~2E5 ion/cm²-s
FACILITIES: Lawrence Berkeley National Laboratories
TESTED: April 2010

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Single Event Latchup Testing of the AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator for Analog Devices

Customer: Analog Devices (PO# 45290784)

RAD Job Number: 10-050

Part Types Tested: Analog Devices AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator

Traceability Information: Lot Date Code: 0927A; see a photograph of sample unit-under-test for traceability information in Appendix A.

Quantity of Parts for Testing: Three units were exposed to 1E7ion/cm² at an LET of approximately 80MeV-cm²/mg using worst-case bias and temperature test conditions

Pre-Irradiation Burn-In: Units-under-test were not burned-in prior to the SEL testing.

Referenced Test Standard(s): ASTM F1192, EIA/JESD57

Electrical Test Conditions: Supply current monitored during exposure.

Test Software / Hardware: ICC.XLS, See Appendix C, Table C.1 for a list of test equipment and calibration dates.

Bias Conditions: All units-under-test will be biased during heavy ion irradiation using a worst-case supply potential in accordance with ASTM F1192 and EIA/JESD57. See Appendix B for the details of the bias conditions.

Ion Energy and LET Ranges: Minimum of 10MeV/n Xe beams with effective LETs of approximately 80MeV-cm²/mg. The 10MeV/n Xe beam had a minimum range of 50μm in silicon to the Bragg Peak.

Heavy Ion Flux and Maximum Fluence Levels: Flux of approximately 1 to 2E5ions/cm². Minimum 1E7 ions/cm² per unit tested

Facility and/or Radiation Source: Lawrence Berkeley National Laboratories (LBNL) Berkeley, CA (10MeV/n beam) or Texas A&M (15MeV/n beam).

Irradiation Temperature: 125°C case temperature as specified as the worst-case condition by the customer.

Units PASSED latch-up at an effective LET of approximately 80MeV-cm²/mg with no significant change in supply current. The units were exposed to a minimum total fluence of 1E7ion/cm²
1.0. Overview and Background

It is well known that heavy ion exposure can cause temporary and/or permanent damage in electronic devices. The damage can occur through various mechanisms including single event latch-up (SEL), single event burnout (SEB) and single event gate rupture (SEGR). An SEL event occurs when a parasitic npnp feedback latch structure becomes biased into the on state due to a dense track of electron-hole pairs created along the heavy ion path in silicon. This latch-up is self-sustaining since there is a positive feedback path created and requires a power cycle to reset. A single event latch-up can lead to single event burnout if the current draw from the SEL event is sufficient to damage the junction and/or bond wire. The damage is worse and/or becomes evident with increasing linear energy transfer (LET) and fluence. The two test standards usually used to govern this testing are ASTM F1192 and EIA/JESD57. This destructive testing is usually performed at the maximum datasheet voltage and temperature to a total fluence of not less than $1E7\text{ion/cm}^2$.

2.0. Single Event Latch-Up Test Apparatus

The single event latch-up testing described in this final report was performed at the Lawrence Berkeley National Laboratories (LBNL) using the 88-Inch Cyclotron. The 88-Inch Cyclotron is operated by the University of California for the US Department of Energy (DOE) and is a K=140 sector-focused cyclotron with both light- and heavy-ion capabilities. Protons and other light-ions are available at high intensities (10-20μA) up to maximum energies of 55 MeV (protons), 65 MeV (deuterons), 135 MeV (3He) and 140 MeV (4He). Most heavy ions through uranium can be accelerated to maximum energies, which vary with the mass and charge state.

For the SEL testing described in this final report the units-under-test were be placed in the Cave 4B vacuum chamber aligned with the heavy ion beam line. The test platter in the vacuum chamber has full x and y alignment capabilities along with 2-dimensional rotation, allowing for a variety of effective LETs for each ion. For SEE testing Lawrence Berkeley Laboratories provides the dosimetry via a local control computer running a Lab View based program. Each ion is calibrated just prior to use using five photomultiplier tubes (PMTs). Four of the five PMTs are used during the test to provide the beam statistics, while the center PMT is removed following calibration. Figure 2.1 shows an illustration of the LBL facility; including the location of Cave 4B, where the heavy ion SEE testing takes place.
Figure 2.1. Map of 88-Inch Cyclotron Facility showing the location of Cave 4B, where the SEE testing was performed.
3.0. Radiation Test Conditions

The AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator described in this final report was irradiated using Xe with a single sided supply voltage of +5.0V and at a case temperature of 125°C (±5°C). A –3.0V potential was also generated using a voltage divider from the –5V supply. See the test circuit schematic in Appendix B for the specific details of the bias conditions. The 10MeV/n beam was used to provide sufficient range in silicon while meeting the maximum LET requirements of the program. The other beams available at Berkeley are the 4.5MeV/n beam and the 16MeV/n beam. The 4.5MeV/n beam does not provide sufficient range for destructive SEE testing while the 16MeV/n beam provides a much smaller selection of ions. Figure 3.1 shows the 10MeV/n beam characteristics for Xe. As seen in the figure, the range to the Bragg Peak is approximately 60μm while the surface LET is approximately 58MeV-cm²/mg for the Xe beam. Figure 3.2 shows the characteristics for all the beams available at Berkeley. Note that the units were de-encapsulated prior to testing and all exposures took place from the top surface providing a distance to the active layer in Silicon of approximately 5 to 10μm.

As noted above, the devices were irradiated to a minimum fluence of 1E7ion/cm². The flux varied during the testing, but was consistently targeted to approximately 2E5ion/cm²-s. The irradiation of the units-under-test continued until either the minimum fluence was reached or a latchup event was observed.

For the elevated temperature required for single event latch-up testing an aluminum plate heater fixed to the back of the board and was used to heat the device-under-test (DUT) with an RTD used to monitor the temperature. The case temperature of the DUT was calibrated prior to the testing to the RTD with a thermocouple, allowing the RTD to provide feedback and maintain a calibrated 125°C case temperature throughout the testing. The data monitored during the test (case temperature, supply voltage and supply current) was routed to the control room (approximately 20-feet away) using shielded coaxial cable.
Figure 3.1. Range of the 10MeV/n Xe beam into silicon. The range to the Bragg Peak is approximately 60µm while the surface LET is approximately 58MeV-cm²/mg.
Figure 3.2. Characteristics of all the beams available at Berkeley. For the testing discussed in this report the 10MeV/n beam was used exclusively.
4.0. Tested Parameters

During the heavy ion exposure, the supply currents to the unit-under-test was measured and recorded in approximately 1-second increments. A plot of supply current versus time/fluence for each of the heavy ion exposures is included in this final report. Figure 4.1 shows the test board used for the SEL testing described in this final report. The test board was mounted on the test stage at Berkeley and provided 3-axis of motion plus rotation. The board had multiple units-under-test that allowed for sequential testing of the units without vacuum breaks during testing.

In general the following minimum criteria must be met for a device to pass SEL testing: During the heavy ion exposure the DUT’s supply current must remain within the unit’s specification limit without cycling power. If this condition is not satisfied following the heavy ion testing, then the SEL testing could be logged as a failure. Note that during heavy ion testing a substantial amount of total dose can be absorbed by the units-under-test. If a functional failure occurs during or following the testing, it is important to separate TID failures from destructive single event effects. Also, a single event latch-up may not be a “destructive” event since it is still functional, however a unit which experiences an SEL (i.e., a high sustained supply current requiring a power cycle to recover) is considered to have failed this test even if the units are functional and meet parametric limits following the testing.
Figure 4.1. Single event test board mounted on the test stage at Berkeley. The board has four units-under-test mounted simultaneously to minimize vacuum breaks during testing. There is also a heater plate mounted to the backside of the board to provide the elevated temperature required for this testing.
## 5.0. Single Event Latch-Up Test Results

Using the criteria established for pass/fail of this single event latchup test, the AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator (of the lot date code identified on the first page of this report) passed the SEL test at the minimum tested LET of 80MeV-cm\(^2\)/mg and using worst-case bias and temperature test conditions for this device type. None of the four units-under-test exhibited any significant increase in supply current during the beam run. Note that additional voltages were generated on the test board using voltage dividers. A single event latchup on any of these generated potentials would have been observed on the primary power supply.

Table 5.1 show a summary of the single event latch-up data acquired. The table shows the part type (AD8346), the serial number of the part irradiated, the test configuration (all units irradiated in a static configuration), the case temperature during testing, the ion species, the effective fluence, the effective LET, the number of SEL events recorded and the SEL cross-section.

Figures 5.1 shows the supply current data during the SEL runs for all four units-under-test. In this figure the supply current is plotted as a function of time. The plots show the response of the units-under-test from the start to the end of the exposure (total of 1E7ion/cm\(^2\)). As seen in these figures, the units-under-test do not show any significant change in supply current during the course of the exposure.

<table>
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<tr>
<th>Part Type</th>
<th>SN</th>
<th>Configuration</th>
<th>Temp Deg C</th>
<th>Ion Species/ Energy</th>
<th>Eff. Fluence (ion/cm(^2))</th>
<th>Effective LET (MeV-cm(^2)/mg)</th>
<th>SEL Events</th>
<th>SEL Cross-Section</th>
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<tr>
<td>AD8346</td>
<td>U9</td>
<td>Static Bias</td>
<td>125</td>
<td>Xe 10MeV/n</td>
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<td>0.00E+00</td>
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<td>0.00E+00</td>
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</tbody>
</table>

Table 5.1. Summary of the SEL test runs for the AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator.
Figure 5.1. Supply current on versus time for AD8346 unit numbers U9-U12. The testing was performed at an effective LET of 83MeV-cm²/mg using Xe at an energy of 10MeV/n. As seen in the figure, the supply current shows no significant increase during the run, indicating that the units-under-test have not experienced a latch-up event.
6.0. Summary/Conclusions

The single event latch-up testing described in this final report was performed at the Lawrence Berkeley National Laboratories (LBNL) using the 88-Inch Cyclotron. The 88-Inch Cyclotron is operated by the University of California for the US Department of Energy (DOE) and is a K=140 sector-focused cyclotron with both light- and heavy-ion capabilities.

The AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator described in this final report was irradiated using Xe with a single supply voltage of +5.0V and at a case temperature of 125°C (±5°C). Note that additional voltages were generated on the test board using voltage dividers. See the test circuit schematic in Appendix B for the specific details of the bias conditions. Single event latchup on any of these generated potentials would have been observed on the primary power supply. The 10MeV/n beam was used to provide sufficient range in silicon while meeting the minimum LET requirements of the program of 80MeV-cm²/mg. The range to the Bragg Peak for Xe at this energy is approximately 60μm.

The devices were irradiated to a minimum fluence of 1E7ion/cm². The flux varied somewhat during the testing, but was approximately 1E5ion/cm²-s to 2E5ion/cm²-s. During the testing the irradiations continued until either the minimum fluence was reached or a latchup event was observed. In general the following minimum criteria must be met for a device to pass SEL testing: During the heavy ion exposure the DUT’s supply current must remain within the unit’s specification limit without cycling power. If this condition is not satisfied following the heavy ion testing, then the SEL testing could be logged as a failure.

Using the criteria established for pass/fail of this single event latchup test, the AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator (of the lot date code identified on the first page of this report) passed the SEL test at the minimum tested LET of 80MeV-cm²/mg and using worst-case bias and temperature test conditions for this device type. None of the four units-under-test exhibited any significant increase in supply current during the beam run.
Appendix A: Photograph of a Sample Unit-Under-Test for Device Traceability and a Decapsulated Unit Ready for SEL Testing
Appendix B: Electrical Bias Conditions Used During Heavy Ion Exposure

Figure B.1. Schematic drawing of the configurations used for the SEL test described in this final report.
Appendix C: Electrical Test Parameters and Equipment List:

The Analog Devices AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator described in this final report was irradiated using the 10MeV/n Xe beam at the Lawrence Berkeley National Laboratories with a single sided supply voltage of +5.0V and at a case temperature of 125°C (±5°C). During the heavy ion exposure, the supply current to the unit-under-test was measured and recorded in approximately 1-second increments.

Table C.1 lists the equipment used during the testing as well as the calibration dates and the date the calibration is due.

Table C.1. Test equipment and calibration dates for testing the Analog Devices AD8346 0.8 GHz to 2.5 GHz Quadrature Modulator.

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<th>Equipment</th>
<th>Serial Number</th>
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<th>Calibration Due</th>
<th>Purpose</th>
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<tr>
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<td>EF201999</td>
<td>N/A</td>
<td>N/A</td>
<td>Square Wave Input</td>
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<td>HP 34401A Multimeter</td>
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<td>5/15/10</td>
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<td>N/A</td>
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<td>N/A</td>
<td>Test power supply-Negative Supply</td>
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<td>38301747</td>
<td>2/19/10</td>
<td>2/19/11</td>
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<td>2/19/11</td>
<td>Temperature Calibration</td>
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<tr>
<td>Tektronics TDS5104 Oscilloscope</td>
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<td>10/22/09</td>
<td>10/22/10</td>
<td>Output Waveform Measurements, if applicable</td>
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