

**RADIATION TEST REPORT**

PRODUCT:	AD8346AFQMLR
Die Type:	AD8346
DATE CODE:	0927
GAMMA:	100k
GAMMA SOURCE:	Co60
DOSE RATE:	140 Rad (si)/s
FACILITIES:	University of Massachusetts @ Lowell
TESTED:	4/8/2010

The RADTEST<sup>SM</sup> DATA SERVICE is a compilation of radiation test results on Analog Devices' Space grade products. It is designed to assist customers in selecting the right product for applications where radiation is a consideration. Many products manufactured by Analog Devices, Inc. have been shown to be radiation tolerant to most tactical radiation environments. Analog Devices, Inc. does not make any claim to maintain or guarantee these levels of radiation tolerance without lot qualification test.

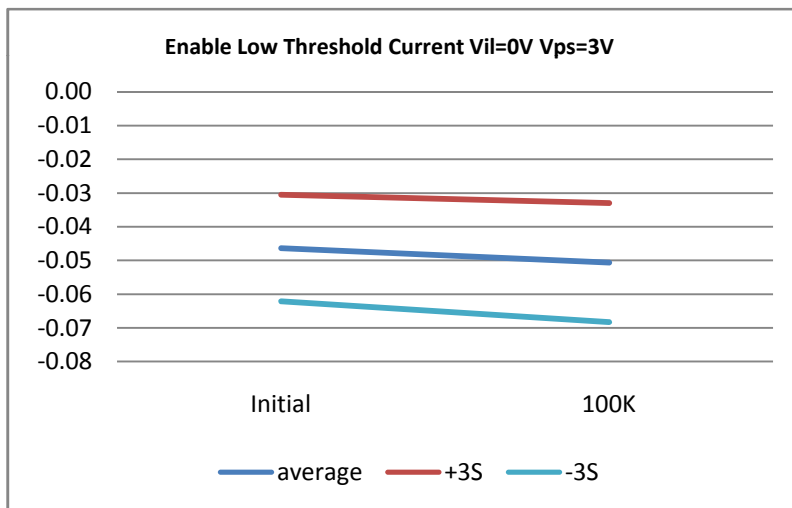
It is the responsibility of the Procuring Activity to screen products from Analog Devices, Inc. for compliance to Nuclear Hardness Critical Items (HCI) specifications.

**WARNING:**

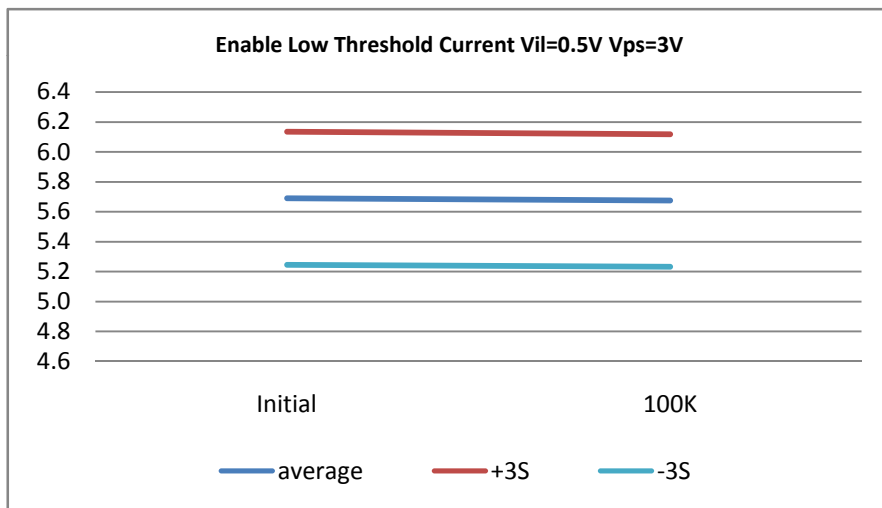
Analog Devices, Inc. does not recommend use of this data to qualify other product grades or process levels. Analog Devices, Inc. is not responsible and has no liability for any consequences, and all applicable Warranties are null and void if any Analog product is modified in any way or used outside of normal environmental and operating conditions, including the parameters specified in the corresponding data sheet. Analog Devices, Inc. does not guarantee that wafer manufacturing is the same for all process levels.



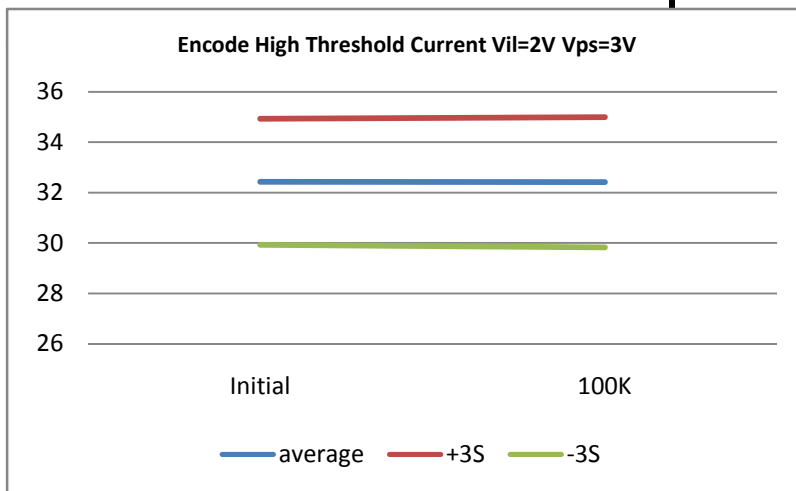
T# 1.0	IIL_0V_3V	uA
SN	Initial	100K
40	-0.04349	-0.04349
80	-0.04349	-0.04349
41	-0.05487	-0.05487
42	-0.04349	-0.05487
43	-0.04349	-0.05487
44	-0.04349	-0.04349
81	-0.04349	-0.05487
82	-0.04349	-0.05487
83	-0.04349	-0.04349
84	-0.05487	-0.04349
min	-0.0549	-0.0549
max	-0.0435	-0.0435
stdev	0.0053	0.0059
average	-0.0463	-0.0506
+3S	-0.0305	-0.0329
-3S	-0.0621	-0.0683



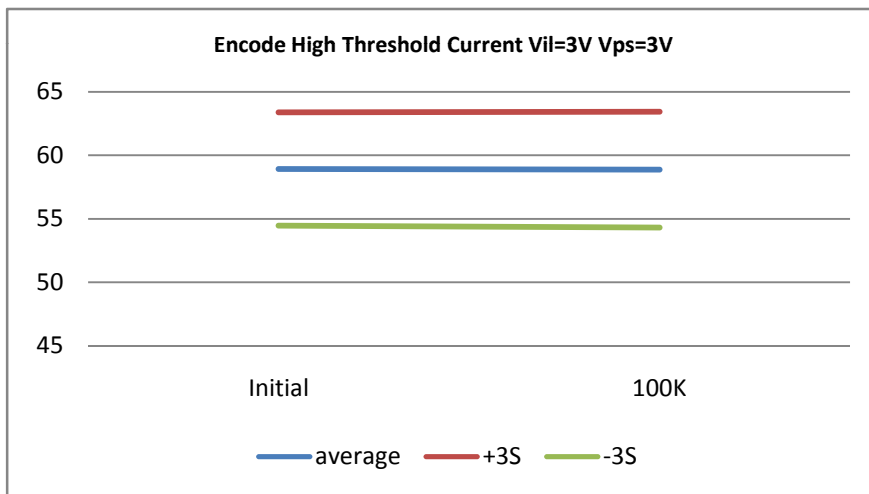
T# 2.0	IIL_0.5V_3V	uA
SN	Initial	100K
40	5.6026	5.63675
80	5.48877	5.50015
41	5.5343	5.5343
42	5.57983	5.59122
43	5.76196	5.73919
44	5.51153	5.47739
81	5.87579	5.86441
82	5.64813	5.64813
83	5.89856	5.88717
84	5.71643	5.65951
min	5.5115	5.4774
max	5.8986	5.8872
stdev	0.1483	0.1473
average	5.6908	5.6752
+3S	6.1356	6.1170
-3S	5.2460	5.2333



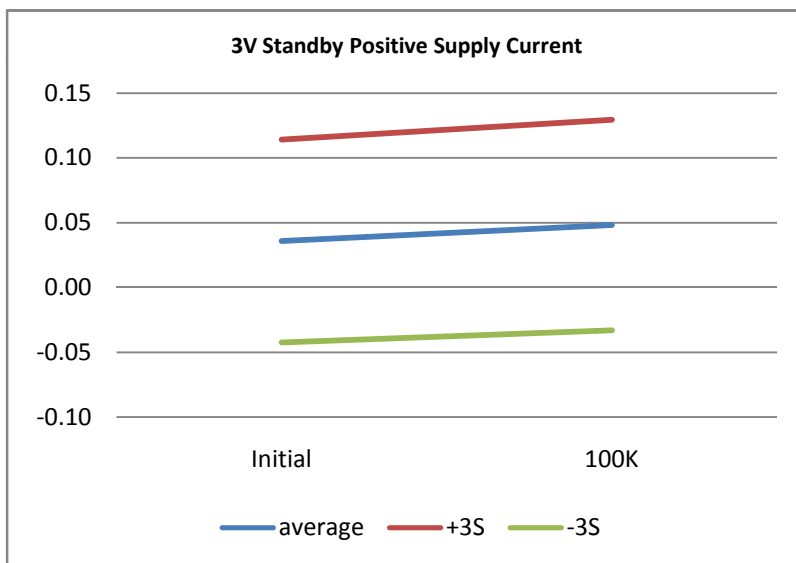
T# 3.0	I <sub>H</sub> _2V_3V	uA
SN	Initial	100K
40	32.04567	32.04567
80	31.60173	31.70418
41	31.57896	31.47652
42	31.76109	31.76109
43	32.74003	32.64897
44	31.81801	31.82939
81	33.7645	33.85557
82	32.17088	32.17088
83	33.53684	33.51408
84	32.05705	32.04567
min	31.5790	31.4765
max	33.7645	33.8556
stdev	0.8328	0.8607
average	32.4284	32.4128
+3S	34.9267	34.9948
-3S	29.9301	29.8308



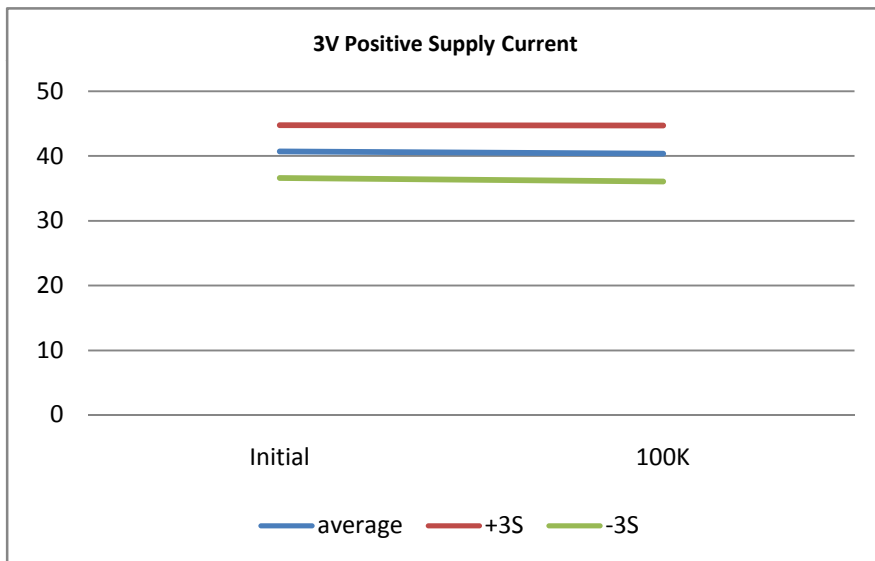
T# 4.0	III_3V_3V	uA
SN	Initial	100K
40	58.22681	58.29511
80	57.39585	57.47553
41	57.35032	57.23649
42	57.72596	57.70319
43	59.47894	59.38788
44	57.79426	57.79426
81	61.28884	61.37991
82	58.5	58.44309
83	60.86767	60.82213
84	58.29511	58.23819
min	57.3503	57.2365
max	61.2888	61.3799
stdev	1.4852	1.5188
average	58.9126	58.8756
+3S	63.3682	63.4320
-3S	54.4571	54.3193



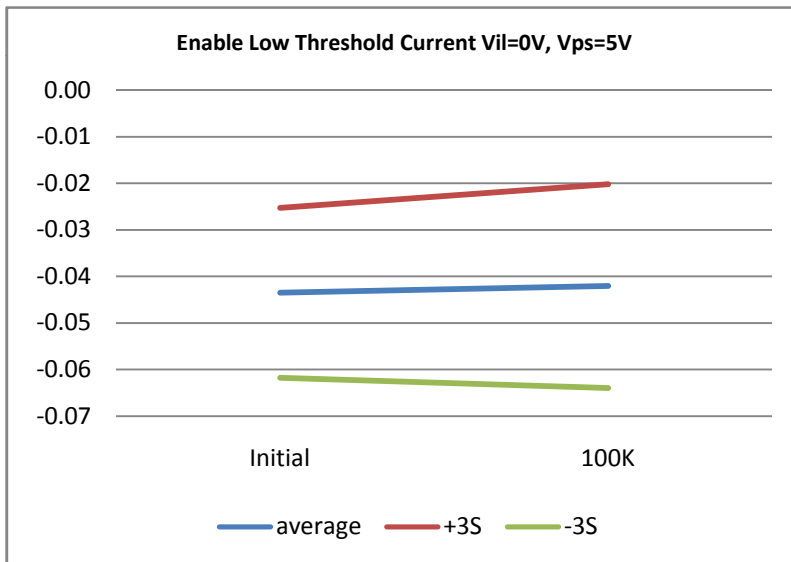
T# 5.0	I_Standby_3V	uA
SN	Initial	100K
40	0.04652	0.03308
80	0.01964	0.04652
41	0.04652	0.04652
42	0.06444	0.09132
43	0.03756	0.05996
44	0.051	0.05548
81	0.06444	0.01964
82	0.02412	0.01964
83	0.00171	0.01964
84	-0.00277	0.0734
min	-0.0028	0.0196
max	0.0644	0.0913
stdev	0.0261	0.0271
average	0.0359	0.0482
+3S	0.1142	0.1295
-3S	-0.0425	-0.0331



T# 6.0	I_PS3V	mA
SN	Initial	100K
40	41.3955	41.44522
80	41.58765	41.65529
41	41.38923	41.04881
42	41.53077	41.26157
43	40.73707	40.67794
44	41.5554	41.22753
81	40.88846	40.65196
82	41.02283	40.64569
83	40.88577	40.59732
84	37.38845	36.8626
min	37.3885	36.8626
max	41.5554	41.2616
stdev	1.3645	1.4439
average	40.6747	40.3717
+3S	44.7684	44.7035
-3S	36.5811	36.0398

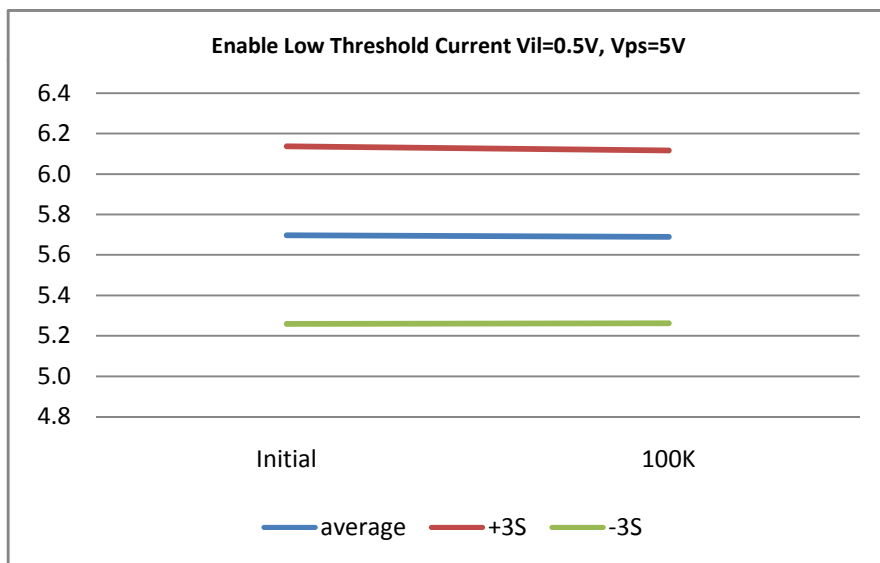


T# 7.0	IIL_0V_5V	uA
SN	Initial	100K
40	-0.04349	-0.05487
80	-0.05487	-0.04349
41	-0.04349	-0.04349
42	-0.04349	-0.04349
43	-0.04349	-0.05487
44	-0.04349	-0.0321
81	-0.04349	-0.04349
82	-0.04349	-0.04349
83	-0.05487	-0.04349
84	-0.0321	-0.0321
min	-0.0549	-0.0549
max	-0.0321	-0.0321
stdev	0.0061	0.0073
average	-0.0435	-0.0421
+3S	-0.0252	-0.0202
-3S	-0.0617	-0.0640

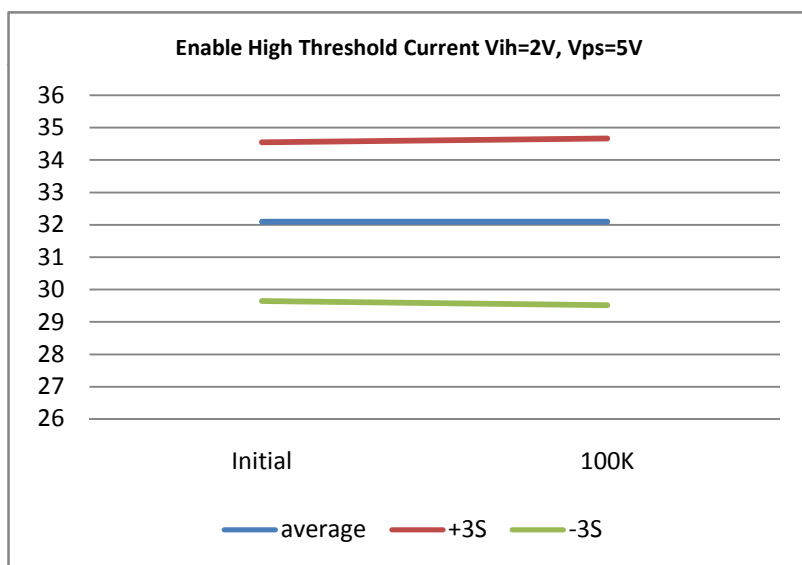




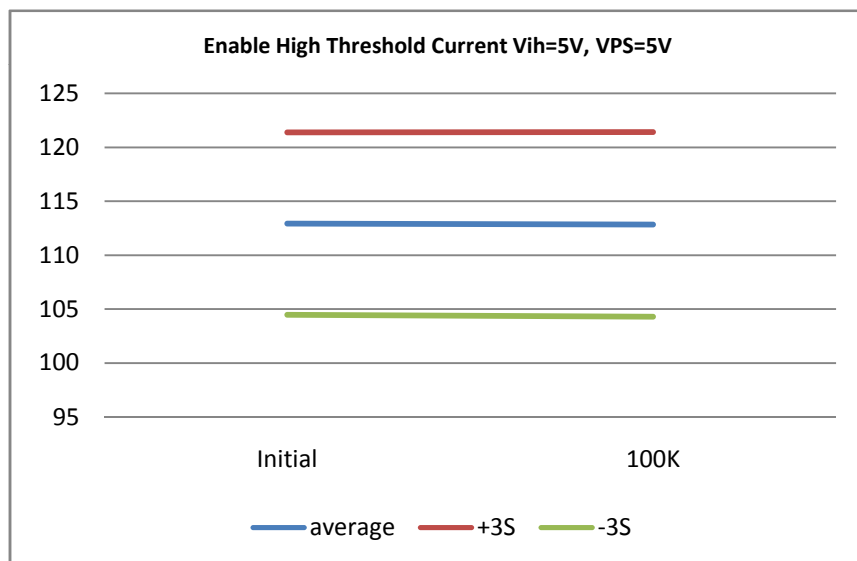
T# 8.0	IIL_0.5V_5V	uA
SN	Initial	100K
40	5.62536	5.62536
80	5.466	5.52292
41	5.56845	5.5343
42	5.59122	5.61398
43	5.76196	5.76196
44	5.51153	5.51153
81	5.87579	5.87579
82	5.65951	5.65951
83	5.92132	5.88717
84	5.69366	5.6709
min	5.5115	5.5115
max	5.9213	5.8872
stdev	0.1464	0.1423
average	5.6979	5.6894
+3S	6.1371	6.1163
-3S	5.2587	5.2625



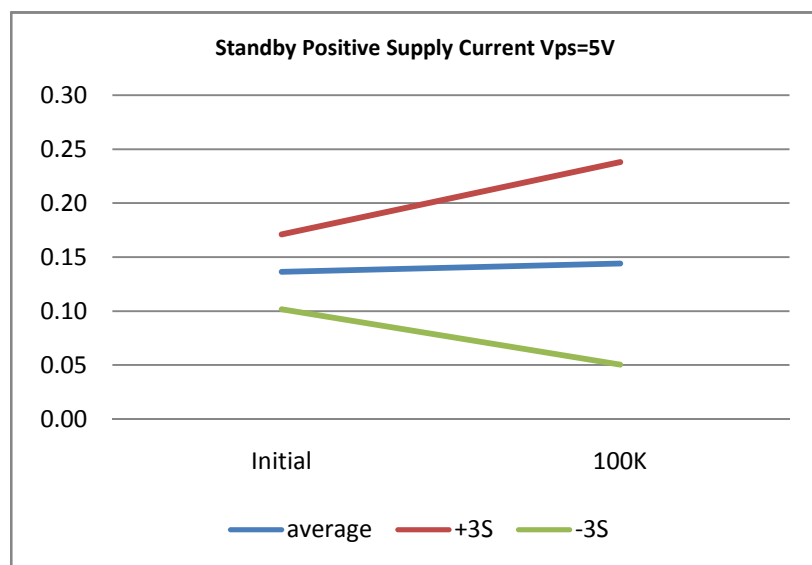
T# 9.0	III_2V_5V	uA
SN	Initial	100K
40	31.71556	31.74971
80	31.33992	31.37407
41	31.26024	31.14641
42	31.45375	31.47652
43	32.33024	32.28471
44	31.52205	31.52205
81	33.42301	33.53684
82	31.84077	31.82939
83	33.19535	33.19535
84	31.72694	31.72694
min	31.2602	31.1464
max	33.4230	33.5368
stdev	0.8164	0.8571
average	32.0940	32.0898
+3S	34.5434	34.6610
-3S	29.6447	29.5185



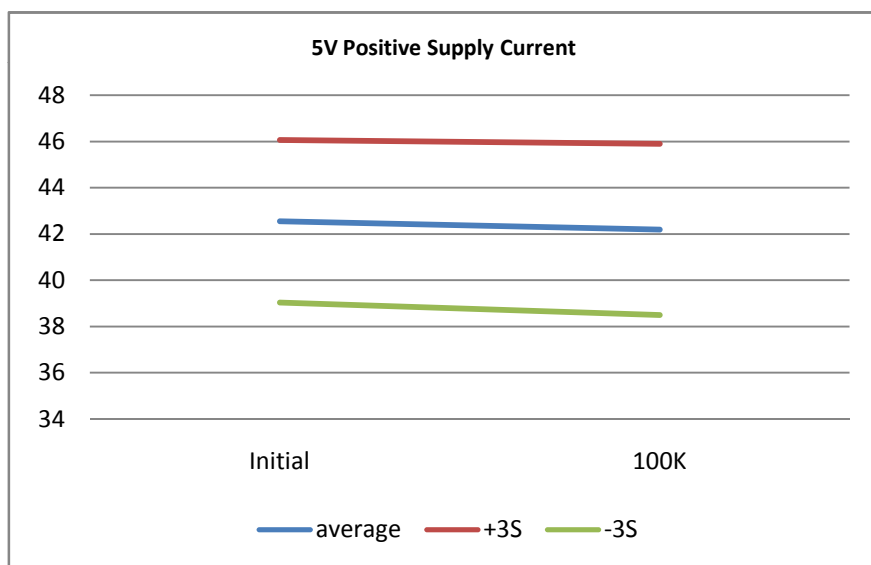
T# 10.0	IIH_5V_5V	uA
SN	Initial	100K
40	111.63633	111.73878
80	110.03133	110.14516
41	109.99719	109.81505
42	110.60048	110.60048
43	114.01538	113.79911
44	110.91921	110.83952
81	117.43028	117.5555
82	112.1941	112.08027
83	116.66762	116.51964
84	111.67048	111.61357
min	109.9972	109.8151
max	117.4303	117.5555
stdev	2.8183	2.8521
average	112.9368	112.8529
+3S	121.3917	121.4093
-3S	104.4820	104.2964



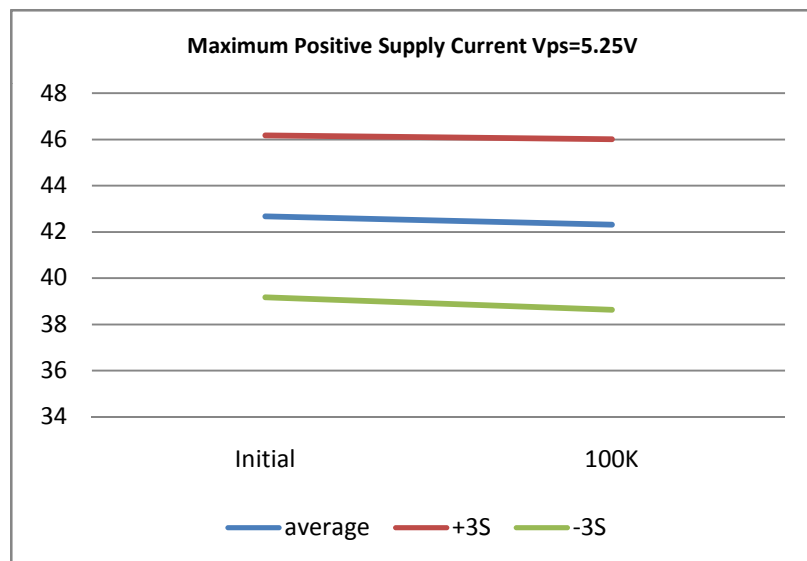
T# 11.0	I_Standby_5V	uA
SN	Initial	100K
40	0.14144	0.11903
80	0.1728	0.13248
41	0.13696	0.11007
42	0.15488	0.12799
43	0.14592	0.19072
44	0.13696	0.13696
81	0.12351	0.1952
82	0.14144	0.12351
83	0.11903	0.13696
84	0.13248	0.13248
min	0.1190	0.1101
max	0.1549	0.1952
stdev	0.0116	0.0313
average	0.1364	0.1442
+3S	0.1712	0.2382
-3S	0.1016	0.0503



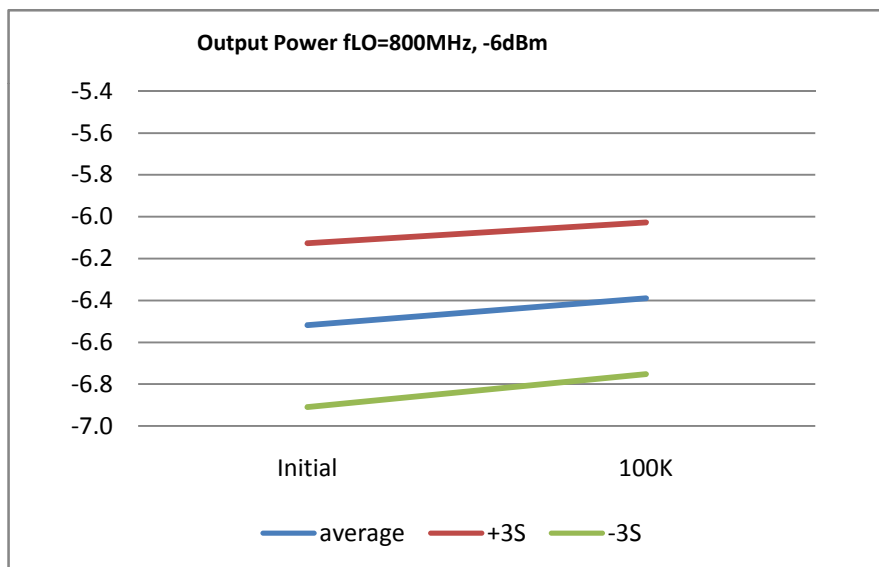
T# 12.0	I_PS5V	mA
SN	Initial	100K
40	43.2983	43.35339
80	43.39729	43.47568
41	43.2665	42.85083
42	43.28666	42.95072
43	42.4504	42.34872
44	43.43581	43.04478
81	42.729	42.47145
82	42.80291	42.36664
83	42.62777	42.29676
84	39.78217	39.23302
min	39.7822	39.2330
max	43.4358	43.0448
stdev	1.1719	1.2327
average	42.5477	42.1954
+3S	46.0634	45.8934
-3S	39.0319	38.4973



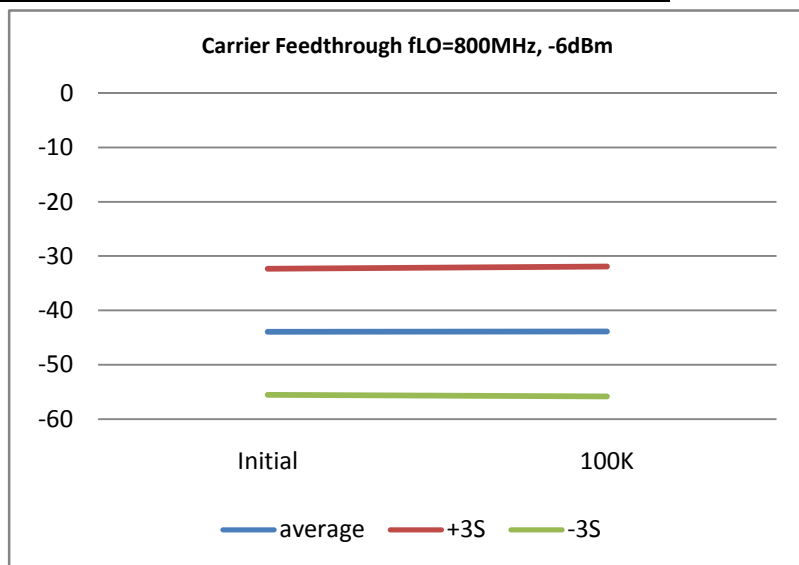
T# 13.0	I_PSmax	mA
SN	Initial	100K
40	43.41667	43.4749
80	43.51476	43.59001
41	43.38979	42.96875
42	43.40323	43.06237
43	42.55847	42.46126
44	43.55015	43.16136
81	42.84647	42.58579
82	42.9271	42.49531
83	42.74211	42.41199
84	39.91129	39.35542
min	39.9113	39.3554
max	43.5502	43.1614
stdev	1.1684	1.2304
average	42.6661	42.3128
+3S	46.1712	46.0041
-3S	39.1610	38.6214



T# 14.0	P_OUT_800M_5V_-6dBm	dBm
SN	Initial	100K
40	-6.4049	-6.29687
80	-6.94647	-6.43023
41	-6.39774	-6.30884
42	-6.38189	-6.26929
43	-6.64693	-6.36437
44	-6.45606	-6.36256
81	-6.442	-6.3139
82	-6.65986	-6.54179
83	-6.45662	-6.34569
84	-6.70786	-6.61067
min	-6.7079	-6.6107
max	-6.3819	-6.2693
stdev	0.1305	0.1207
average	-6.5186	-6.3896
+3S	-6.1272	-6.0274
-3S	-6.9100	-6.7519

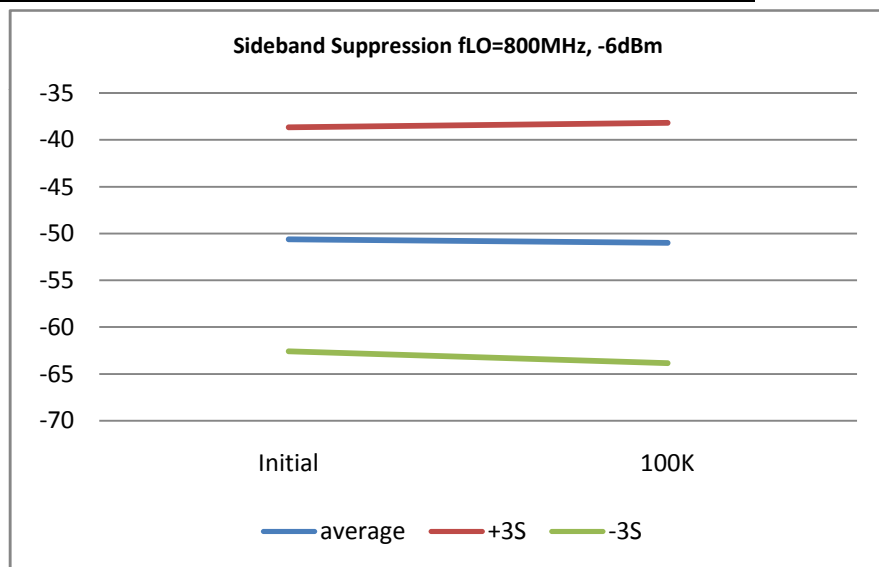


T# 15.0	P_FDTH_800M_5V_-6dBm	dBm
SN	Initial	100K
40	-43.21033	-43.0896
80	-42.57579	-42.0975
41	-42.6313	-42.5244
42	-43.4985	-43.5116
43	-42.9076	-42.7402
44	-39.08445	-39.0934
81	-43.80114	-43.5501
82	-42.34058	-42.1961
83	-44.6671	-44.3643
84	-52.61095	-52.9297
min	-52.6110	-52.9297
max	-39.0845	-39.0934
stdev	3.8701	3.9867
average	-43.9427	-43.8637
+3S	-32.3325	-31.9035
-3S	-55.5529	-55.8239

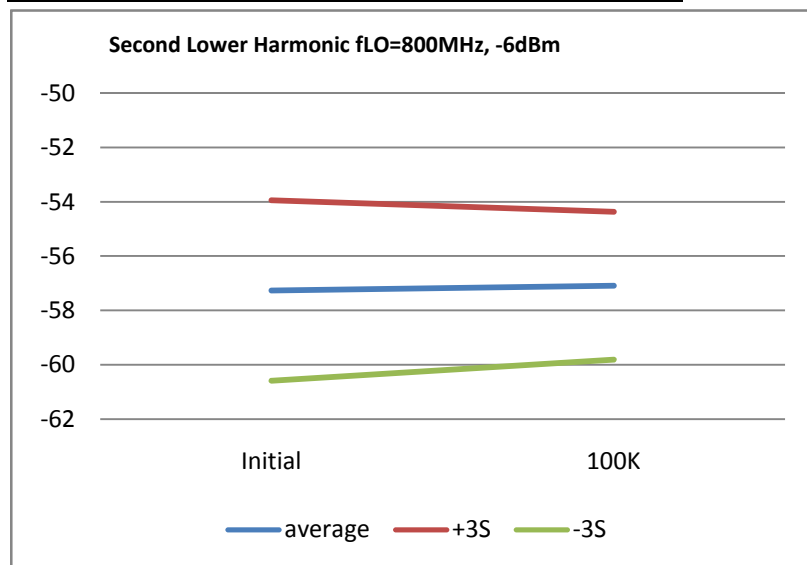




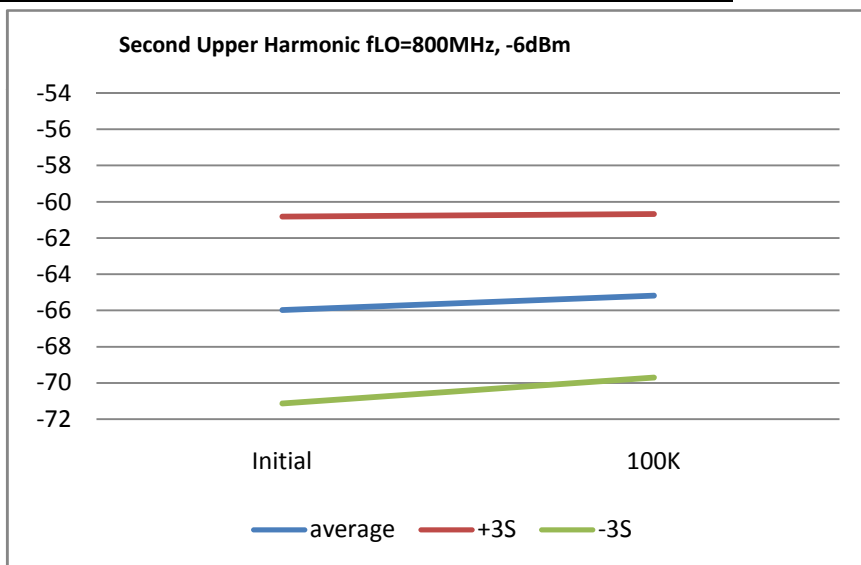
T# 16.0	P_SUPP_800M_5V_-6dBm	dBc
SN	Initial	100K
40	-49.20441	-49.46524
80	-55.7748	-58.3296
41	-48.89781	-49.31752
42	-48.00656	-47.81147
43	-58.07111	-57.27609
44	-48.63382	-48.31064
81	-49.02772	-49.96219
82	-54.12421	-56.45049
83	-45.70754	-45.3617
84	-52.4376	-53.48157
min	-58.0711	-57.2761
max	-45.7075	-45.3617
stdev	3.9912	4.2821
average	-50.6133	-50.9965
+3S	-38.6396	-38.1503
-3S	-62.5870	-63.8427



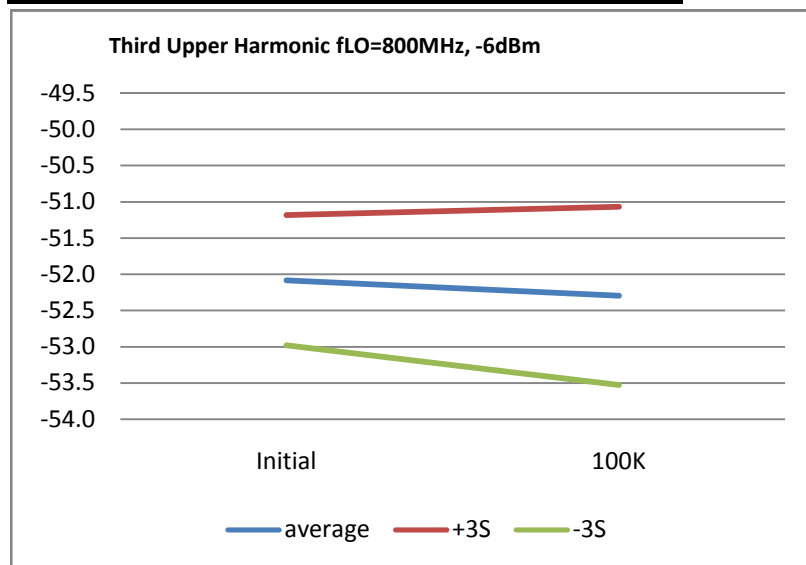
T# 17.0	P_2ndL_800M_5V_-6dBm	dBc
SN	Initial	100K
40	-57.34462	-57.2644
80	-56.9357	-57.2616
41	-58.46782	-58.5993
42	-56.8781	-57.0617
43	-57.72836	-56.3257
44	-55.2206	-55.7192
81	-57.22583	-56.5649
82	-58.78679	-57.866
83	-57.02867	-57.0909
84	-56.8046	-57.4643
min	-58.7868	-58.5993
max	-55.2206	-55.7192
stdev	1.1069	0.9071
average	-57.2676	-57.0865
+3S	-53.9470	-54.3652
-3S	-60.5882	-59.8078



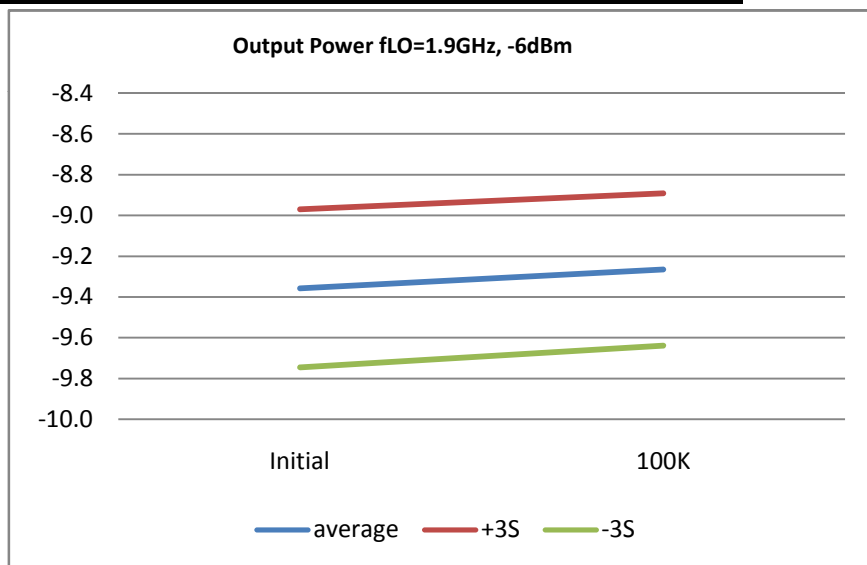
T# 18.0	P_2ndU_800M_5V_-6dBm	dBc
SN	Initial	100K
40	-68.84386	-67.42218
80	-65.25427	-64.92085
41	-68.75362	-67.13229
42	-64.99509	-64.23141
43	-66.75595	-67.77734
44	-64.2516	-64.31567
81	-66.11435	-65.13779
82	-66.67807	-65.24528
83	-66.88298	-63.7886
84	-63.30681	-63.86275
min	-68.7536	-67.7773
max	-63.3068	-63.7886
stdev	1.7204	1.5062
average	-65.9673	-65.1864
+3S	-60.8062	-60.6677
-3S	-71.1284	-69.7051



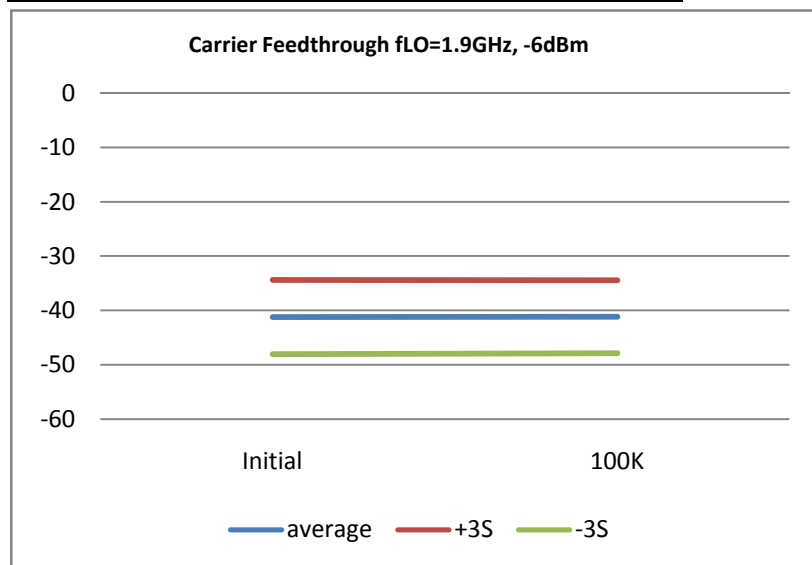
T# 19.0	P_3rdU_800M_5V_-6dBm	dBc
SN	Initial	100K
40	-51.19473	-51.1293
80	-53.16486	-52.3324
41	-51.83493	-51.9517
42	-52.2106	-52.5915
43	-52.28699	-52.6613
44	-51.98498	-52.4902
81	-52.13697	-52.3921
82	-52.60202	-52.7688
83	-51.99543	-51.8265
84	-51.61448	-51.7044
min	-52.6020	-52.7688
max	-51.6145	-51.7044
stdev	0.2996	0.4106
average	-52.0833	-52.2983
+3S	-51.1844	-51.0666
-3S	-52.9822	-53.5300



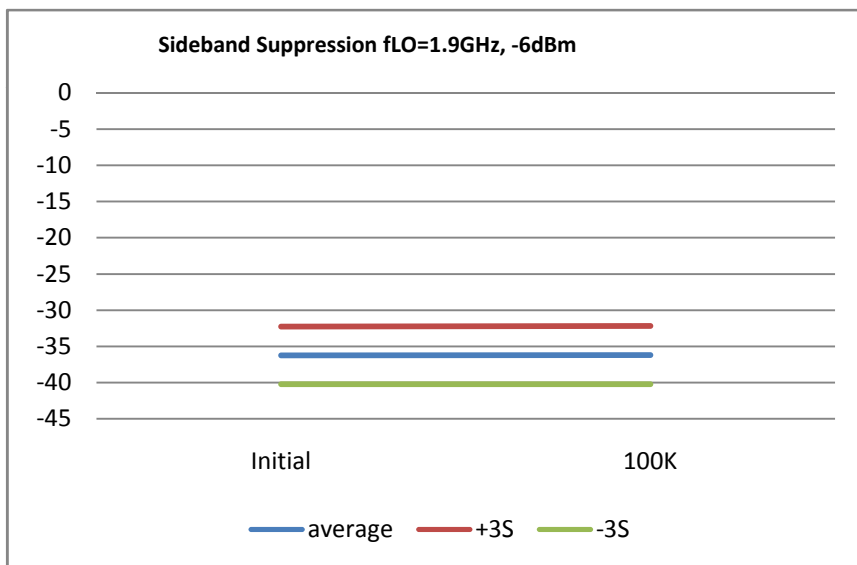
T# 20.0	P_OUT_1900M_5V_-6dBm	dBm
SN	Initial	100K
40	-9.21068	-9.06979
80	-9.73315	-9.20382
41	-9.19366	-9.13601
42	-9.29499	-9.17428
43	-9.35552	-9.27238
44	-9.23795	-9.15046
81	-9.36935	-9.25215
82	-9.43242	-9.33703
83	-9.36655	-9.2818
84	-9.61424	-9.51997
min	-9.6142	-9.5200
max	-9.1937	-9.1360
stdev	0.1292	0.1246
average	-9.3581	-9.2655
+3S	-8.9706	-8.8917
-3S	-9.7456	-9.6393



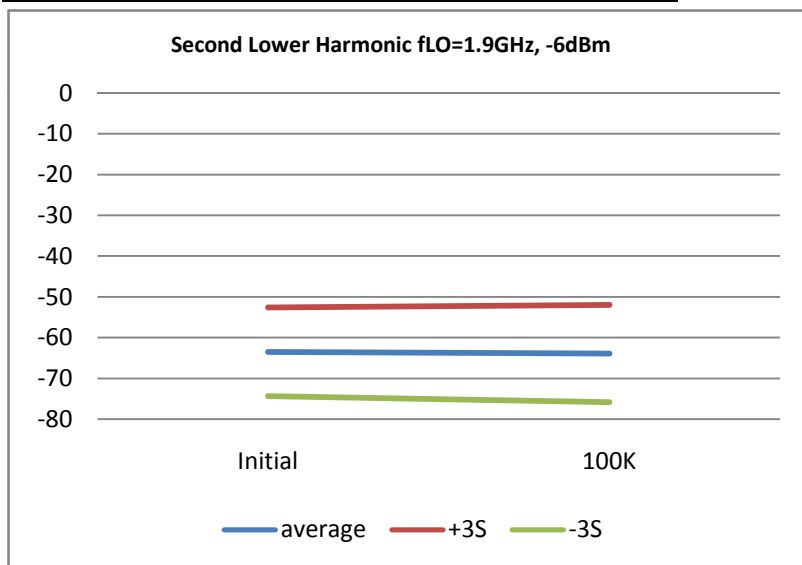
T# 21.0	P_FDTH_1900M_5V_-6dBm	dBm
SN	Initial	100K
40	-39.65215	-39.6854
80	-41.22395	-40.715
41	-40.60215	-40.6339
42	-41.22795	-41.1902
43	-39.8772	-39.8537
44	-39.87693	-39.9279
81	-40.88956	-40.679
82	-39.12954	-39.0238
83	-41.69581	-41.6806
84	-46.47395	-46.3228
min	-46.4740	-46.3228
max	-39.1295	-39.0238
stdev	2.2772	2.2432
average	-41.2216	-41.1640
+3S	-34.3901	-34.4345
-3S	-48.0531	-47.8934



T# 22.0	P_SUPP_1900M_5V_-6dBm	dBc
SN	Initial	100K
40	-36.54203	-36.60685
80	-39.60801	-39.57812
41	-35.12255	-35.12448
42	-36.28767	-36.17947
43	-37.59297	-37.51513
44	-35.75117	-35.5562
81	-35.99414	-36.11668
82	-38.63375	-38.58922
83	-34.44322	-34.334
84	-36.14749	-36.16944
min	-38.6338	-38.5892
max	-34.4432	-34.3340
stdev	1.3281	1.3363
average	-36.2466	-36.1981
+3S	-32.2622	-32.1893
-3S	-40.2310	-40.2069

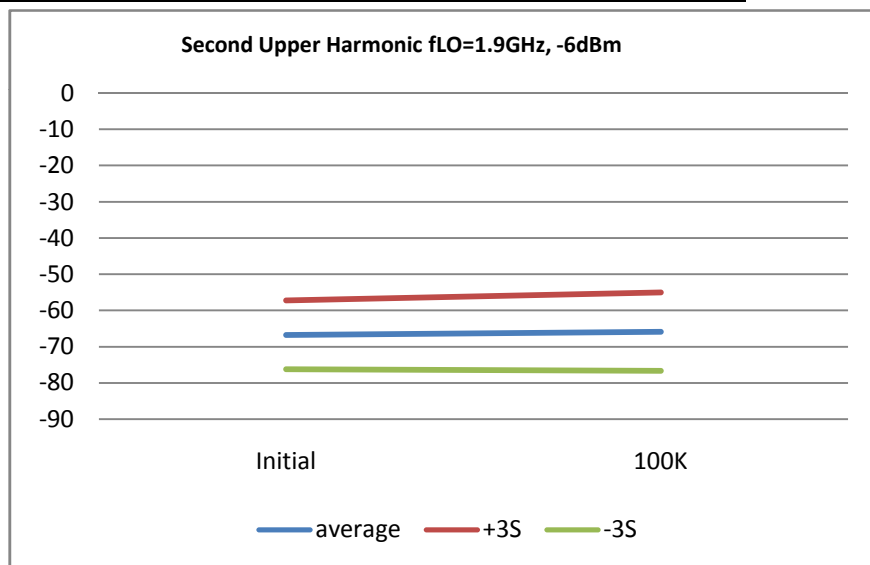


T# 23.0	P_2ndL_1900M_5V_-6dBm	dBc
SN	Initial	100K
40	-67.53575	-68.0631
80	-61.11323	-63.3518
41	-70.2448	-71.792
42	-62.85448	-63.0575
43	-62.87695	-64.6949
44	-59.7822	-59.8588
81	-65.35649	-64.5231
82	-66.15623	-66.5273
83	-59.63906	-60.0167
84	-60.98397	-60.9605
min	-70.2448	-71.7920
max	-59.6391	-59.8588
stdev	3.6225	3.9814
average	-63.4868	-63.9288
+3S	-52.6193	-51.9846
-3S	-74.3543	-75.8731

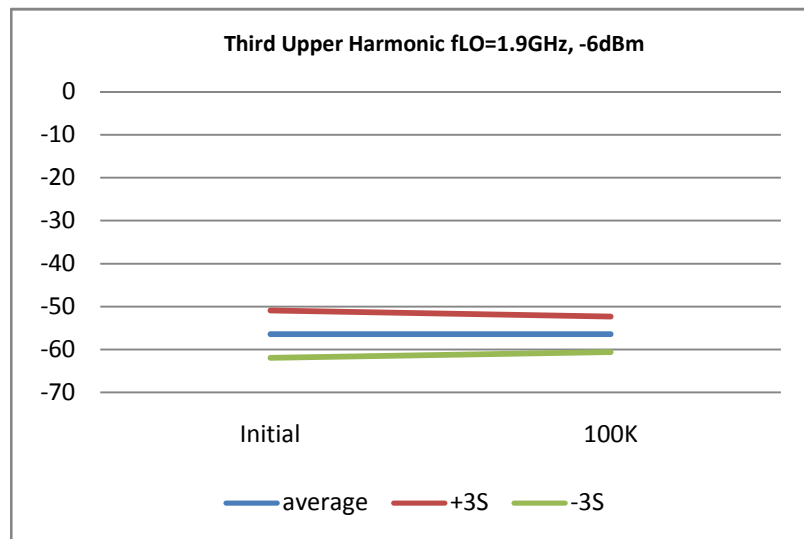




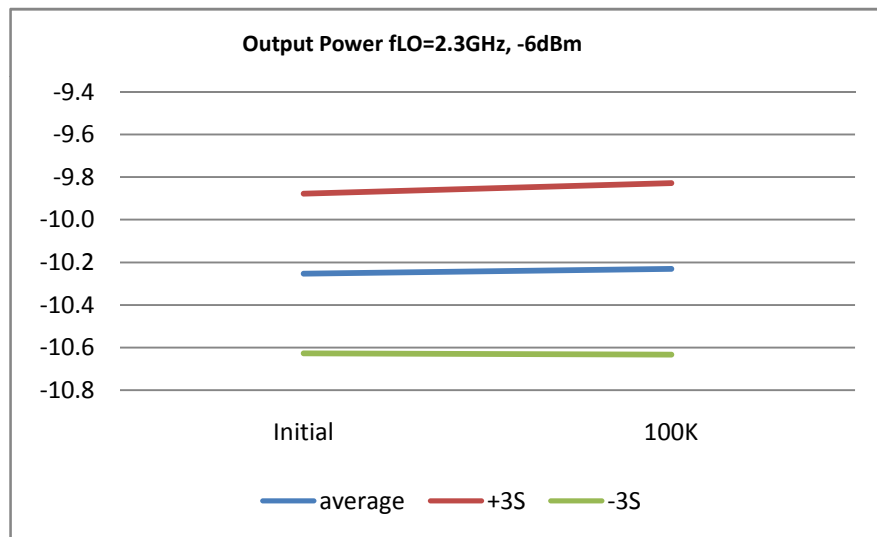
T# 24.0	P_2ndU_1900M_5V_-6dBm	dBc
SN	Initial	100K
40	-67.70024	-67.98931
80	-66.67001	-64.80231
41	-61.16465	-60.08454
42	-68.9181	-66.28912
43	-66.57801	-65.00902
44	-68.7318	-70.06108
81	-70.27907	-70.19398
82	-64.69827	-62.92579
83	-64.06399	-63.70823
84	-69.36616	-68.45821
min	-70.2791	-70.1940
max	-61.1647	-60.0845
stdev	3.1752	3.5993
average	-66.7250	-65.8412
+3S	-57.1995	-55.0434
-3S	-76.2505	-76.6391



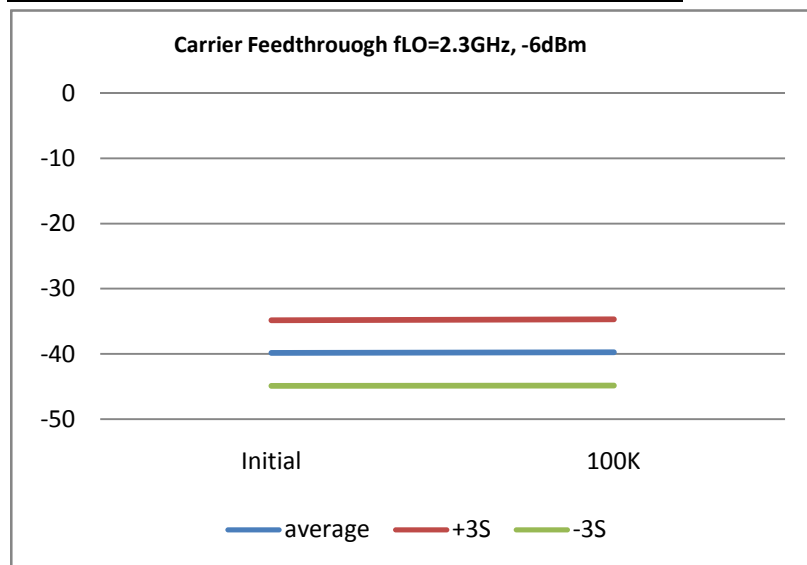
T# 25.0	P_3rdU_1900M_5V_-6dBm	dBc
SN	Initial	100K
40	-54.1935	-54.0792
80	-56.41755	-55.6801
41	-54.84215	-55.1703
42	-56.1082	-55.9664
43	-60.44255	-59.1019
44	-55.38042	-55.2034
81	-55.58315	-56.123
82	-57.5835	-57.9006
83	-56.66675	-56.6632
84	-55.0545	-55.6008
min	-60.4426	-59.1019
max	-54.8422	-55.1703
stdev	1.8446	1.3853
average	-56.4577	-56.4662
+3S	-50.9240	-52.3103
-3S	-61.9913	-60.6221



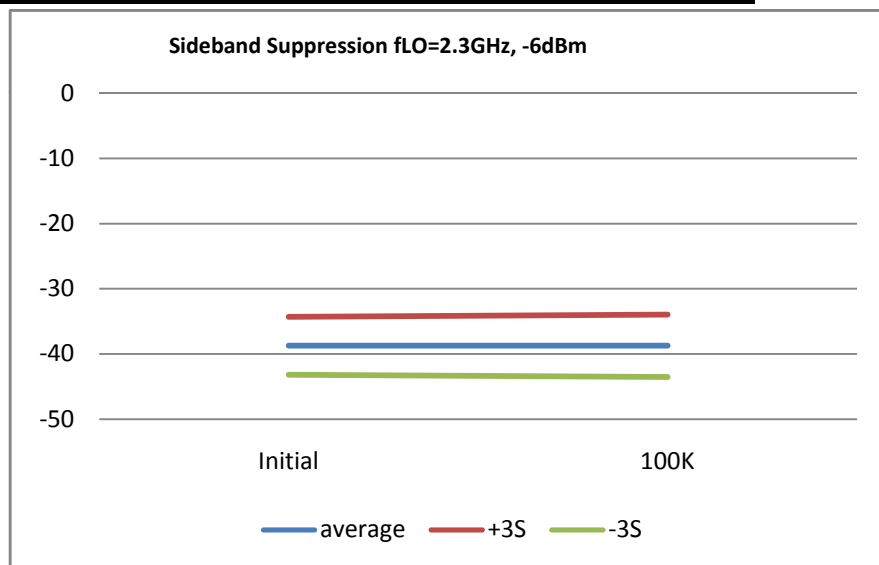
T# 26.0	P_OUT_2300M_5V_-6dBm	dBm
SN	Initial	100K
40	-10.07813	-9.97535
80	-10.48526	-10.11531
41	-10.07898	-10.06499
42	-10.21889	-10.16629
43	-10.34016	-10.30957
44	-10.09843	-10.08297
81	-10.28363	-10.23562
82	-10.25507	-10.25341
83	-10.27673	-10.24395
84	-10.46497	-10.48739
min	-10.4650	-10.4874
max	-10.0790	-10.0650
stdev	0.1250	0.1343
average	-10.2521	-10.2305
+3S	-9.8771	-9.8276
-3S	-10.6271	-10.6334



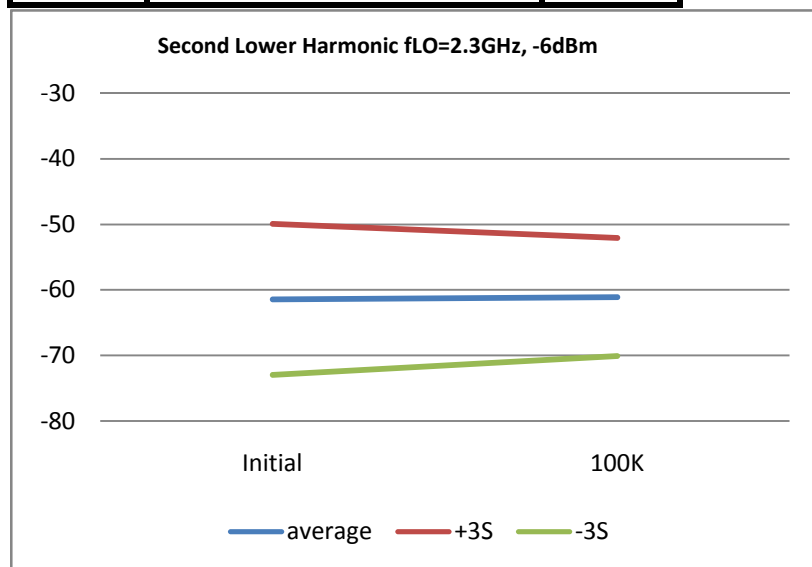
T# 27.0	P_FDTH_2300M_5V_-6dBm	dBm
SN	Initial	100K
40	-38.54677	-38.4664
80	-39.78568	-39.2462
41	-39.70629	-39.677
42	-39.79162	-39.7139
43	-38.91461	-38.8191
44	-38.96551	-38.9776
81	-39.62967	-39.3833
82	-37.89395	-37.7504
83	-40.33381	-40.2031
84	-43.58694	-43.5374
min	-43.5869	-43.5374
max	-37.8940	-37.7504
stdev	1.6799	1.6967
average	-39.8528	-39.7577
+3S	-34.8131	-34.6675
-3S	-44.8925	-44.8479



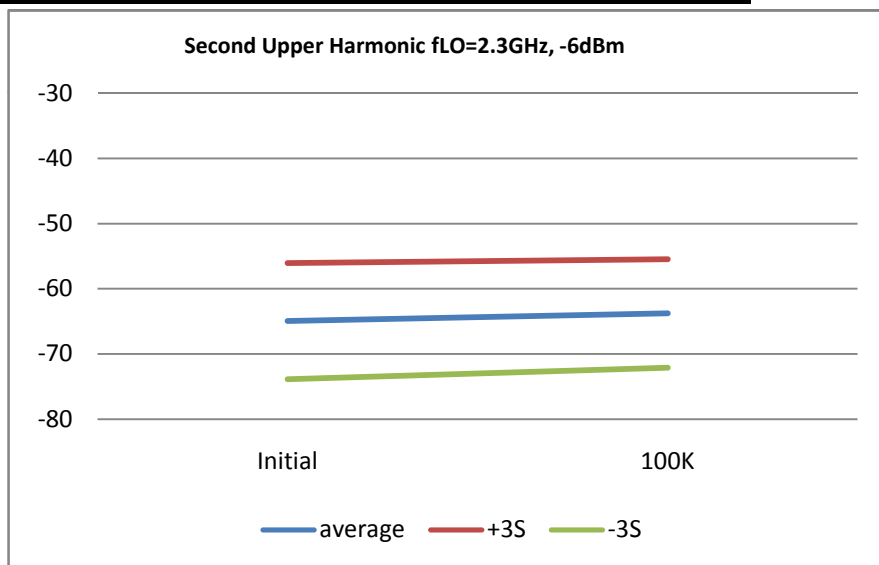
T# 28.0	P_SUPP_2300M_5V_-6dBm	dBc
SN	Initial	100K
40	-39.37348	-39.15152
80	-42.5493	-42.42432
41	-37.57383	-37.49194
42	-38.89455	-38.79669
43	-40.25092	-40.26407
44	-37.77762	-37.63937
81	-38.39827	-38.48447
82	-41.10387	-41.45604
83	-36.57129	-36.50825
84	-39.32625	-39.35141
min	-41.1039	-41.4560
max	-36.5713	-36.5083
stdev	1.4806	1.5987
average	-38.7371	-38.7490
+3S	-34.2954	-33.9529
-3S	-43.1788	-43.5452



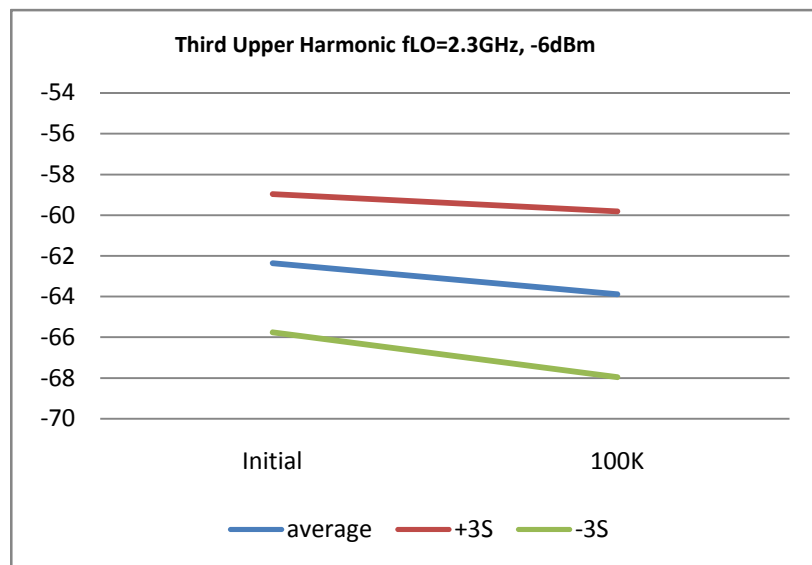
T# 29.0	P_2ndL_2300M_5V_-6dBm	dBc
SN	Initial	100K
40	-59.56551	-59.613
80	-61.7327	-58.9975
41	-63.06612	-62.3152
42	-60.11311	-60.8358
43	-60.20346	-59.2833
44	-58.83427	-58.9548
81	-60.37962	-60.9658
82	-58.43501	-58.7255
83	-60.11478	-59.7624
84	-70.31391	-67.9031
min	-70.3139	-67.9031
max	-58.4350	-58.7255
stdev	3.8430	3.0042
average	-61.4325	-61.0932
+3S	-49.9036	-52.0807
-3S	-72.9615	-70.1057



T# 30.0	P_2ndU_2300M_5V_-6dBm	dBc
SN	Initial	100K
40	-69.87802	-62.76011
80	-65.20147	-62.82569
41	-63.09616	-63.39084
42	-61.88959	-60.0208
43	-61.75925	-62.70869
44	-66.08611	-62.24778
81	-64.45142	-63.2729
82	-69.56097	-69.53946
83	-64.03241	-63.66669
84	-68.83337	-65.39043
min	-69.5610	-69.5395
max	-61.7593	-60.0208
stdev	2.9690	2.7727
average	-64.9637	-63.7797
+3S	-56.0566	-55.4617
-3S	-73.8707	-72.0977

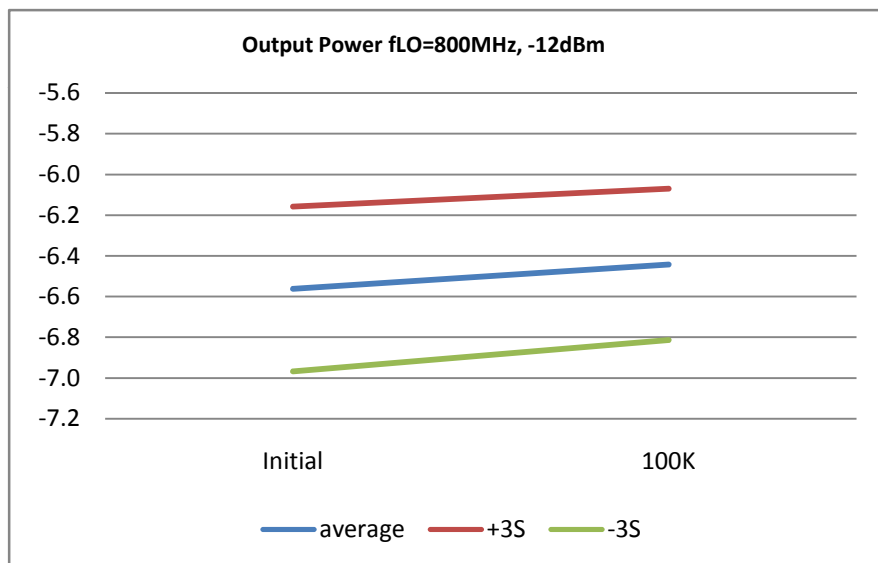


T# 31.0	P_3rdU_2300M_5V_-6dBm	dBc
SN	Initial	100K
40	-61.65044	-61.6682
80	-63.04016	-62.4523
41	-62.2254	-62.7615
42	-60.61202	-62.9436
43	-62.67278	-67.0079
44	-62.13042	-63.6688
81	-63.97909	-63.0208
82	-63.85889	-64.1412
83	-61.77985	-63.5454
84	-61.64725	-64.0259
min	-63.9791	-67.0079
max	-60.6120	-62.7615
stdev	1.1299	1.3573
average	-62.3632	-63.8894
+3S	-58.9735	-59.8175
-3S	-65.7529	-67.9612

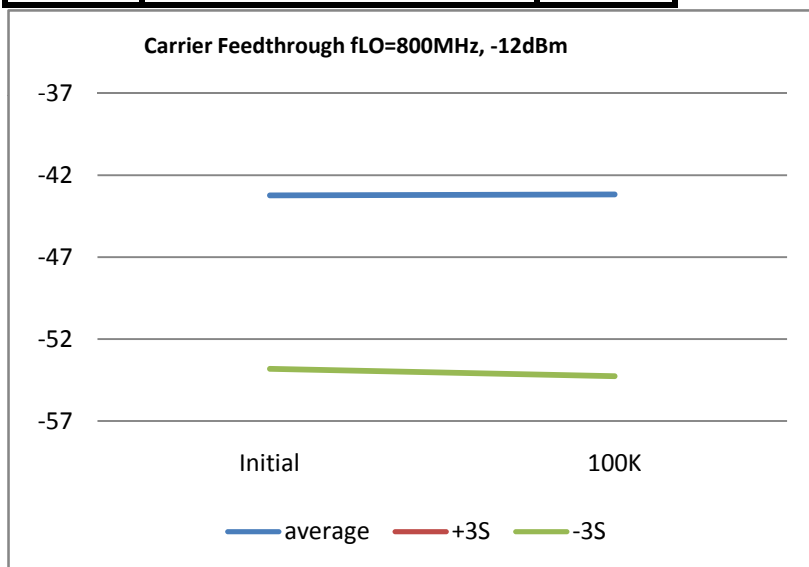




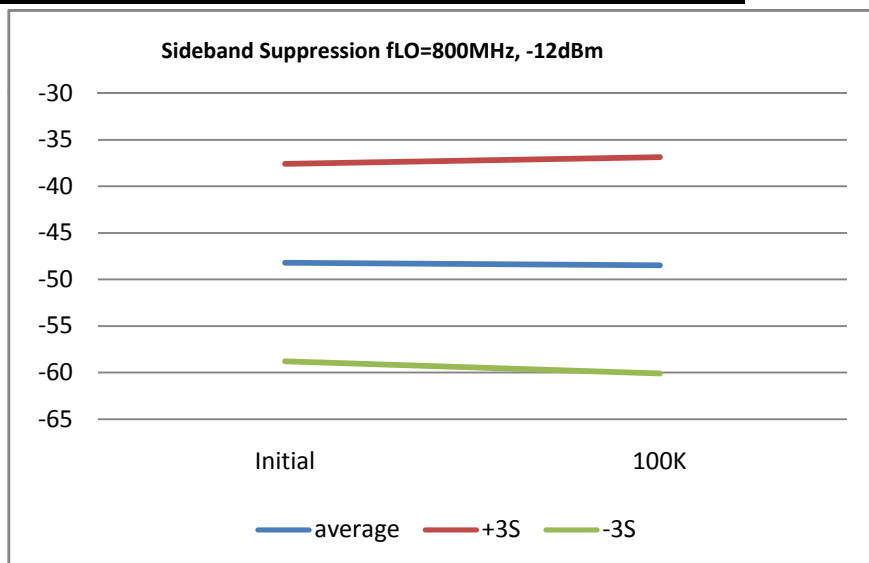
T# 32.0	P_OUT_800M_5V_-12dBm	dBm
SN	Initial	100K
40	-6.44327	-6.33447
80	-6.82838	-6.47106
41	-6.43956	-6.35877
42	-6.42233	-6.32397
43	-6.69979	-6.41765
44	-6.49869	-6.41251
81	-6.47855	-6.3623
82	-6.69565	-6.58588
83	-6.49869	-6.3942
84	-6.7652	-6.67772
min	-6.7652	-6.6777
max	-6.4223	-6.3240
stdev	0.1350	0.1238
average	-6.5623	-6.4416
+3S	-6.1572	-6.0703
-3S	-6.9674	-6.8129



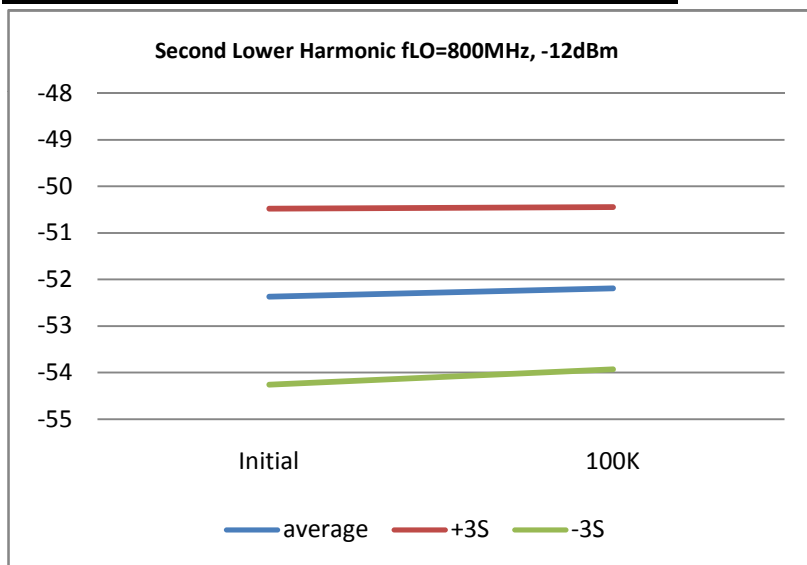
T# 33.0	P_FDTM_800M_5V_-12dBm	dBm
SN	Initial	100K
40	-42.62271	-42.4391
80	-41.85687	-41.4586
41	-42.3698	-42.2717
42	-42.70489	-42.718
43	-42.26725	-42.0165
44	-38.70693	-38.7213
81	-43.1114	-42.9053
82	-41.72362	-41.591
83	-43.81924	-43.5247
84	-51.11075	-51.5875
min	-51.1108	-51.5875
max	-38.7069	-38.7213
stdev	3.5282	3.6970
average	-43.2267	-43.1670
+3S	-32.6421	-32.0759
-3S	-53.8113	-54.2580



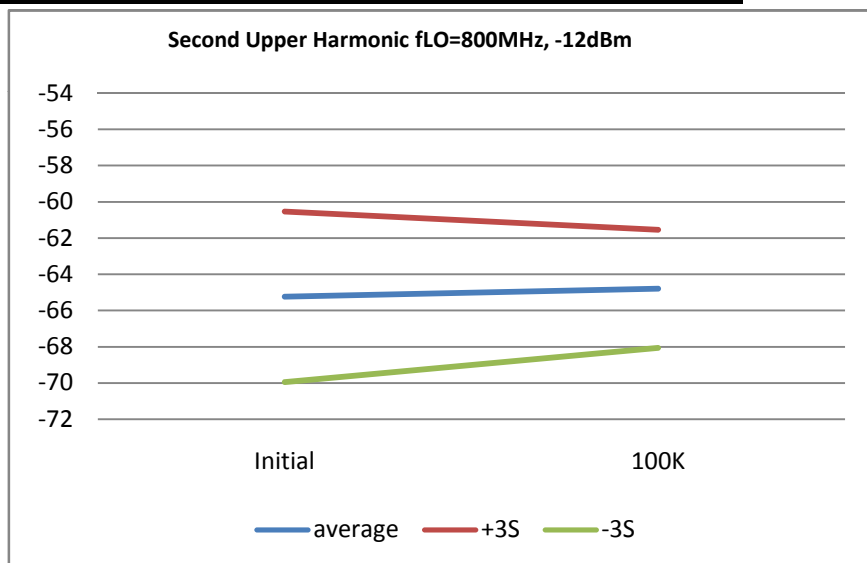
T# 34.0	P_SUPP_800M_5V_-12dBm	dBc
SN	Initial	100K
40	-46.94849	-46.92512
80	-56.51926	-58.76743
41	-46.41336	-46.66319
42	-45.61772	-45.55386
43	-53.2439	-53.16721
44	-45.45109	-45.30933
81	-47.59518	-48.10804
82	-52.75018	-54.25718
83	-43.96359	-43.87589
84	-50.58063	-50.96759
min	-53.2439	-54.2572
max	-43.9636	-43.8759
stdev	3.5364	3.8690
average	-48.2020	-48.4878
+3S	-37.5928	-36.8807
-3S	-58.8111	-60.0949



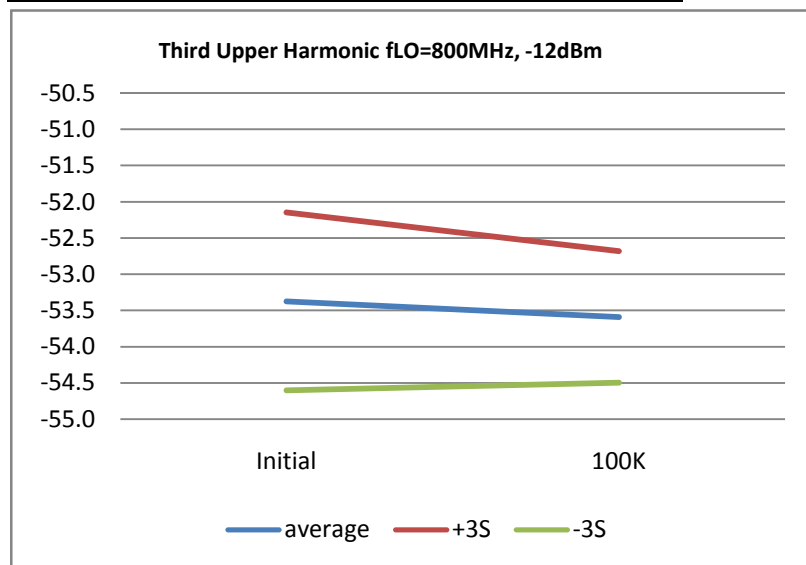
T# 35.0	P_2ndL_800M_5V_-12dBm	dBc
SN	Initial	100K
40	-51.41946	-51.5669
80	-52.19316	-51.8951
41	-53.26909	-52.8116
42	-52.20795	-51.9757
43	-52.51322	-51.787
44	-51.65223	-51.5466
81	-51.58459	-51.5257
82	-52.09572	-52.3024
83	-52.45267	-52.4688
84	-53.21239	-53.0976
min	-53.2691	-53.0976
max	-51.5846	-51.5257
stdev	0.6301	0.5811
average	-52.3735	-52.1894
+3S	-50.4833	-50.4460
-3S	-54.2636	-53.9328



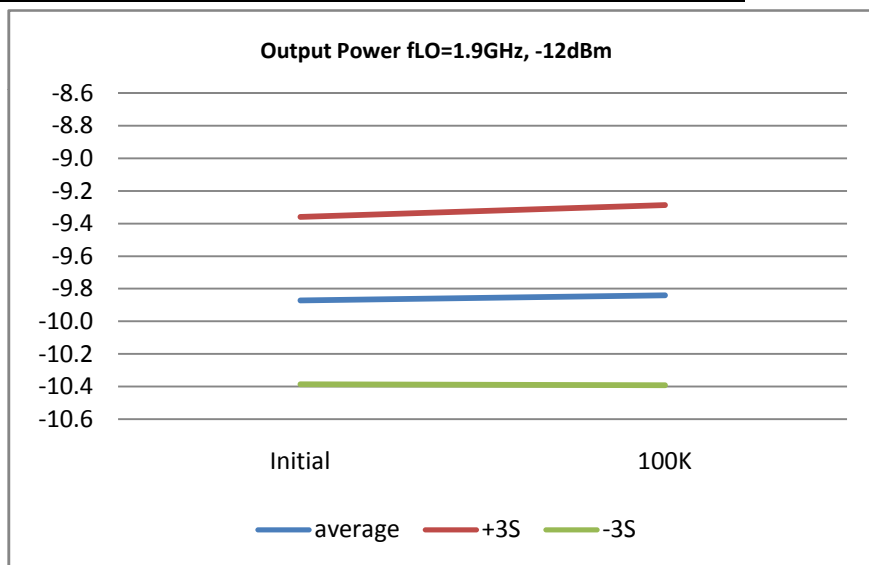
T# 36.0	P_2ndU_800M_5V_-12dBm	dBc
SN	Initial	100K
40	-66.71329	-67.34454
80	-64.85638	-63.66654
41	-67.97444	-66.72515
42	-65.03229	-64.09049
43	-66.81864	-65.49615
44	-63.58636	-63.9142
81	-65.62107	-65.25552
82	-65.16264	-64.54685
83	-64.40199	-65.12284
84	-63.32652	-63.24936
min	-67.9744	-66.7252
max	-63.3265	-63.2494
stdev	1.5703	1.0862
average	-65.2405	-64.8001
+3S	-60.5295	-61.5414
-3S	-69.9515	-68.0587



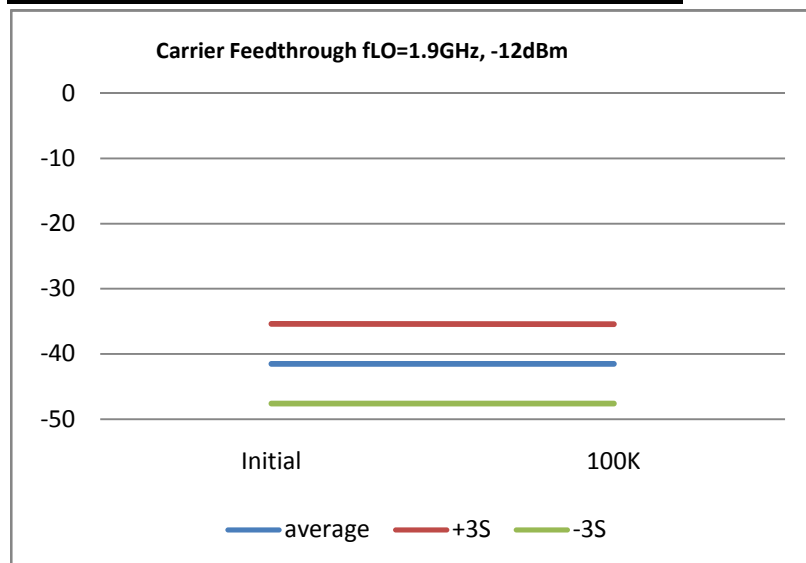
T# 37.0	P_3rdU_800M_5V_-12dBm	dBc
SN	Initial	100K
40	-52.52887	-52.7551
80	-53.64079	-53.8634
41	-52.83789	-53.4341
42	-53.75921	-54.0513
43	-53.6731	-53.7596
44	-53.44845	-53.2616
81	-53.33745	-53.5183
82	-53.89804	-53.9566
83	-53.26111	-53.4871
84	-52.77801	-53.2469
min	-53.8980	-54.0513
max	-52.7780	-53.2469
stdev	0.4097	0.3028
average	-53.3742	-53.5894
+3S	-52.1452	-52.6812
-3S	-54.6031	-54.4977



T# 38.0	P_OUT_1900M_5V_-12dBm	dBm
SN	Initial	100K
40	-9.63429	-9.50774
80	-10.13611	-9.65428
41	-9.66357	-9.65612
42	-9.78753	-9.72745
43	-9.96076	-9.93337
44	-9.67719	-9.64227
81	-9.85307	-9.7985
82	-9.92795	-9.87951
83	-9.9158	-9.8704
84	-10.19274	-10.21308
min	-10.1927	-10.2131
max	-9.6636	-9.6423
stdev	0.1710	0.1843
average	-9.8723	-9.8401
+3S	-9.3593	-9.2873
-3S	-10.3853	-10.3929

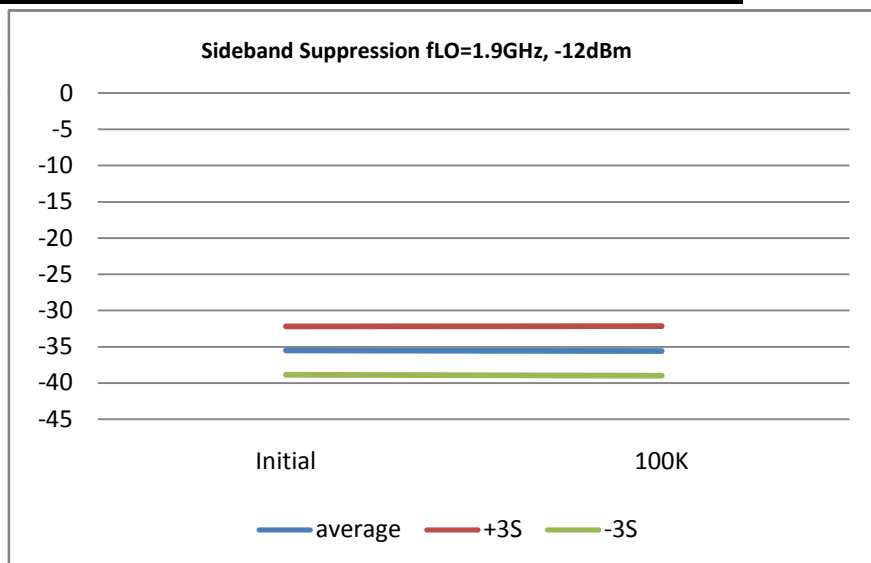


T# 39.0	P_FDTH_1900M_5V_-12dBm	dBm
SN	Initial	100K
40	-40.13699	-40.0791
80	-41.12698	-40.6897
41	-41.52037	-41.6349
42	-41.36176	-41.3797
43	-40.49257	-40.5578
44	-39.75758	-39.8513
81	-41.34151	-41.1938
82	-39.4828	-39.4173
83	-42.11798	-42.0549
84	-46.01053	-46.0291
min	-46.0105	-46.0291
max	-39.4828	-39.4173
stdev	2.0316	2.0315
average	-41.5106	-41.5148
+3S	-35.4160	-35.4203
-3S	-47.6053	-47.6093

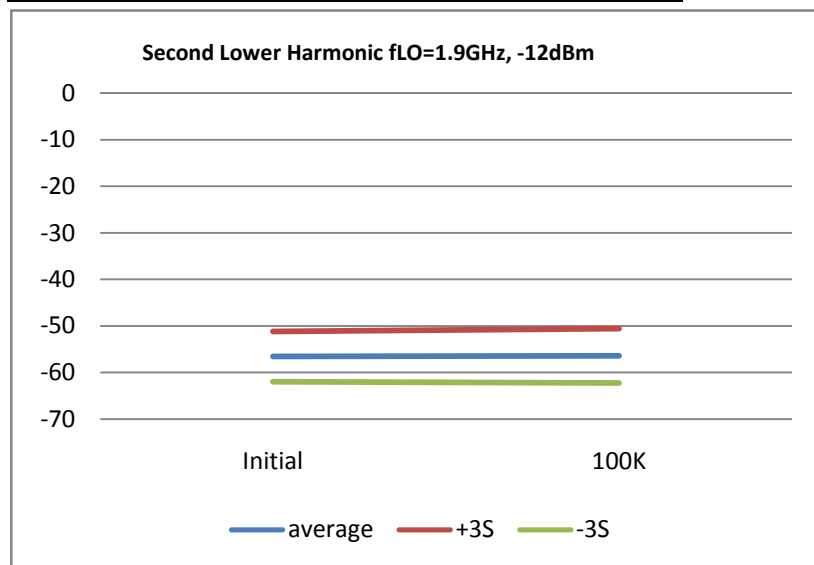




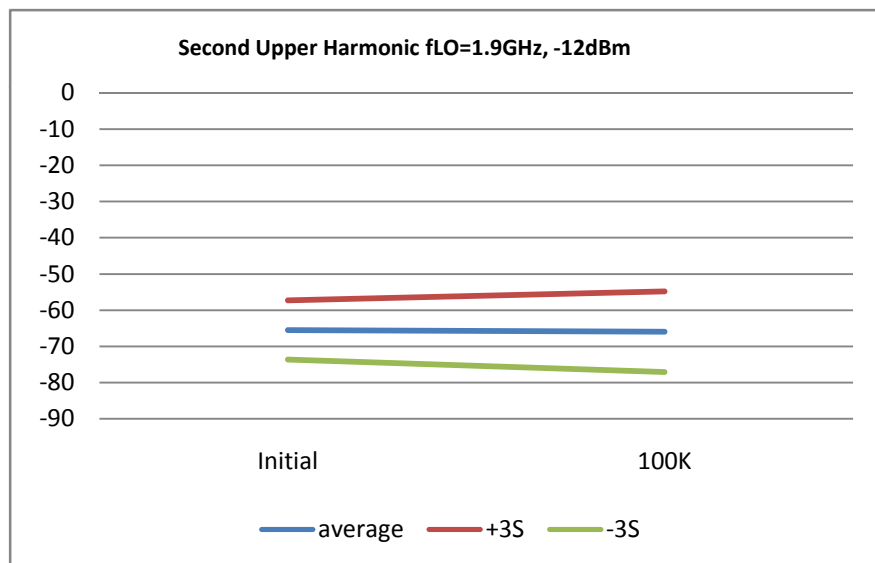
T# 40.0	P_SUPP_1900M_5V_-12dBm	dBc
SN	Initial	100K
40	-35.87472	-35.89506
80	-37.811	-37.76599
41	-34.93423	-35.011
42	-35.37355	-35.46117
43	-36.57525	-36.6503
44	-34.55964	-34.53358
81	-35.66446	-35.79333
82	-37.34908	-37.45696
83	-33.90413	-33.91341
84	-35.97837	-36.03656
min	-37.3491	-37.4570
max	-33.9041	-33.9134
stdev	1.1074	1.1419
average	-35.5423	-35.6070
+3S	-32.2202	-32.1814
-3S	-38.8645	-39.0326



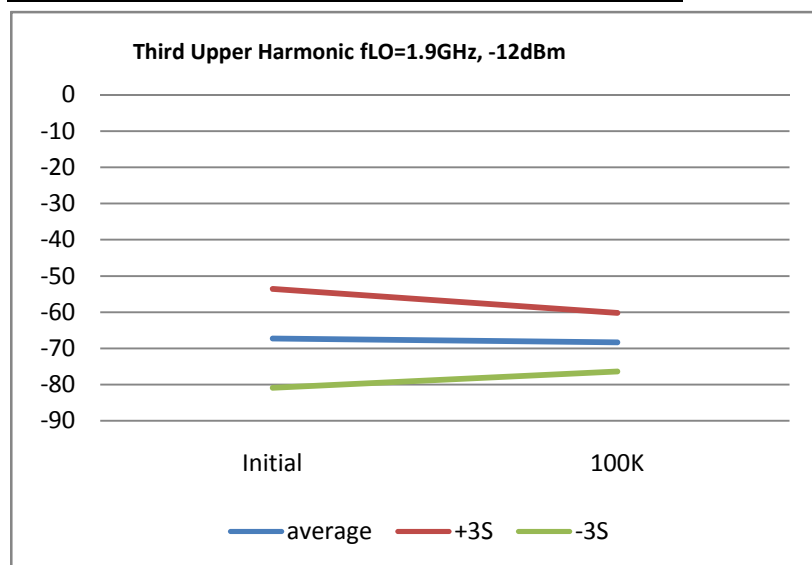
T# 41.0	P_2ndL_1900M_5V_-12dBm	dBc
SN	Initial	100K
40	-59.2887	-58.8855
80	-56.58751	-56.6506
41	-58.17468	-58.2545
42	-56.52039	-55.4431
43	-56.24437	-57.9012
44	-55.32871	-55.1693
81	-57.25616	-57.3787
82	-59.74331	-58.8582
83	-54.77963	-54.2557
84	-54.3801	-53.7881
min	-59.7433	-58.8582
max	-54.3801	-53.7881
stdev	1.8033	1.9472
average	-56.5534	-56.3811
+3S	-51.1434	-50.5395
-3S	-61.9634	-62.2227



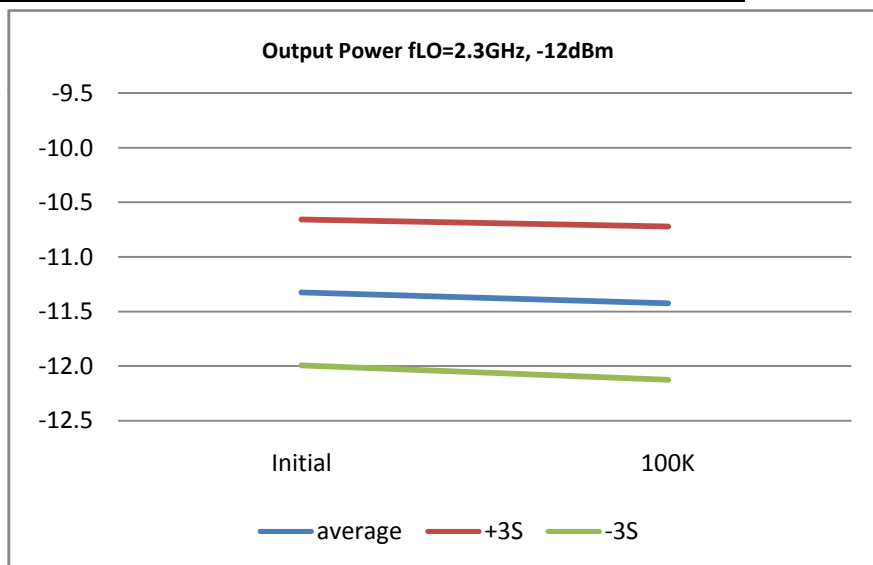
T# 42.0	P_2ndU_1900M_5V_-12dBm	dBc
SN	Initial	100K
40	-66.17976	-65.13538
80	-65.45699	-63.89648
41	-61.28361	-60.95106
42	-66.28007	-65.45141
43	-63.42944	-64.52763
44	-65.77834	-64.93677
81	-70.6845	-73.48337
82	-65.68862	-64.92097
83	-64.20432	-64.54815
84	-66.322	-68.75288
min	-70.6845	-73.4834
max	-61.2836	-60.9511
stdev	2.7257	3.7017
average	-65.4589	-65.9465
+3S	-57.2817	-54.8413
-3S	-73.6360	-77.0517



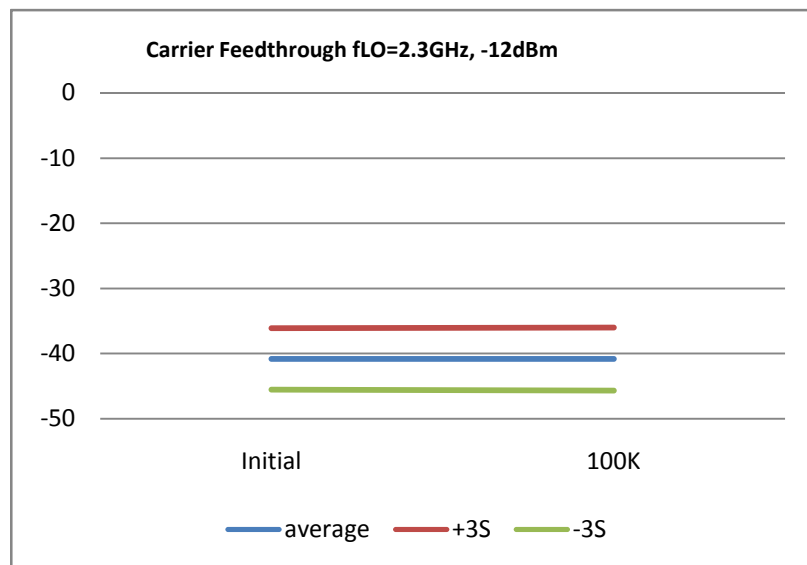
T# 43.0	P_3rdU_1900M_5V_-12dBm	dBc
SN	Initial	100K
40	-61.48191	-62.7083
80	-63.41961	-63.068
41	-63.58735	-67.2278
42	-63.54781	-66.3456
43	-77.341	-73.5112
44	-64.79172	-64.6802
81	-64.4305	-66.8817
82	-68.39765	-68.3828
83	-68.28431	-69.3459
84	-67.51997	-69.9765
min	-77.3410	-73.5112
max	-63.5478	-64.6802
stdev	4.5576	2.7032
average	-67.2375	-68.2940
+3S	-53.5648	-60.1843
-3S	-80.9103	-76.4037



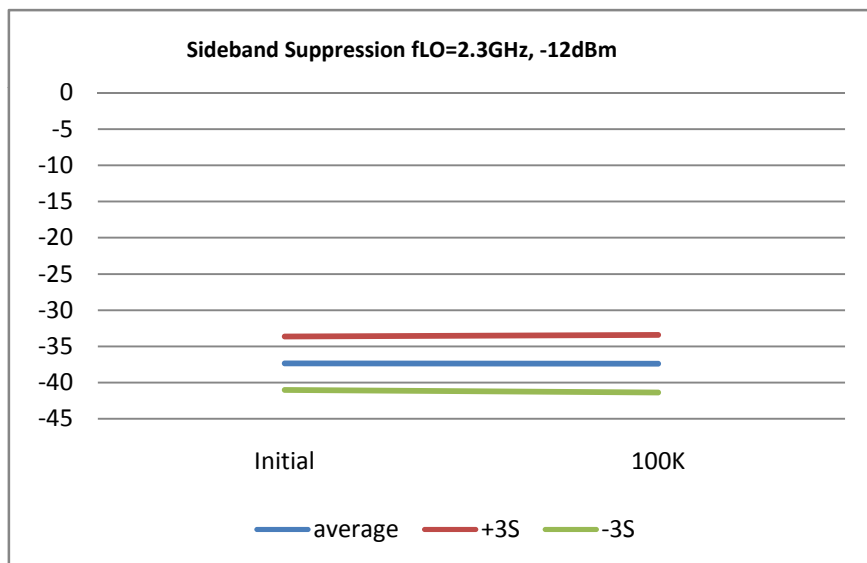
T# 44.0	P_OUT_2300M_5V_-12dBm	dBm
SN	Initial	100K
40	-10.98384	-10.8997
80	-11.53503	-11.07994
41	-11.04738	-11.17083
42	-11.23129	-11.32417
43	-11.60841	-11.66821
44	-11.0412	-11.13059
81	-11.30854	-11.38417
82	-11.29551	-11.38278
83	-11.4495	-11.50406
84	-11.61228	-11.81732
min	-11.6123	-11.8173
max	-11.0412	-11.1306
stdev	0.2223	0.2343
average	-11.3243	-11.4228
+3S	-10.6574	-10.7200
-3S	-11.9911	-12.1256



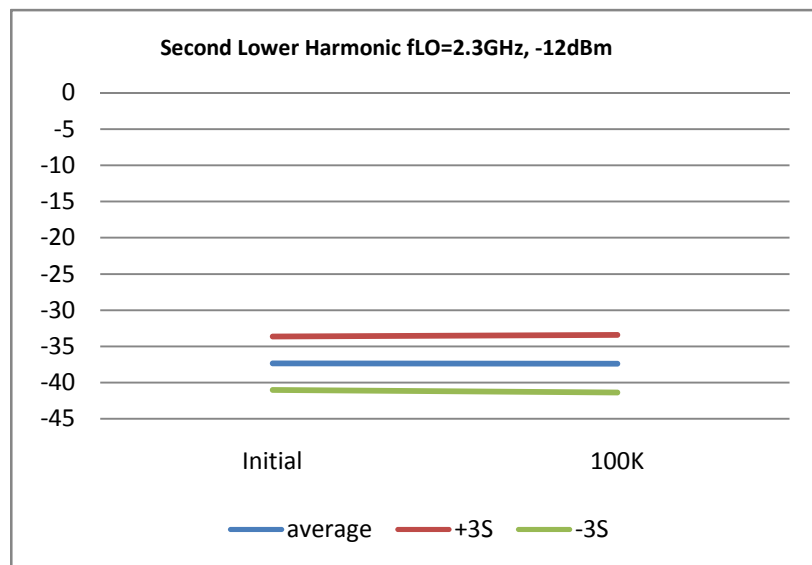
T# 45.0	P_FDTH_2300M_5V_-12dBm	dBm
SN	Initial	100K
40	-39.50181	-39.3824
80	-40.63414	-40.0539
41	-40.80413	-40.8331
42	-40.78768	-40.893
43	-40.17747	-40.1482
44	-39.77354	-39.8145
81	-40.60432	-40.4876
82	-38.83182	-38.8006
83	-41.41684	-41.3796
84	-44.18848	-44.2969
min	-44.1885	-44.2969
max	-38.8318	-38.8006
stdev	1.5680	1.6061
average	-40.8230	-40.8317
+3S	-36.1189	-36.0134
-3S	-45.5272	-45.6500



T# 46.0	P_SUPP_2300M_5V_-12dBm	dBc
SN	Initial	100K
40	-37.61074	-37.55342
80	-39.46382	-39.19994
41	-36.81527	-36.79763
42	-37.5299	-37.66558
43	-38.95733	-38.99634
44	-36.09609	-35.93382
81	-36.46103	-36.5416
82	-38.94041	-39.26933
83	-35.83477	-35.79136
84	-38.00484	-37.98582
min	-38.9573	-39.2693
max	-35.8348	-35.7914
stdev	1.2256	1.3249
average	-37.3300	-37.3727
+3S	-33.6530	-33.3980
-3S	-41.0069	-41.3474

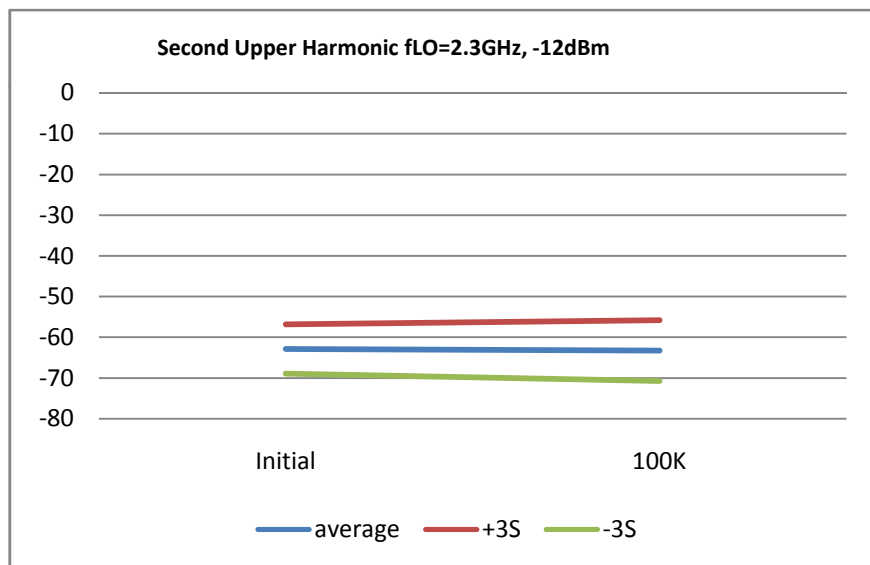


T# 47.0	P_2ndL_2300M_5V_-12dBm	dBc
SN	Initial	100K
40	-59.9251	-58.0358
80	-39.46382	-39.1999
41	-36.81527	-36.7976
42	-37.5299	-37.6656
43	-38.95733	-38.9963
44	-36.09609	-35.9338
81	-36.46103	-36.5416
82	-38.94041	-39.2693
83	-35.83477	-35.7914
84	-38.00484	-37.9858
min	-38.9573	-39.2693
max	-35.8348	-35.7914
stdev	1.2256	1.3249
average	-37.3300	-37.3727
+3S	-33.6530	-33.3980
-3S	-41.0069	-41.3474

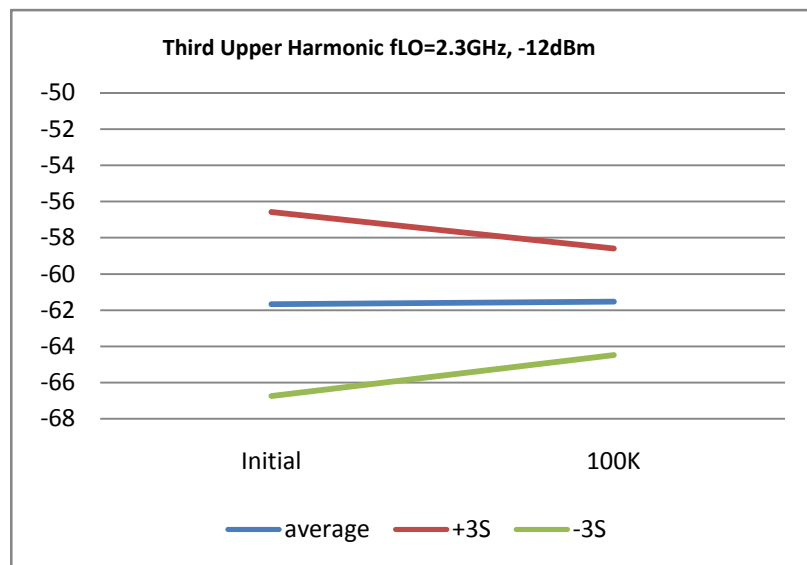




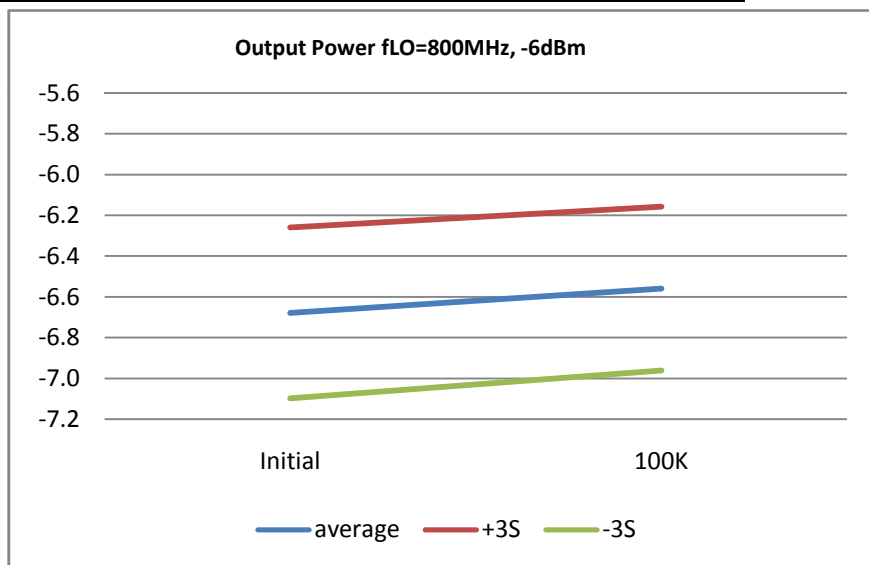
T# 48.0	P_2ndU_2300M_5V_-12dBm	dBc
SN	Initial	100K
40	-60.83591	-59.83096
80	-62.2382	-61.17096
41	-63.3685	-60.81337
42	-59.82768	-60.36859
43	-61.66413	-63.29947
44	-60.96841	-62.91432
81	-65.57469	-63.77822
82	-62.07824	-68.08659
83	-64.59518	-64.98411
84	-64.60143	-61.76461
min	-65.5747	-68.0866
max	-59.8277	-60.3686
stdev	2.0171	2.4854
average	-62.8348	-63.2512
+3S	-56.7835	-55.7950
-3S	-68.8861	-70.7073



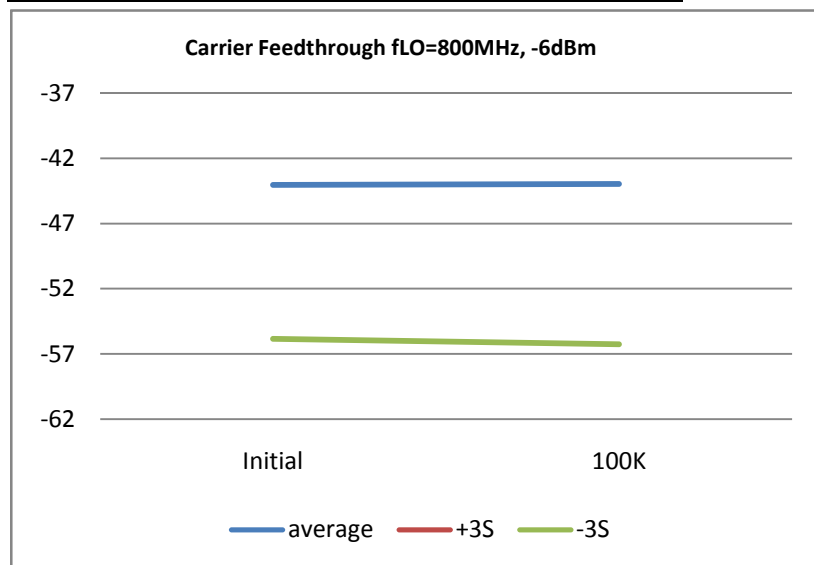
T# 49.0	P_3rdU_2300M_5V_-12dBm	dBc
SN	Initial	100K
40	-62.99934	-63.546
80	-61.74117	-62.0266
41	-63.19764	-61.5397
42	-60.48152	-61.5967
43	-59.92308	-59.4835
44	-63.3496	-62.4621
81	-62.94643	-61.6287
82	-60.04633	-61.6646
83	-59.92943	-62.7602
84	-63.4484	-61.1416
min	-63.4484	-62.7602
max	-59.9231	-59.4835
stdev	1.6935	0.9822
average	-61.6653	-61.5346
+3S	-56.5847	-58.5879
-3S	-66.7459	-64.4813



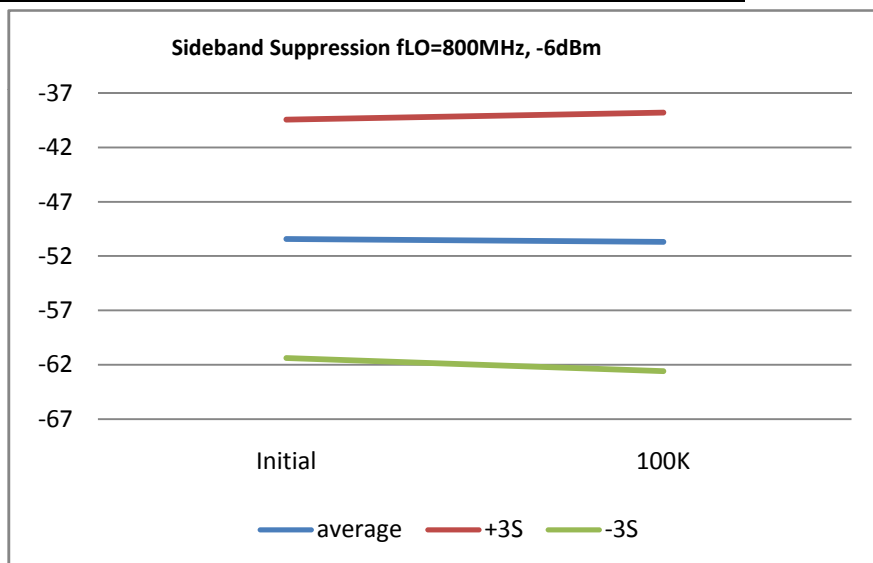
T# 50.0	P_OUT_800M_3V_-6dBm	dBm
SN	Initial	100K
40	-6.5607	-6.45637
80	-7.00067	-6.58619
41	-6.55193	-6.47284
42	-6.53913	-6.43933
43	-6.79946	-6.5231
44	-6.61295	-6.5231
81	-6.59361	-6.48103
82	-6.80045	-6.69488
83	-6.61086	-6.50749
84	-6.91482	-6.83207
min	-6.9148	-6.8321
max	-6.5391	-6.4393
stdev	0.1398	0.1342
average	-6.6779	-6.5592
+3S	-6.2584	-6.1566
-3S	-7.0974	-6.9619



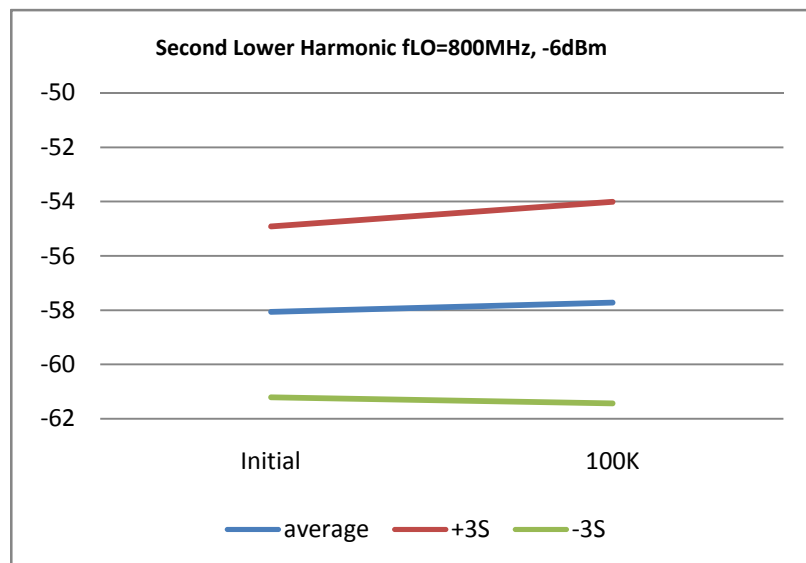
T# 51.0	P_FDTH_800M_3V_-6dBm	dBm
SN	Initial	100K
40	-43.34559	-43.1352
80	-42.64472	-42.1815
41	-42.56519	-42.4218
42	-43.54975	-43.6185
43	-43.07123	-42.8364
44	-39.28809	-39.3088
81	-43.87181	-43.5884
82	-42.33729	-42.1862
83	-44.65346	-44.2892
84	-52.93911	-53.3952
min	-52.9391	-53.3952
max	-39.2881	-39.3088
stdev	3.9358	4.1022
average	-44.0345	-43.9556
+3S	-32.2272	-31.6491
-3S	-55.8418	-56.2620



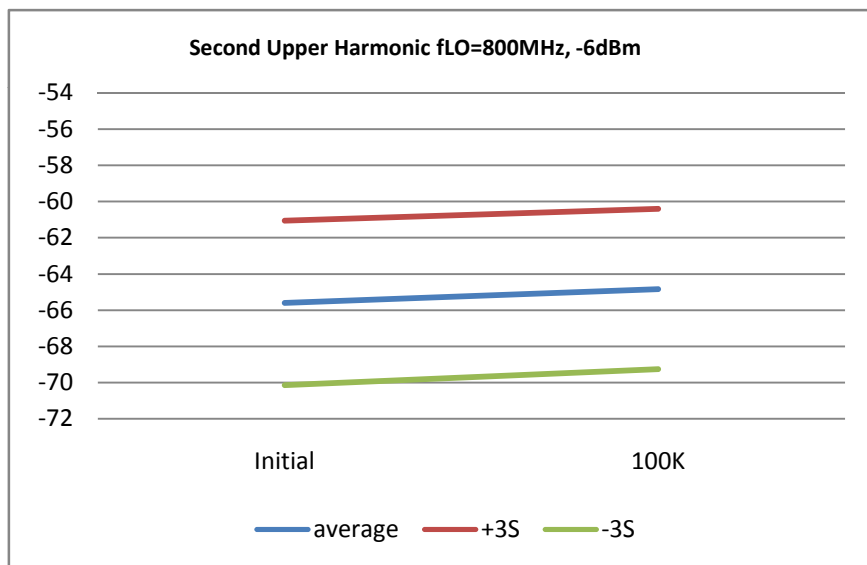
T# 52.0	P_SUPP_800M_3V_-6dBm	dBc
SN	Initial	100K
40	-49.62172	-49.61446
80	-55.82624	-57.86838
41	-48.86418	-49.32478
42	-48.16035	-47.88942
43	-57.36256	-57.36901
44	-48.55321	-48.19611
81	-48.96526	-49.68433
82	-53.75822	-55.23479
83	-45.98666	-45.58749
84	-51.59381	-52.10925
min	-57.3626	-57.3690
max	-45.9867	-45.5875
stdev	3.6548	3.9686
average	-50.4055	-50.6744
+3S	-39.4412	-38.7685
-3S	-61.3698	-62.5803



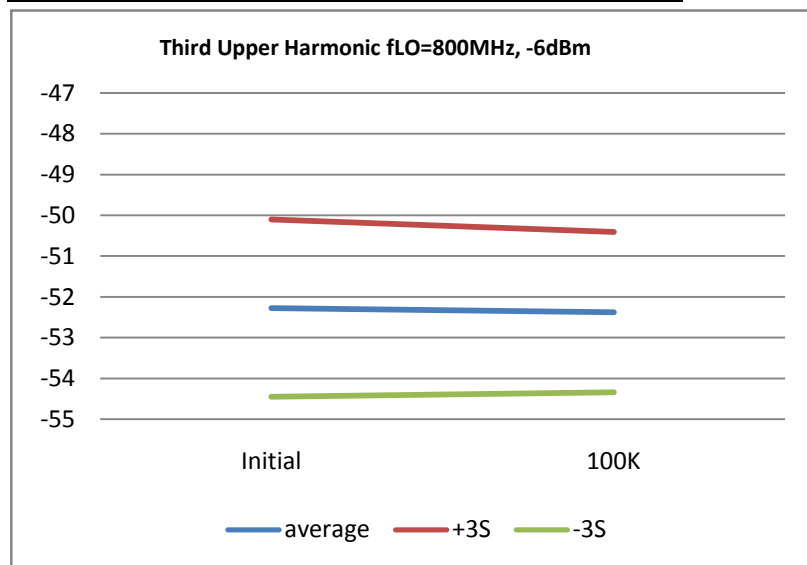
T# 53.0	P_2ndL_800M_3V_-6dBm	dBc
SN	Initial	100K
40	-57.64206	-57.5563
80	-56.75034	-57.5131
41	-59.93258	-59.9813
42	-57.03493	-57.2847
43	-58.06271	-57.3386
44	-56.69175	-55.8218
81	-57.67524	-57.1658
82	-58.99216	-58.8055
83	-57.69169	-57.3709
84	-58.39525	-57.968
min	-59.9326	-59.9813
max	-56.6918	-55.8218
stdev	1.0484	1.2375
average	-58.0595	-57.7171
+3S	-54.9142	-54.0045
-3S	-61.2048	-61.4297



T# 54.0	P_2ndU_800M_3V_-6dBm	dBc
SN	Initial	100K
40	-67.43827	-68.2661
80	-65.09655	-64.31519
41	-67.73367	-67.71246
42	-66.45325	-63.60504
43	-67.53925	-66.13561
44	-64.26149	-64.61675
81	-65.15665	-65.19237
82	-65.10832	-64.0767
83	-65.02723	-63.93522
84	-63.49582	-63.36042
min	-67.7337	-67.7125
max	-63.4958	-63.3604
stdev	1.5131	1.4745
average	-65.5970	-64.8293
+3S	-61.0576	-60.4059
-3S	-70.1363	-69.2527

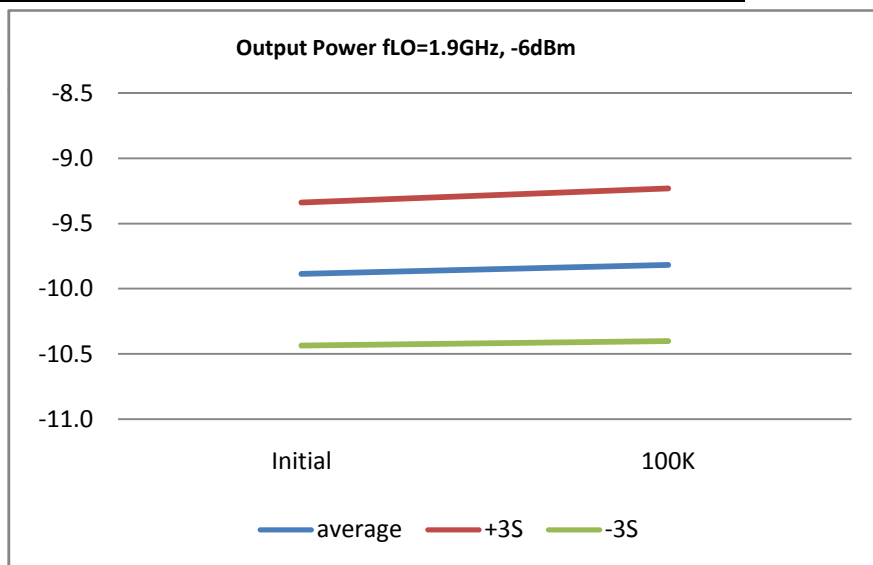


T# 55.0	P_3rdU_800M_3V_-6dBm	dBc
SN	Initial	100K
40	-51.02961	-51.07
80	-52.61922	-52.5692
41	-51.64639	-52.0992
42	-52.60757	-52.7919
43	-53.29356	-53.6193
44	-52.04465	-52.0439
81	-52.61759	-52.512
82	-52.78505	-52.4136
83	-52.23663	-52.1604
84	-50.96637	-51.3516
min	-53.2936	-53.6193
max	-50.9664	-51.3516
stdev	0.7258	0.6570
average	-52.2747	-52.3740
+3S	-50.0972	-50.4030
-3S	-54.4522	-54.3449

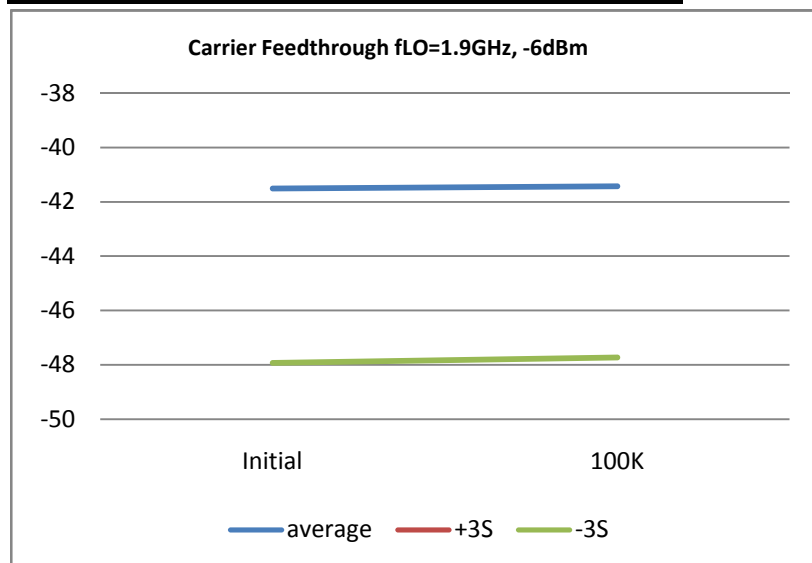




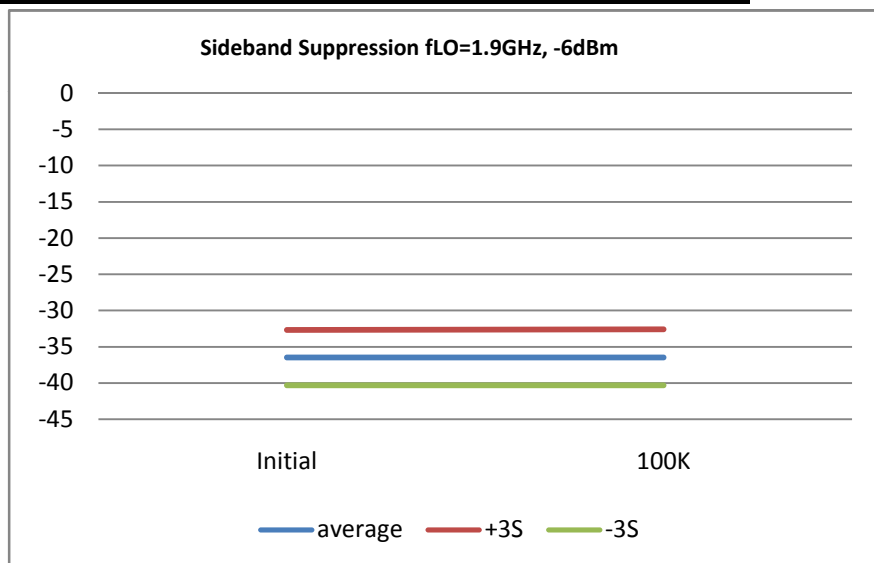
T# 56.0	P_OUT_1900M_3V_-6dBm	dBm
SN	Initial	100K
40	-9.72585	-9.58435
80	-10.11171	-9.69801
41	-9.71271	-9.65614
42	-9.79485	-9.70207
43	-9.86138	-9.80092
44	-9.73887	-9.67205
81	-9.88838	-9.79261
82	-9.9243	-9.83541
83	-9.8742	-9.8014
84	-10.29992	-10.27032
min	-10.2999	-10.2703
max	-9.7127	-9.6561
stdev	0.1827	0.1953
average	-9.8868	-9.8164
+3S	-9.3389	-9.2304
-3S	-10.4348	-10.4023



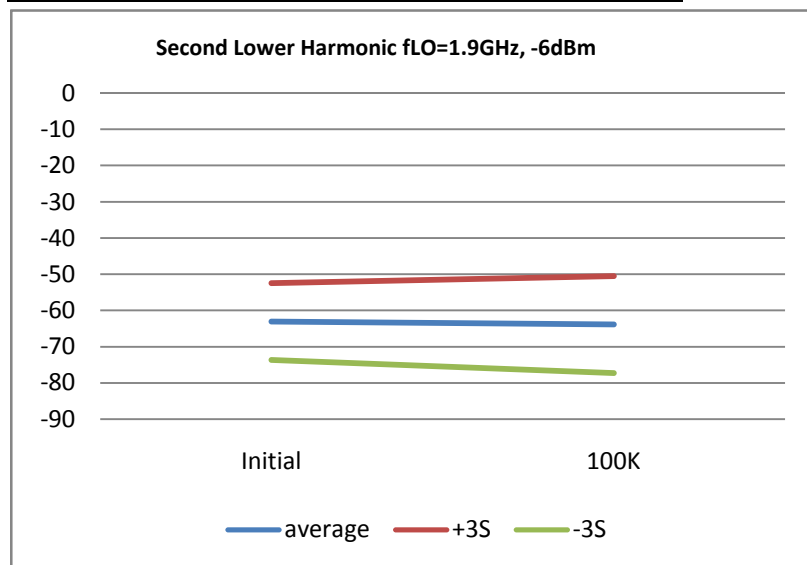
T# 57.0	P_FDTH_1900M_3V_-6dBm	dBm
SN	Initial	100K
40	-40.05492	-40.0783
80	-41.4888	-41.1756
41	-40.48601	-40.4382
42	-41.62303	-41.5722
43	-40.3083	-40.2965
44	-40.37454	-40.432
81	-41.14333	-40.8795
82	-39.64468	-39.5119
83	-42.04382	-42.0052
84	-46.44693	-46.2607
min	-46.4469	-46.2607
max	-39.6447	-39.5119
stdev	2.1403	2.1022
average	-41.5088	-41.4245
+3S	-35.0879	-35.1181
-3S	-47.9298	-47.7310



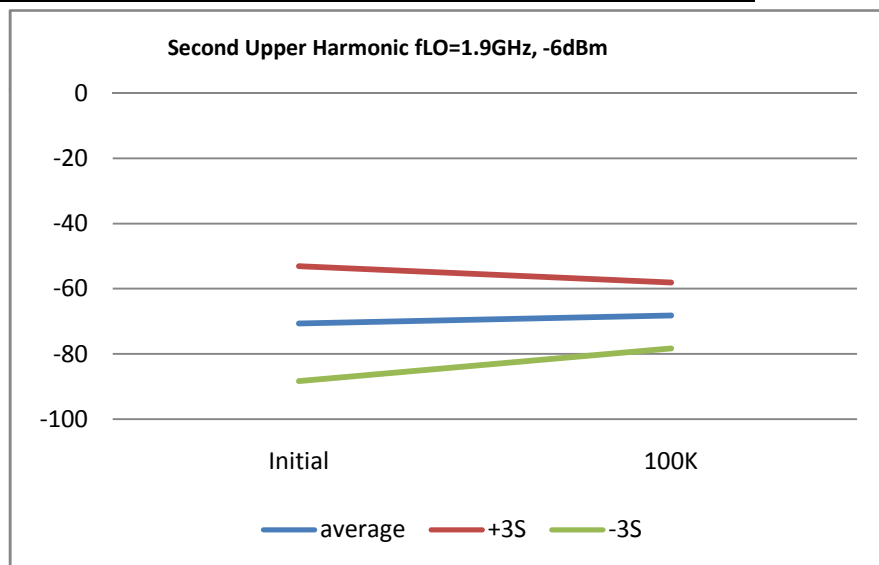
T# 58.0	P_SUPP_1900M_3V_-6dBm	dBc
SN	Initial	100K
40	-36.93061	-37.00636
80	-39.60527	-39.59172
41	-35.44049	-35.42455
42	-36.64828	-36.61232
43	-37.90023	-37.82828
44	-35.9377	-35.83465
81	-36.33449	-36.45528
82	-38.71439	-38.70732
83	-34.78444	-34.68706
84	-36.3126	-36.29921
min	-38.7144	-38.7073
max	-34.7844	-34.6871
stdev	1.2722	1.2859
average	-36.5091	-36.4811
+3S	-32.6924	-32.6234
-3S	-40.3258	-40.3388



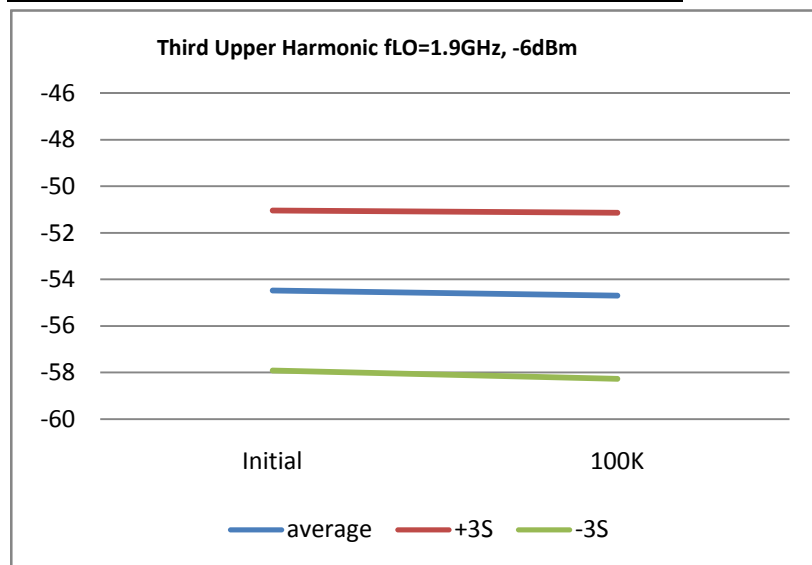
T# 59.0	P_2ndL_1900M_3V_-6dBm	dBc
SN	Initial	100K
40	-69.16631	-70.6893
80	-61.28302	-63.1732
41	-68.49264	-72.1296
42	-63.03167	-62.1148
43	-62.15581	-64.1766
44	-59.38021	-59.554
81	-65.13081	-66.065
82	-66.91245	-67.3803
83	-59.96886	-60.4013
84	-59.24726	-59.3486
min	-68.4926	-72.1296
max	-59.2473	-59.3486
stdev	3.5273	4.4663
average	-63.0400	-63.8962
+3S	-52.4582	-50.4974
-3S	-73.6218	-77.2951



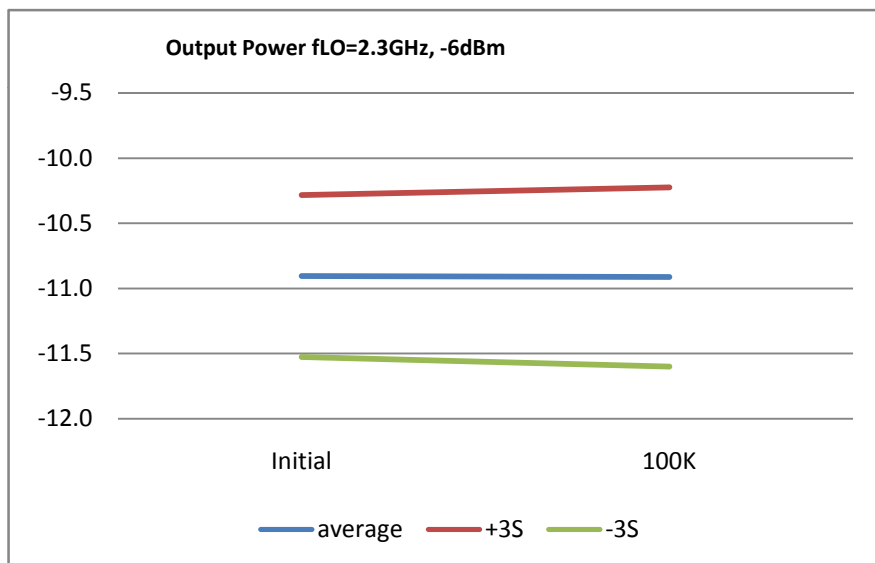
T# 60.0	P_2ndU_1900M_3V_-6dBm	dBc
SN	Initial	100K
40	-67.71229	-67.33711
80	-64.62028	-65.97495
41	-65.56506	-65.61913
42	-71.36938	-70.32551
43	-67.74027	-66.92917
44	-67.67384	-65.45819
81	-82.92917	-74.63084
82	-69.58286	-67.37033
83	-65.44063	-64.81636
84	-74.97971	-70.53415
min	-82.9292	-74.6308
max	-65.4406	-64.8164
stdev	5.8736	3.3648
average	-70.6601	-68.2105
+3S	-53.0392	-58.1162
-3S	-88.2811	-78.3048



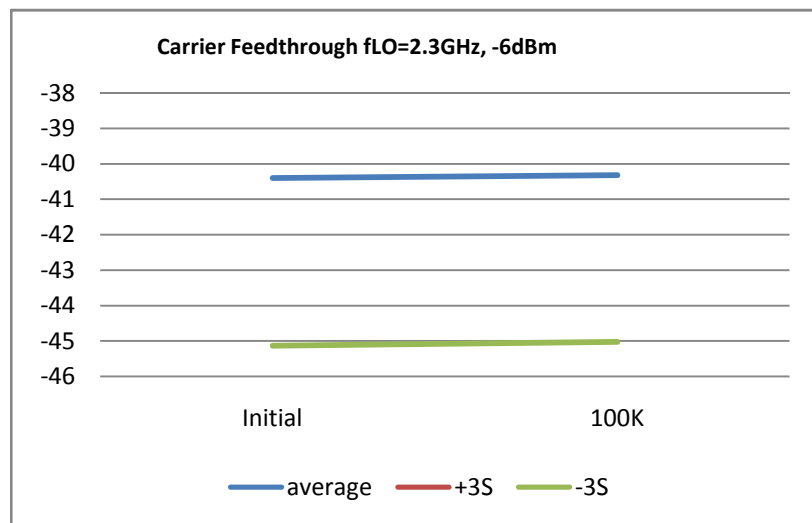
T# 61.0	P_3rdU_1900M_3V_-6dBm	dBc
SN	Initial	100K
40	-53.38711	-52.922
80	-54.7544	-54.2992
41	-54.14041	-53.8723
42	-53.8321	-54.2188
43	-55.14141	-55.4604
44	-53.82967	-54.0253
81	-53.6078	-53.8442
82	-56.7771	-56.2698
83	-53.34591	-53.4277
84	-55.15363	-56.4722
min	-56.7771	-56.4722
max	-53.3459	-53.4277
stdev	1.1447	1.1896
average	-54.4785	-54.6988
+3S	-51.0443	-51.1300
-3S	-57.9127	-58.2676



T# 62.0	P_OUT_2300M_3V_-6dBm	dBm
SN	Initial	100K
40	-10.69315	-10.60114
80	-10.90848	-10.70255
41	-10.69923	-10.70854
42	-10.82332	-10.80082
43	-10.95956	-10.95452
44	-10.72438	-10.7243
81	-10.91001	-10.91121
82	-10.85021	-10.87625
83	-10.90213	-10.88035
84	-11.36636	-11.4361
min	-11.3664	-11.4361
max	-10.6992	-10.7085
stdev	0.2072	0.2292
average	-10.9044	-10.9115
+3S	-10.2828	-10.2239
-3S	-11.5260	-11.5991

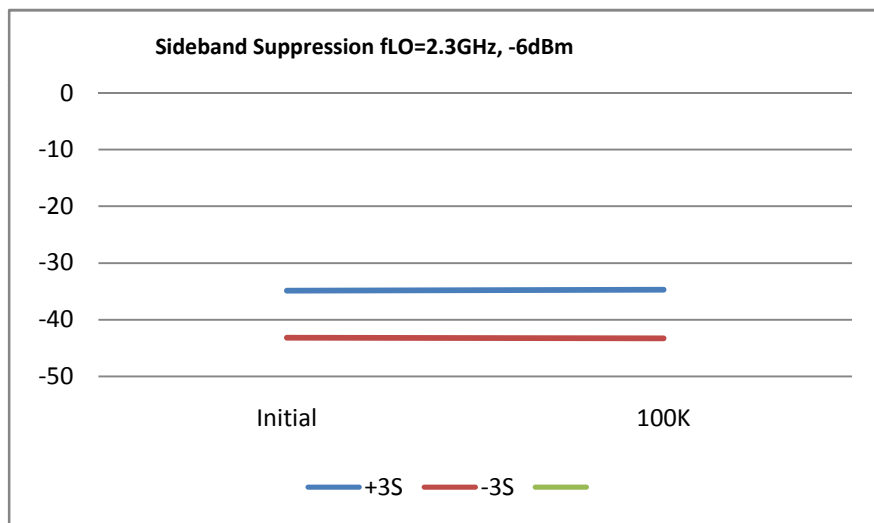


T# 63.0	P_FDTH_2300M_3V_-6dBm	dBm
SN	Initial	100K
40	-39.17505	-39.146
80	-40.2577	-39.918
41	-39.89178	-39.9014
42	-40.40956	-40.34
43	-39.57817	-39.451
44	-39.625	-39.5988
81	-40.16543	-39.9324
82	-38.6882	-38.6328
83	-40.92202	-40.7969
84	-43.94965	-43.8684
min	-43.9497	-43.8684
max	-38.6882	-38.6328
stdev	1.5763	1.5705
average	-40.4037	-40.3152
+3S	-35.6750	-35.6036
-3S	-45.1325	-45.0268

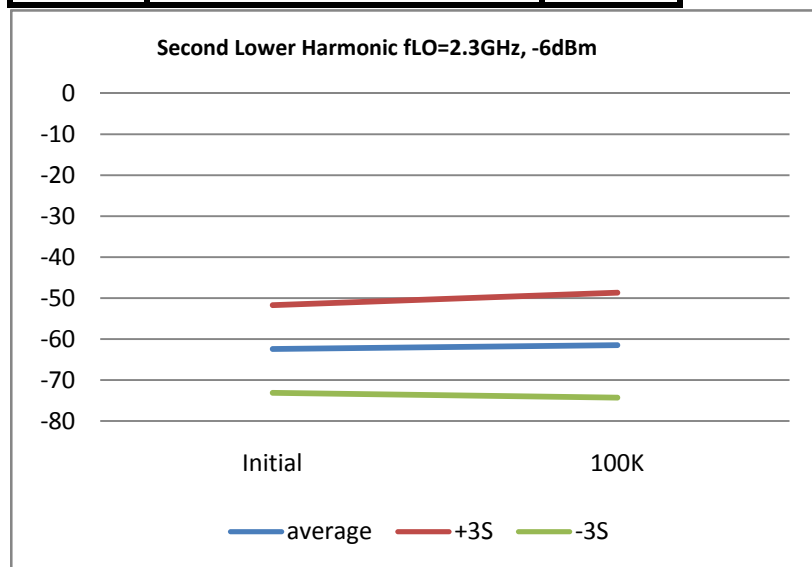




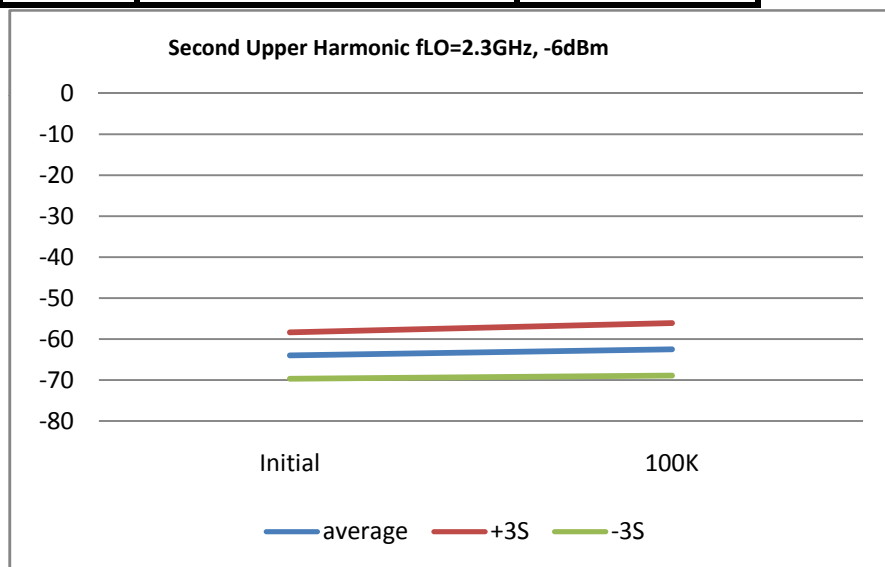
T# 64.0	P_SUPP_2300M_3V_-6dBm	dBc
SN	Initial	100K
40	-39.90879	-39.70132
80	-42.30561	-42.1837
41	-37.92894	-37.82417
42	-39.61024	-39.47343
43	-40.9029	-40.84249
44	-38.17872	-38.06462
81	-38.59989	-38.67716
82	-40.84481	-41.10019
83	-36.98863	-37.02545
84	-39.25668	-38.92077
min	-40.9029	-41.1002
max	-36.9886	-37.0255
stdev	1.3876	1.4291
average	-39.0389	-38.9910
+3S	-34.8761	-34.7038
-3S	-43.2016	-43.2783



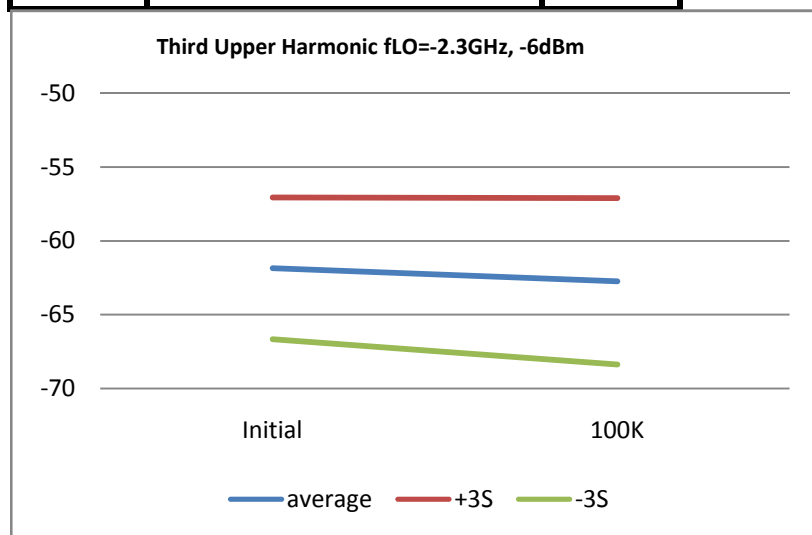
T# 65.0	P_2ndL_2300M_3V_-6dBm	dBc
SN	Initial	100K
40	-60.16895	-58.8813
80	-60.45384	-59.9553
41	-62.66509	-61.2547
42	-62.88406	-59.7241
43	-61.23943	-59.4245
44	-58.86359	-57.9655
81	-62.06987	-61.9765
82	-59.95415	-58.3883
83	-61.13137	-61.6954
84	-70.60699	-71.3572
min	-70.6070	-71.3572
max	-58.8636	-57.9655
stdev	3.5699	4.2615
average	-62.4268	-61.4733
+3S	-51.7172	-48.6888
-3S	-73.1364	-74.2577



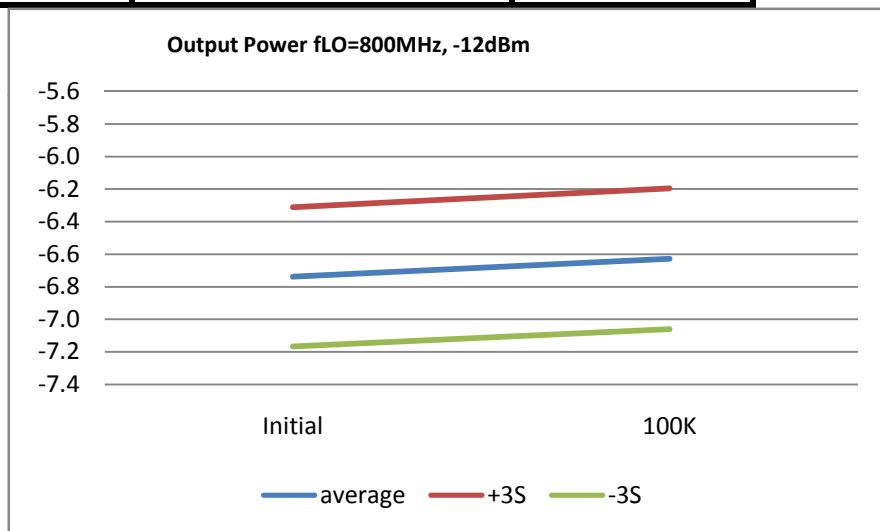
T# 66.0	P_2ndU_2300M_3V_-6dBm	dBc
SN	Initial	100K
40	-63.60011	-61.38568
80	-64.29469	-61.14646
41	-63.71803	-63.03746
42	-61.30589	-59.666
43	-67.22801	-61.88298
44	-62.74091	-59.84719
81	-63.21179	-62.02634
82	-64.02713	-62.65789
83	-63.68974	-65.02207
84	-66.17439	-65.58298
min	-67.2280	-65.5830
max	-61.3059	-59.6660
stdev	1.8818	2.1329
average	-64.0120	-62.4654
+3S	-58.3666	-56.0668
-3S	-69.6574	-68.8640



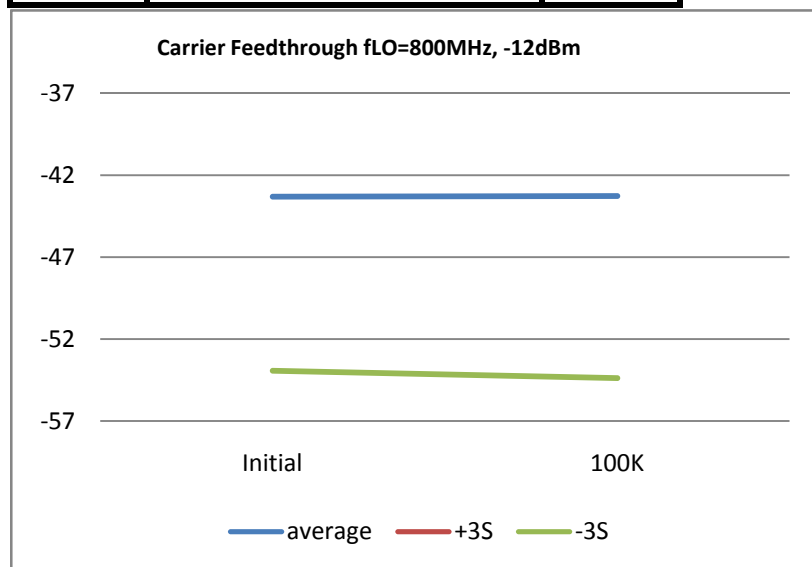
T# 67.0	P_3rdU_2300M_3V_-6dBm	dBc
SN	Initial	100K
40	-59.83021	-58.3404
80	-60.39361	-58.905
41	-61.72379	-60.8302
42	-61.08602	-63.499
43	-65.65543	-65.0651
44	-61.60635	-59.4951
81	-60.84261	-63.8119
82	-61.20588	-64.0817
83	-60.67795	-61.6777
84	-62.06126	-63.4292
min	-65.6554	-65.0651
max	-60.6780	-59.4951
stdev	1.6030	1.8789
average	-61.8574	-62.7362
+3S	-57.0483	-57.0996
-3S	-66.6665	-68.3729



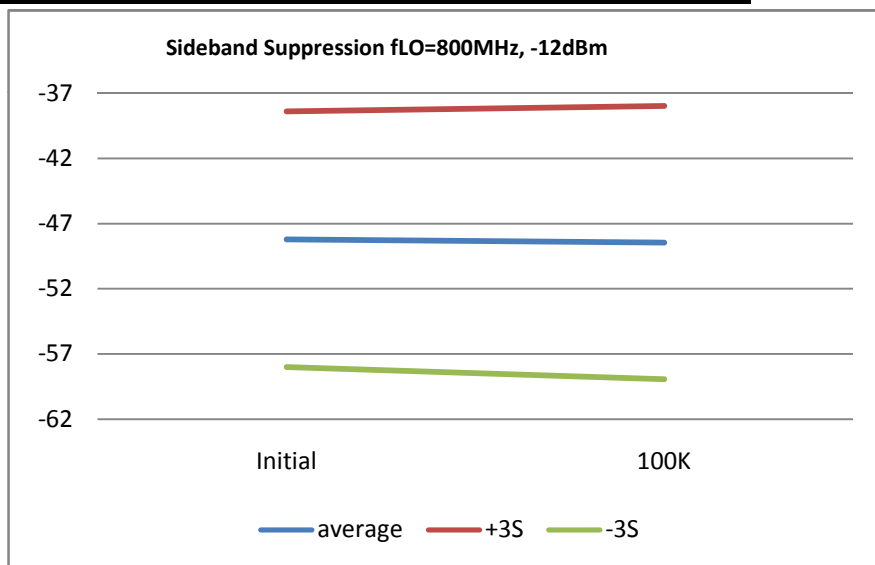
T# 68.0	P_OUT_800M_3V_-12dBm	dBm
SN	Initial	100K
40	-6.61866	-6.51371
80	-6.8181	-6.64748
41	-6.61879	-6.53372
42	-6.60171	-6.50552
43	-6.82902	-6.58934
44	-6.67252	-6.58859
81	-6.65418	-6.54903
82	-6.86335	-6.76147
83	-6.66974	-6.56875
84	-7.00275	-6.93023
min	-7.0028	-6.9302
max	-6.6017	-6.5055
stdev	0.1428	0.1443
average	-6.7390	-6.6283
+3S	-6.3105	-6.1953
-3S	-7.1676	-7.0614



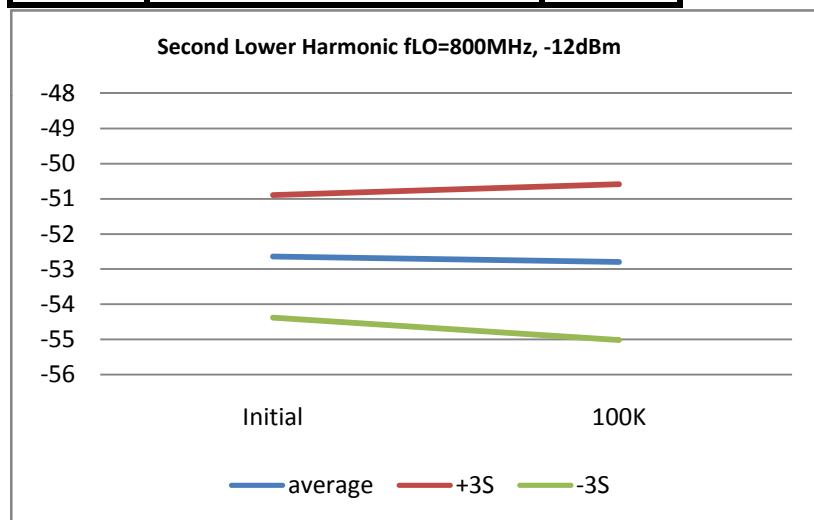
T# 69.0	P_FDTH_800M_3V_-12dBm	dBm
SN	Initial	100K
40	-42.75695	-42.554
80	-41.72119	-41.5081
41	-42.32343	-42.2762
42	-42.83711	-42.8649
43	-42.40593	-42.1384
44	-38.87197	-38.8914
81	-43.19567	-42.9431
82	-41.80019	-41.7057
83	-43.82908	-43.5398
84	-51.27673	-51.7446
min	-51.2767	-51.7446
max	-38.8720	-38.8914
stdev	3.5404	3.7042
average	-43.3175	-43.2630
+3S	-32.6963	-32.1503
-3S	-53.9387	-54.3757



T# 70.0	P_SUPP_800M_3V_-12dBm	dBc
SN	Initial	100K
40	-47.39793	-47.4442
80	-55.3584	-57.16346
41	-46.56138	-46.84948
42	-45.91112	-45.80717
43	-53.54571	-53.58789
44	-45.65911	-45.7364
81	-47.58262	-48.19402
82	-52.01687	-53.14005
83	-44.44438	-44.27439
84	-49.91766	-50.11334
min	-53.5457	-53.5879
max	-44.4444	-44.2744
stdev	3.2724	3.4963
average	-48.2049	-48.4628
+3S	-38.3876	-37.9738
-3S	-58.0221	-58.9519

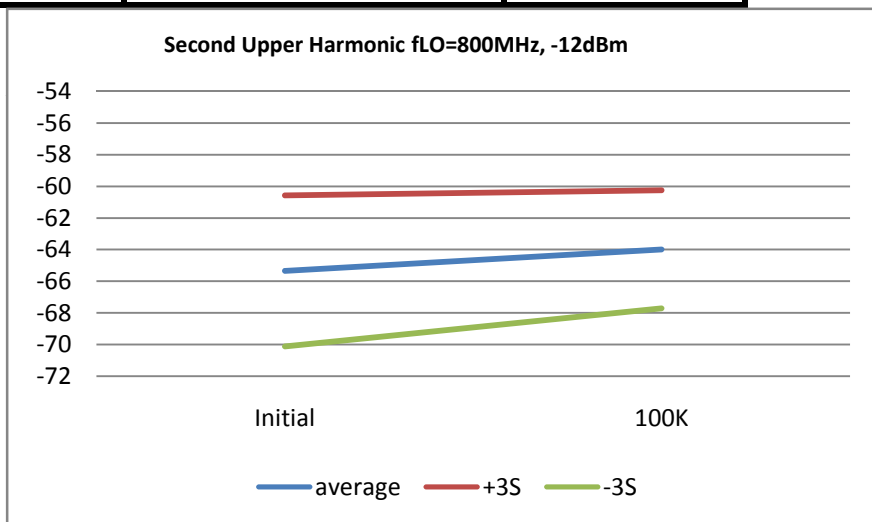


T# 71.0	P_2ndL_800M_3V_-12dBm	dBc
SN	Initial	100K
40	-52.25216	-52.6275
80	-52.60526	-52.5447
41	-53.1049	-54.1835
42	-52.16353	-52.2696
43	-52.72855	-52.3641
44	-51.91058	-51.9237
81	-52.12709	-52.4013
82	-53.47112	-53.2696
83	-52.38235	-52.665
84	-53.23605	-53.3243
min	-53.4711	-54.1835
max	-51.9106	-51.9237
stdev	0.5805	0.7393
average	-52.6405	-52.8001
+3S	-50.8991	-50.5822
-3S	-54.3819	-55.0180

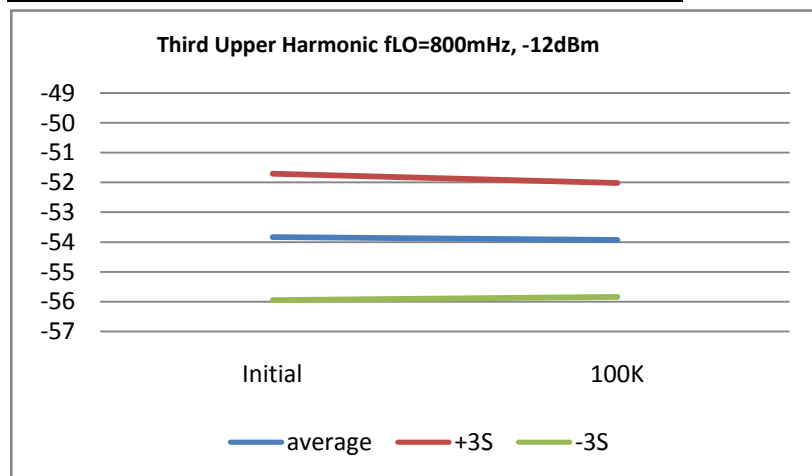




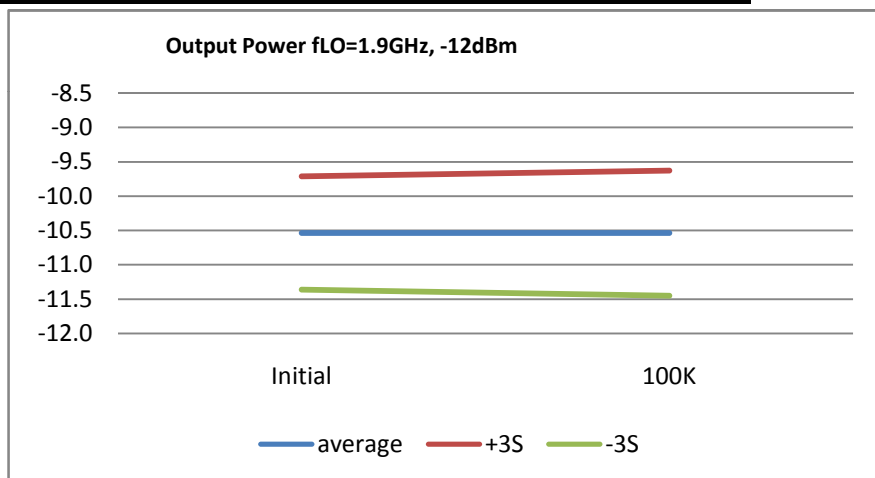
T# 72.0	P_2ndU_800M_3V_-12dBm	dBc
SN	Initial	100K
40	-67.2029	-68.04528
80	-65.75709	-64.78802
41	-67.99579	-65.17645
42	-63.73055	-62.38406
43	-66.86037	-66.19846
44	-63.73207	-63.52738
81	-66.23094	-64.15884
82	-65.3415	-63.46513
83	-65.01539	-64.11252
84	-63.82569	-62.78442
min	-67.9958	-66.1985
max	-63.7306	-62.3841
stdev	1.5916	1.2449
average	-65.3415	-63.9759
+3S	-60.5667	-60.2411
-3S	-70.1164	-67.7107



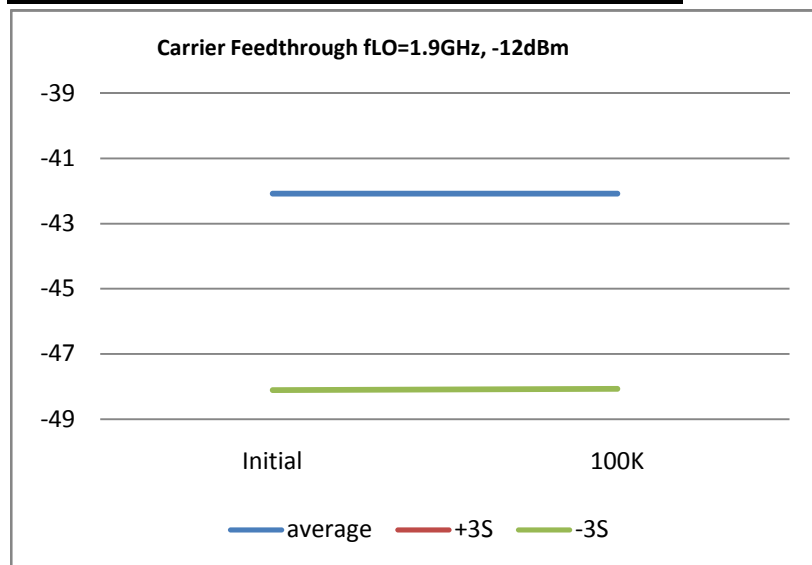
T# 73.0	P_3rdU_800M_3V_-12dBm	dBc
SN	Initial	100K
40	-52.41659	-52.6344
80	-53.73013	-53.9364
41	-53.35068	-53.6405
42	-53.95135	-53.9838
43	-55.00662	-55.1385
44	-53.37566	-53.5558
81	-53.82478	-53.7882
82	-54.0965	-54.2654
83	-54.32148	-54.0524
84	-52.66349	-52.9262
min	-55.0066	-55.1385
max	-52.6635	-52.9262
stdev	0.7080	0.6371
average	-53.8238	-53.9188
+3S	-51.7000	-52.0075
-3S	-55.9477	-55.8301



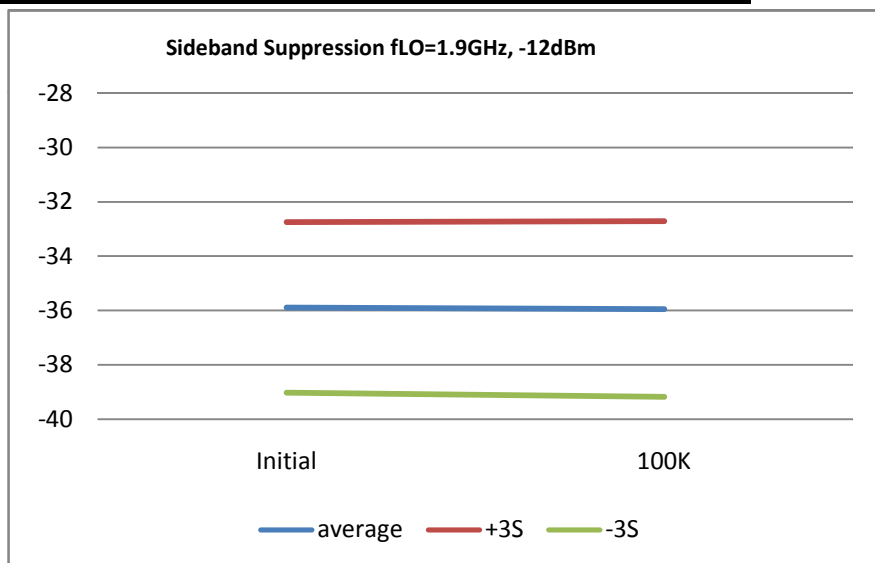
T# 74.0	P_OUT_1900M_3V_-12dBm	dBm
SN	Initial	100K
40	-10.26952	-10.14623
80	-10.43795	-10.2623
41	-10.30018	-10.31468
42	-10.39305	-10.38017
43	-10.59617	-10.5791
44	-10.29495	-10.2845
81	-10.48945	-10.47269
82	-10.5295	-10.51251
83	-10.52469	-10.50224
84	-11.15838	-11.24472
min	-11.1584	-11.2447
max	-10.2950	-10.2845
stdev	0.2745	0.3039
average	-10.5358	-10.5363
+3S	-9.7123	-9.6247
-3S	-11.3593	-11.4479



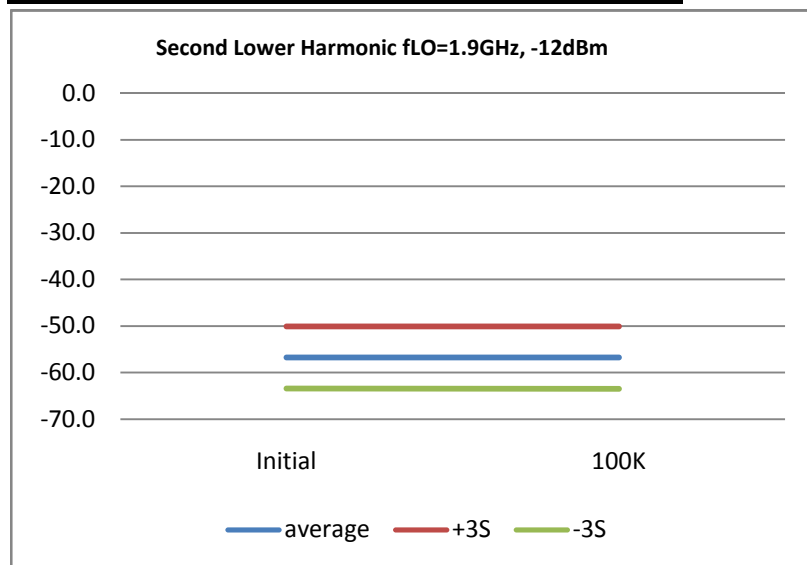
T# 75.0	P_FDTM_1900M_3V_-12dBm	dBm
SN	Initial	100K
40	-40.78804	-40.7539
80	-41.5163	-41.3907
41	-41.57149	-41.6194
42	-42.08625	-42.1099
43	-41.0791	-41.1382
44	-40.49317	-40.595
81	-41.80613	-41.6691
82	-40.26577	-40.2123
83	-42.70657	-42.7046
84	-46.63248	-46.6122
min	-46.6325	-46.6122
max	-40.2658	-40.2123
stdev	2.0094	1.9962
average	-42.0801	-42.0826
+3S	-36.0521	-36.0940
-3S	-48.1082	-48.0712



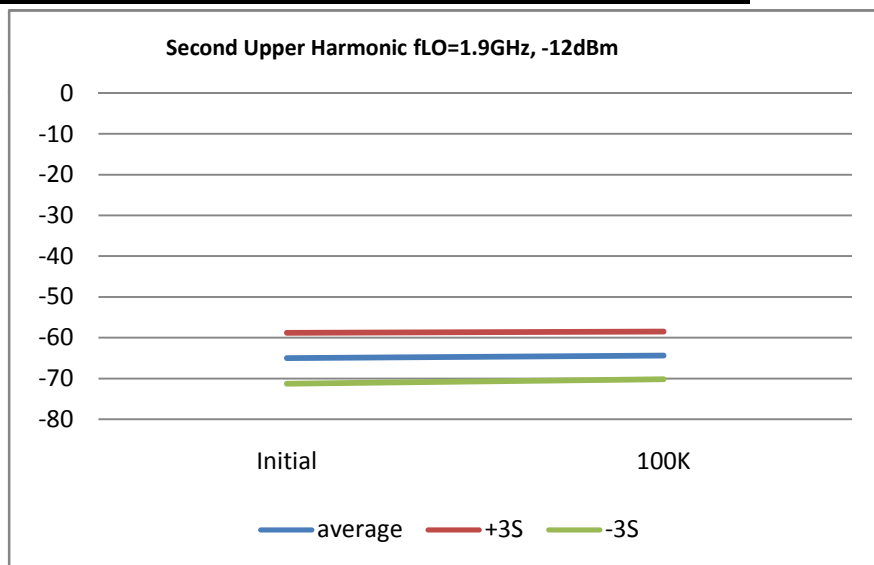
T# 76.0	P_SUPP_1900M_3V_-12dBm	dBc
SN	Initial	100K
40	-36.26459	-36.28284
80	-37.94166	-37.98376
41	-35.27349	-35.29497
42	-35.96647	-36.08369
43	-37.00675	-37.11668
44	-34.87195	-34.88796
81	-35.99521	-36.07607
82	-37.47993	-37.53967
83	-34.34625	-34.34491
84	-36.17799	-36.23533
min	-37.4799	-37.5397
max	-34.3463	-34.3449
stdev	1.0471	1.0781
average	-35.8898	-35.9474
+3S	-32.7485	-32.7130
-3S	-39.0310	-39.1818



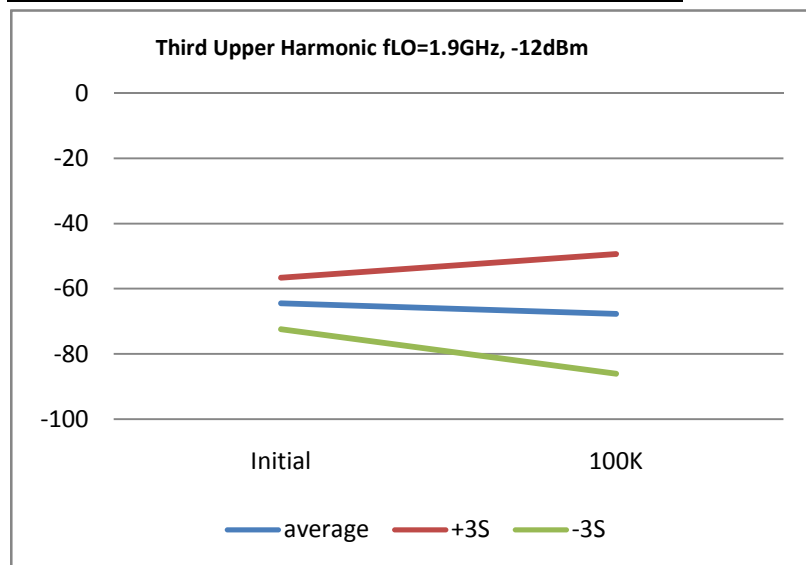
T# 77.0	P_2ndL_1900M_3V_-12dBm	dBc
SN	Initial	100K
40	-58.93688	-58.6585
80	-56.50454	-57.0585
41	-58.65906	-58.9024
42	-56.0334	-56.2862
43	-56.68394	-57.7708
44	-55.15585	-55.2183
81	-58.15165	-57.7933
82	-60.63802	-60.1339
83	-54.89537	-54.6052
84	-54.04522	-53.6953
min	-60.6380	-60.1339
max	-54.0452	-53.6953
stdev	2.2211	2.2260
average	-56.7828	-56.8007
+3S	-50.1196	-50.1226
-3S	-63.4460	-63.4787



T# 78.0	P_2ndU_1900M_3V_-12dBm	dBc
SN	Initial	100K
40	-64.45184	-63.78458
80	-63.32018	-63.65179
41	-63.10784	-63.73594
42	-63.64068	-63.53011
43	-63.75595	-62.88525
44	-62.43002	-62.30192
81	-66.93452	-65.87424
82	-67.12256	-63.58263
83	-65.64647	-64.79986
84	-67.8435	-68.34222
min	-67.8435	-68.3422
max	-62.4300	-62.3019
stdev	2.0806	1.9433
average	-65.0602	-64.3815
+3S	-58.8184	-58.5517
-3S	-71.3020	-70.2113

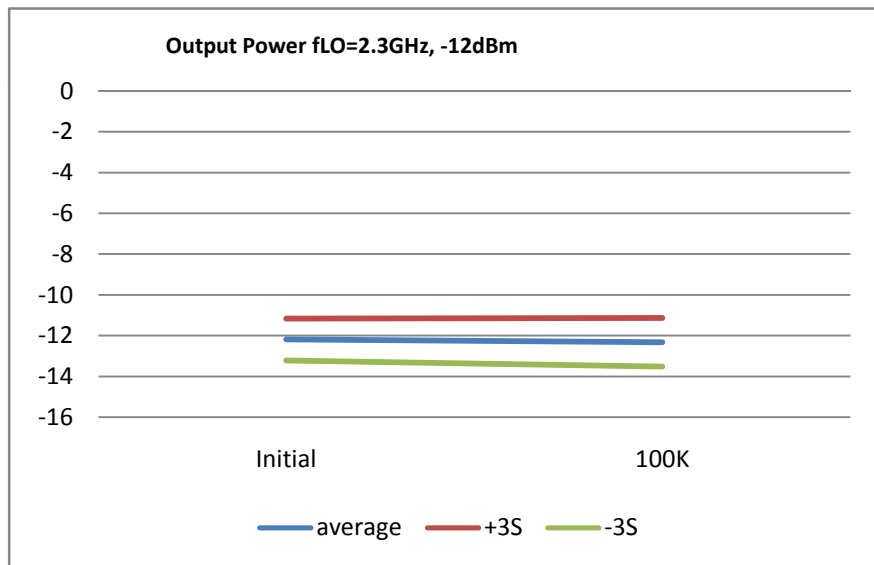


T# 79.0	P_3rdU_1900M_3V_-12dBm	dBc
SN	Initial	100K
40	-60.83123	-59.9153
80	-63.56822	-62.3403
41	-62.20819	-64.3633
42	-63.5072	-65.0659
43	-65.62373	-69.276
44	-62.62246	-65.37
81	-61.95972	-62.3209
82	-67.50093	-70.7678
83	-63.47702	-63.5005
84	-69.11884	-81.0943
min	-69.1188	-81.0943
max	-61.9597	-62.3209
stdev	2.6421	6.1106
average	-64.5023	-67.7198
+3S	-56.5760	-49.3882
-3S	-72.4285	-86.0515

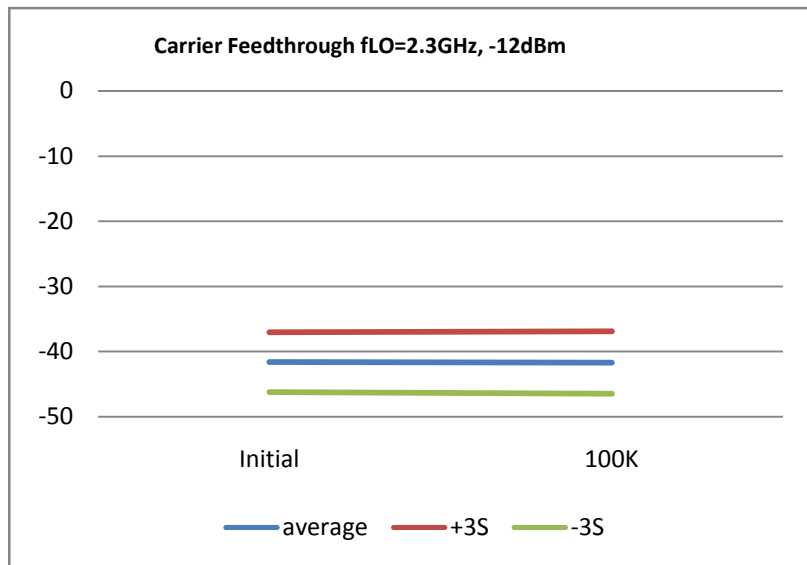




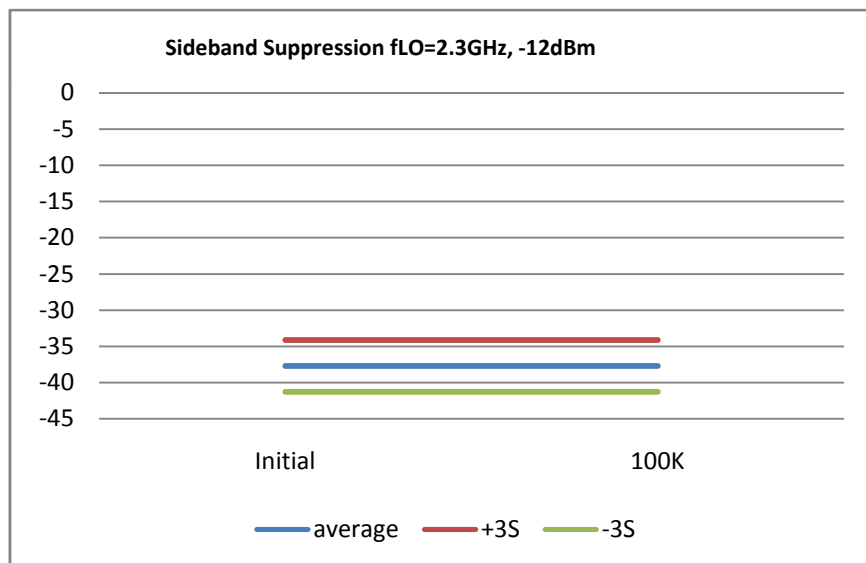
T# 80.0	P_OUT_2300M_3V_-12dBm	dBm
SN	Initial	100K
40	-11.79527	-11.71887
80	-11.94491	-11.85878
41	-11.86485	-12.02295
42	-12.02632	-12.15294
43	-12.40537	-12.46798
44	-11.84776	-11.95684
81	-12.12755	-12.24329
82	-12.10644	-12.23014
83	-12.24178	-12.306
84	-12.91015	-13.22867
min	-12.9102	-13.2287
max	-11.8478	-11.9568
stdev	0.3440	0.3981
average	-12.1913	-12.3261
+3S	-11.1594	-11.1318
-3S	-13.2232	-13.5204



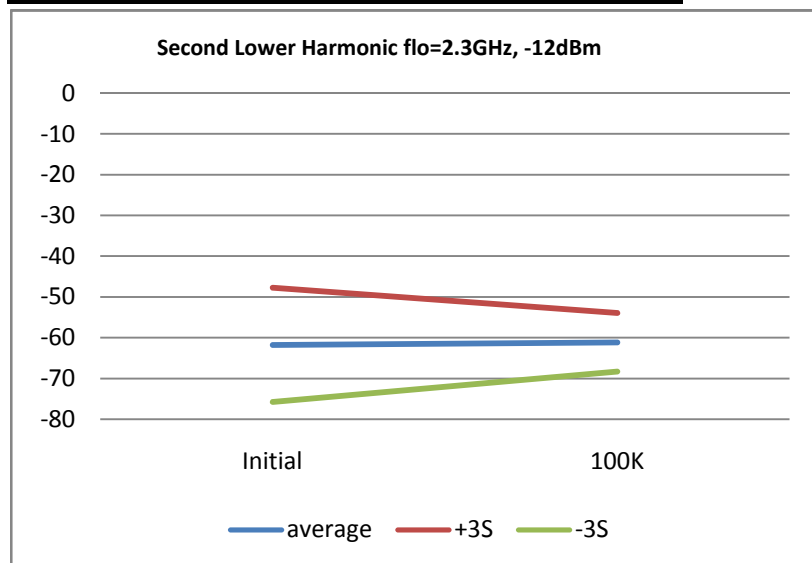
T# 81.0	P_FDTM_2300M_3V_-12dBm	dBm
SN	Initial	100K
40	-40.36292	-40.3265
80	-41.13395	-40.9831
41	-41.24232	-41.2864
42	-41.65902	-41.82
43	-40.92109	-40.9253
44	-40.55983	-40.6847
81	-41.34214	-41.2642
82	-39.93494	-39.9758
83	-42.19521	-42.2358
84	-45.00627	-45.2318
min	-45.0063	-45.2318
max	-39.9349	-39.9758
stdev	1.5339	1.5918
average	-41.6076	-41.6780
+3S	-37.0059	-36.9027
-3S	-46.2093	-46.4533



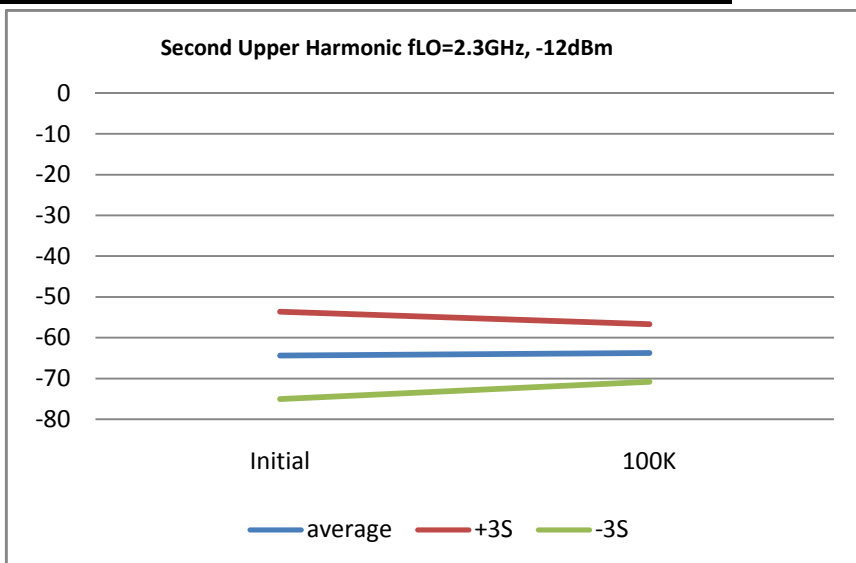
T# 82.0	P_SUPP_2300M_3V_-12dBm	dBc
SN	Initial	100K
40	-38.09265	-37.87876
80	-39.39093	-39.23651
41	-37.15125	-37.05151
42	-38.52149	-38.60738
43	-39.65721	-39.62581
44	-36.65149	-36.6017
81	-36.70293	-36.74405
82	-38.72576	-38.71261
83	-36.33071	-36.33978
84	-37.8483	-37.84042
min	-39.6572	-39.6258
max	-36.3307	-36.3398
stdev	1.1862	1.1933
average	-37.6986	-37.6904
+3S	-34.1401	-34.1106
-3S	-41.2572	-41.2702



T# 83.0	P_2ndL_2300M_3V_-12dBm	dBc
SN	Initial	100K
40	-63.15165	-60.1158
80	-61.01	-58.9313
41	-62.60705	-60.3264
42	-59.55487	-61.8109
43	-60.83112	-59.3198
44	-59.26534	-60.2531
81	-59.83614	-61.3747
82	-58.36723	-59.2854
83	-60.89861	-60.2354
84	-72.93507	-66.6936
min	-72.9351	-66.6936
max	-58.3672	-59.2854
stdev	4.6827	2.4013
average	-61.7869	-61.1624
+3S	-47.7387	-53.9586
-3S	-75.8352	-68.3662



T# 84.0	P_2ndU_2300M_3V_-12dBm	dBc
SN	Initial	100K
40	-62.27439	-63.14883
80	-67.82944	-61.97533
41	-65.86201	-61.74384
42	-64.57748	-64.3511
43	-61.67484	-62.14262
44	-62.98919	-61.74542
81	-62.40091	-67.86854
82	-60.56237	-61.77183
83	-64.79788	-64.28674
84	-72.0957	-66.36976
min	-72.0957	-67.8685
max	-60.5624	-61.7438
stdev	3.5794	2.3605
average	-64.3700	-63.7850
+3S	-53.6319	-56.7034
-3S	-75.1082	-70.8666



T# 85.0	P_3rdU_2300M_3V_-12dBm	dBc
SN	Initial	100K
40	-62.33715	-62.8769
80	-64.12678	-60.6918
41	-64.48578	-62.6849
42	-61.54842	-60.2544
43	-62.43362	-59.744
44	-61.98879	-61.0875
81	-61.53816	-63.5114
82	-62.89298	-60.7584
83	-61.0808	-64.4608
84	-59.29362	-59.6744
min	-64.4858	-64.4608
max	-59.2936	-59.6744
stdev	1.4968	1.8087
average	-61.9078	-61.5220
+3S	-57.4173	-56.0958
-3S	-66.3982	-66.9481

