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|-----------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Product</b>              | <b>ADV202</b>                                                                                                                                                                                                                             |
| Title                       | Changes between ADV202 Rev0.0 and Rev0.1                                                                                                                                                                                                  |
| Product Identification      | Rev 0.0 parts are branded with a 0.0 on line 4 :<br><br>ADV202XCA<br>Surf®<br>Gxxxxx.x<br>0306 <b>0.0</b><br>HongKong<br><br>Rev 0.1 parts are branded with a 0.1 on line 4:<br><br>ADV202XBC<br>Gxxxxx.x<br>0326 <b>0.1</b><br>Singapore |
| Compatibility               | Rev0.0 parts have been issued in the 121-pin package only.<br>Any 121-pin Rev0.1 part is a drop-in compatible replacement for the 121-pin Rev0.0 part.                                                                                    |
| Recommended Customer Action | Any Rev0.0 121-pin part should be replaced with the rev0.1 121-pin part.<br>This PCN should be used in conjunction with the datasheet.                                                                                                    |
| Date issued                 | July 2003                                                                                                                                                                                                                                 |

## 1. Pin functionality

Rev 0.0:

Pin H8, CFG<0> functionality is available on 121-pin parts.

Rev 0.1:

Pin H8, CFG<0> functionality is not available on 121-pin parts.

## 2. Boot Mode Register

Rev 0.0 :

BMODE <2:0> is programmed to a value of 7 for Normal Boot mode.

Rev 0.1:

BMODE <2:0> is programmed to a value of 4 for Normal Boot mode.

## 3. Dedicated Chip Select mode/ FIFO Threshold registers

Rev 0.0 :

In Dedicated Chip Select mode, the FSRQx signal is asserted whenever the FIFO is full/empty.  
FSRQx is asserted after the rising edge of WE/.

Rev 0.1 :

In Dedicated Chip Select mode, the FSRQx signal is asserted according to the settings in the FIFO  
Threshold registers.  
FSRQx is asserted after the falling edge of WE/.