

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. ADI contact information is listed below.

PIN Title:	Product Information Notice for MAX96717F. Included Additional Errata Items and Updated Appendix A.
Publication Date:	4-May-2026
Effectivity Date:	4-May-2026
Revision Description:	Initial Release.

Description Of Change:

Pages 4-5: Added item 10, “In ADC Round Robin mode, inaccurate conversions with `adc_rr_sleep=0x0000` or not enabling channel 7 via `ch7_hi_limit_ie/ch7_lo_limit_ie` (Errata ID=830008)”. No silicon fix is planned.

Page 5: Added item 11, “Reading the `adc_calDone_if` register bit before ADC conversion is finished can keep it from being asserted (Errata ID=830009)”. No silicon fix is planned.

Pages 6-7: Corrected Appendix A - ADC GPIO Input Verification Test procedure.

Reason For Change:

This PIN is being published to provide additional information on product application and performance.

Impact of the change (positive or negative) on fit, form, function & reliability:

There is no impact to fit, form, function, quality, or reliability.

Summary of Supporting Information:

Errata sheet.

Comments:

THIS IS NOT A PCN. This is an advisory to provide additional information on product application and performance.

Supporting Documents:

Attachment 1:
[MAX96717F Errata Sheet.pdf](#)

ADI Contact Information:

For questions on this PIN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Korea:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_Korea@analog.com	PCN_ROA@analog.com

Appendix A - Impacted items, see csv PN listing in PCN Zip file

Appendix B – Revision History:

Rev	Publish	Effectivity Date	Rev Description
Rev. -	4-May-2026	4-May-2026	Initial Release.