Product Information Notice - PIN 2568 -

Analog Devices, Inc. One Analog Way, Wilmington, MA01887, USA

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. ADI contact information is listed below.

PIN Title:	Product Information Notice for MAX96793. Included Additional Errata Items. Updated Appendix A.
Publication Date:	17-Apr-2026
Effectivity Date:	*17-Apr-2026*
Revision Description:	Initial Release.

Description Of Change:

Page 5: Added item 12, "In ADC Round Robin mode, inaccurate conversions with adc_rr_sleep=0x0000 or not enabling channel 7 via ch7_hi_limit_ie/ch7_lo_limit_ie (Errata ID=830008)". No silicon fix is planned.

Pages 5-6: Added item 13, "Reading the adc_calDone_if register bit before ADC conversion is finished can keep it from being asserted (Errata ID=830009)". No silicon fix is planned.

Pages 7-8: Corrected Appendix A - ADC GPIO Input Verification Test procedure.

Reason For Change:

This PIN is being published to provide additional information on product application and performance.

Impact of the change (positive or negative) on fit, form, function & reliability:

There is no impact to fit, form, function, quality, or reliability.

Summary of Supporting Information:

Errata sheet.

Comments:

THIS IS NOT A PCN. This is an advisory to provide additional information on product application and performance.

Supporting Documents:

Attachment 1:
MAX96793 Errata Sheet.pdf

ADI Contact Information:

For questions on this PIN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Korea:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_Korea@analog.com	PCN_ROA@analog.com

Appendix A - Impacted items, see csv PN listing in PCN Zip file

Appendix B – Revision History:

Rev	Publish	Effectivity Date	Rev Description
Rev. -	17-Apr-2026	17-Apr-2026	Initial Release.