

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title:	Qualification of Alternate Fab Site for MAX16956/MAX77596/MAX77597
Publication Date:	09-Oct-2023
Effectivity Date:	11-Jan-2024 (the earliest date that a customer could expect to receive changed material)
Revision Description:	-

Description Of Change:

Analog Devices is adding Powerchip Fab/Taiwan as an alternate wafer fab site to Analog Devices Beaverton/USA and Tower Semiconductor/San Antonio/USA

Note:

Powerchip Fab/Taiwan is 12-inch

Analog Devices Beaverton/USA and Tower Semiconductor/San Antonio/USA are 8-inch

Reason For Change:

This change will ensure manufacturing agility and continuity of supply.

Impact of the change (positive or negative) on fit, form, function & reliability:

There is no impact to fit, form, function, or reliability.

Product Identification (this section will describe how to identify the changed material):

Analog Devices maintains full traceability by device marking, packaging labels and shipment documents.

Summary of Supporting Information:

Qualification has been performed per AEC-Q100, Stress Test Qualification for Integrated Circuits.
See attached Qualification Results Summary.

Supporting Documents:

Attachment 1: Type: Qualification Report Summary

[Fab101_S18U.pdf](#)

Attachment 2: Type: Delta Qualification Matrix

[PCN 2356-Delta-Qualification-Matrix-ZVEI-5_0_16.xlsm](#)

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_ROA@analog.com

Appendix A - Impacted items, see csv PN listing in PCN Zip file