

PCN # 1801D1

DATE: April 15, 2019

EXPECTED PCN SHIP DATE: April 15, 2019



Quality Assurance
160 Rio Robles
San Jose, CA 95134

www.maximintegrated.com

PROCESS CHANGE NOTICE

PRODUCT CHANGE NOTICE

MAXIM INTEGRATED HEREBY ISSUES NOTIFICATION OF CHANGE
THAT MAY AFFECT THE FOLLOWING CATEGORIES:

<input checked="" type="checkbox"/> DESIGN	<input type="checkbox"/> WAFER FAB	<input type="checkbox"/> ASSEMBLY	<input type="checkbox"/> TEST	<input type="checkbox"/> ELEC/MECH SPECS
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AFFECTED PRODUCT:

Ordering P/N: (See PN listing XLS in PCN ZIP file)

CHANGE FROM: - Die design change for MAX9259/MAX9301 (GMSL1 devices) DieID/Die Revision (HS40Y/VBZD)	CHANGE TO: - DieID/Die revision HS40Y/CUI0 New die has an added High Pass Filter/HPF (resistor and capacitor) in the serial clock signal path between SSPLL buffer output and P2S (parallel-to-serial) buffer input
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JUSTIFICATION: -

To improve the production test yield performance of these devices, a simple metal mask change is required by adding a High Pass Filter/HPF (described above). This change is required to ensure supply chain continuity by eliminating the inconsistent manufacturing yield for max frequency performance. The datasheet electrical table specifications and overall content remain unchanged.

The form, fit and function of these devices are identical before/after the change.

TRACEABILITY: Maxim Integrated maintains full traceability by device marking, packaging labels and shipment documents.

Maxim Integrated's Change Notification System is designed to keep our customer base apprised of major product, manufacturing, or facility improvements.

Nasser Ali Chaouche

Nasser AliChaouche / PCN Coordinator

For further information, please contact either of the people listed below.

Contact your local Maxim Integrated Company Representative

or

Nasser AliChaouche, PCN Coordinator

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