

### Interface Timing corrections

Parameter	Current Data Sheet Specification	Corrected Data Sheet Specification	Unit	Description
t <sub>7A</sub>	50	140	ns min	Minimum SYNC high time in Readback mode
t <sub>10</sub>	30	36	ns max	24 <sup>th</sup> SCLK falling edge to BUSY falling edge
t <sub>14</sub>	2000 max	100/2000	ns min/max	BUSY rising edge to DAC output response time
t <sub>17</sub>	8	3	μs typ	DAC output settling time; boost mode off
t <sub>20</sub>	20	30	ns max	SCLK rising edge to SDO valid

