

**Qualification Results Summary for  
LFCSP Package with Non-conductive Die Attach Epoxy  
at STATS ChipPAC China (SCC)**

QUALIFICATION RESULTS			
Test	Conditions	Sample Size	Result
Highly Accelerated Stress Test (HAST)*	JEDEC <i>JESD22-A110</i>	3 x 77	<b>PASS</b>
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3 x 77	<b>PASS</b>
Autoclave (AC)*	JEDEC <i>JESD22-A102</i>	3 x 77	<b>PASS</b>
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3 x 11	<b>PASS</b>
High Temperature Storage Life (HTSL)	JEDEC <i>JESD22-A103</i>	1 x 77	<b>PASS</b>
Field Induced Charged Device Model (FICDM)	JEDEC <i>JESD22-C101</i>	3/Voltage	<b>PASS ±1250V</b>

\*Preconditioned per JEDEC/IPC J-STD-020